### About Yenepoya Group of Colleges

Yenepoya Group of Colleges is established in the port city of Mangaluru in southern Karnataka. Located amidst the pristine ambiance and haleyon surroundings of this coastal region, the colleges have emerged as a world class facility for higher education. They imbibe the true spirit of global learning and provide the best quality education to its students.

#### About the Institute

The Institute was established in the year 2008 with the aim of equipping the young talents of craving youth with professional education and to confer sufficient knowledge upon them to meet the demand and challenges of the volatile global environment. The campus is situated in 20 acres of sprawling campus with lush greenery. Well qualified and dedicated team of faculty members strives to bring out the best performance from the students. The Institute has well-furnished and ventilated classrooms, well equipped laboratories, digital library, Wi-Fi, separate gents and ladies hostel, ATM, etc.

The Institution has won the best ISTE chapter award during 2010-11. As of now, the institution offers five undergraduate full time programmes in the engineering disciplines namely CSE, EEE, ECE, ISE, and ME with a total annual intake of 360 students.

### About the Department

The Department of Electronics and Communication Engineering was established in the year 2008. The department has qualified dynamic faculty members with well-equipped laboratories and being upgraded with time and

technology. The department has an association called ECHOLECTIC to nurture and enhance young minds and inspire them to conduct and participate in curricular and extracurricular activities

#### About Techlabs

Trident Techlabs Pvt. Ltd., head quartered at New Delhi and are a specialist large sized company providing independent Software and Hardware solution to entire Academics for Engineering and Technology all over India.

Trident Techlabs PVT. LTD., (ISO 9001:2008) are the academic channel partners of National Instruments, Mentor Graphics, Tanner for VLSI Labs, PTC, SprutCAM, Simulation X, Texas instruments, PSIM, Power World, Cyme.

#### Objectives of workshop

- ✓ Help to understand Latest Trends in VLSI.
- ✓ Understanding of complete FPGA & ASIC Design Flow.
- √ Key concepts coverage in each step of the Design Flows.

#### Course Contents

Day 1: Overview of & Current Trends in VLSI Design, Discussion on Mentor Graphics Tools for FPGA Design Flow, Demonstration on FPGA Design Flow using Mentor Graphics Tools and Exercises including Design and Implementation.

Day2: Discussion on Mentor Graphics ASIC Design Tools, Demonstration on Analog and Mixed Signal Design using Mentor Graphics Tools, Hands on Analog and Mixed Signal Design using Mentor Graphics Tools, and Multisim Overview & Demo

#### Registration

Registration fee: Rs.250/-(Refundable for selected delegates who attend entire course)

Registration Fee is to be paid through a DD drawn in favor of "Principal, Dr.MVSIT", Payable at Moodbidri, Karnataka, India. The number of participants is restricted to 25 and will be registered on "first come first

#### How to apply

Scanned copies of filled registration form and DD should reach the coordinator on or before 11/08/15 by mail to:

# yitworkshopece@gmail.com

#### Intimation/Confirmation by mail:

12<sup>th</sup> Aug 2015

#### Contact & Resource persons

Mr. V Arul Selvan, Regional Manager Mob: 09840609696 Mail-id: v.arul@tridenttechlabs.com

Mr. S Anand, Sr. Field Application Engineer Mob: 08939942012 Mail-id: s.anand@tridenttechlabs.com

Mr. Shuddhodhan Shetty, Sales Engineer, Mob: 09535009497 Mail-id: s.shetty@tridenttechlabs.com

### Registration Form

Dr. M V Shetty Institute of Technology (under Yenepoya Group of Colleges) Dept. of Electronics & Communication Engineering

Two day workshop on "FPGA and ASIC design using Mentor Graphics Tools"

- 1. Name
- 2. Designation:
- 3. Department:
- 4. Institution:
- 5. Gender:
- 6. Qualification:
- 7. Mobile No:
- 8. Email:
- 9. Accommodation required:

Payment Details:

DD Number:

Bank: Date:

(Multiple copies can be used)

Principal/HOD sign (with seal)

Applicant Sign

#### Organizing Committee Chief Patron

Mr. Yenepoya Abdulla Kunhı Chancellor, Yenepoya University

# Patrons

Prof. P.C.M. Kunhi Chairman, I A F.

Mr. Yenopoya Abdulla Javeed Director Operations, Yenepoya Group

Mr. Farhaad Yenepoya Director Finance, Venepoya Group

# Advisors

Dr. Vivekanand V. Vernekar Administrator, Dr.MVSIT

Dr. Sandeep J. Nayak Principal, Dr.MVSIT

### Convener

Prof. Gangadhar G Head, ECE Dept., Dr.MVSIT

### Coordinators

Mr. Dharatesh B Asst. Prof., ECE Dept., Dr.MVSIT Mob: 9964116587

Mr. Dinesha Shetty Asst Prof, FCF Dept, Dr MVSIT Mob: 9964381449 Dr. M V Shetty Institute of Technology Thodar, Mijar post, Moodbidri – 574225



(Under Yenepoya Group of Colleges)



In association with



Two day workshop on "FPGA and ASIC design using Mentor Graphics Tools"



Date
13<sup>th</sup> and 14<sup>th</sup> Aug 2015
Organized by
. of Electronics and Communic

Dept. of Electronics and Communication Engineering Dr. M V S I T, Moodbidri.

# Alva's Institute of Engineering & Technology



Shobhavana Campus, Mijar, Moodbidri, D.K - 574225

Phone: 08258-262725, Fax: 08258-262726

# DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

# **FDP Permission letter**

From,

Prof. Braveen. J.

Asso. Porof.

Dept. Of ECE

AZET.

To,

IQAC Chairman,

A.I.E.T. Mijar

Respected Sir,

Sub: Request to give permission to attend the FDP.

As I want to strengthen my professional development and increase the effectiveness of my teaching, I like to attend Faculty development program an two days Workshop on FPGA & ASIC design using menton Group Lica! from 13th Aug gokto 14th Aug 1016 ( 02 days) at Don M. V. Ebethy Inakhk of Technology 

Place: Mood bidou

Date: 11/08/2015.
Permitted

a Institute of Engg. & Technology Milar MOODBIDRI 674 226

Name and designation

Alva's Institute of Engly 574 225, D.M.



# ALVA'S EDUCATION FOUNDATION®

MOODBIDRI - 574 227, D. K.

No.

DEBIT/CREDIT VOUCHER

12-8-2015

Sl. No	Particular	College	Dr	Cr
	TA and DA for			
	TA and DA for attending Workshop		250 -	1
-	0 4			-
Anna de Day (Digital De		10150 (050 Nin 2000)		
			950  -	

Rupees:

Two hundred fifty only.

Narration:

Amount faid towards Prof. Praveen J for 2 days

weekshop on FPGA

Dept. of Electropidities By Tachnology

Dept. of Electropidities By Tachnology

Dept. of Electropidities of English & Text Chair For English & Tex

Receiver's/Remitter's

Signature