

About Yenepoya Group of Colleges

Yenepoya Group of Colleges is established in the port city of Mangaluru in southern Karnataka. Located amidst the pristine ambience and halcyon surroundings of this coastal region, the colleges have emerged as a world class facility for higher education. They imbibe the true spirit of global learning and provide the best quality education to its students.

About the Institute

The Institute was established in the year 2008 with the aim of equipping the young talents of craving youth with professional education and to confer sufficient knowledge upon them to meet the demand and challenges of the volatile global environment. The campus is situated in 20 acres of sprawling campus with lush greenery. Well qualified and dedicated team of faculty members strives to bring out the best performance from the students. The Institute has well-furnished and ventilated classrooms, well equipped laboratories, digital library, Wi-Fi, separate gents and ladies hostel, ATM, etc.

The Institution has won the best ISTE chapter award during 2010-11. As of now, the institution offers five undergraduate full time programmes in the engineering disciplines namely CSE, EEE, ECE, ISE, and ME with a total annual intake of 360 students.

About the Department

The Department of Electronics and Communication Engineering was established in the year 2008. The department has qualified dynamic faculty members with well-equipped laboratories and being upgraded with time and

technology. The department has an association called **ECHOLECTIC** to nurture and enhance young minds and inspire them to conduct and participate in curricular and extracurricular activities.

About Techlabs

Trident Techlabs Pvt. Ltd., head quartered at New Delhi and are a specialist large sized company providing independent Software and Hardware solution to entire Academics for Engineering and Technology all over India.

Trident Techlabs PVT. LTD., (ISO 9001:2008) are the academic channel partners of National Instruments, Mentor Graphics, Tanner for VLSI Labs, PTC, SprutCAM, Simulation X, Texas instruments, PSIM, Power World, Cyme.

Objectives of workshop

- ✓ Help to understand Latest Trends in VLSI.
- ✓ Understanding of complete FPGA & ASIC Design Flow.
- ✓ Key concepts coverage in each step of the Design Flows.

Course Contents

Day 1: Overview of & Current Trends in VLSI Design, Discussion on Mentor Graphics Tools for FPGA Design Flow, Demonstration on FPGA Design Flow using Mentor Graphics Tools and Exercises including Design and Implementation.

Day2: Discussion on Mentor Graphics ASIC Design Tools, Demonstration on Analog and Mixed Signal Design using Mentor Graphics Tools, Hands on Analog and Mixed Signal

Design using Mentor Graphics Tools, and Multisim Overview & Demo.

Registration

Registration fee: Rs.250/- (Refundable for selected delegates who attend entire course)

Registration Fee is to be paid through a DD drawn in favor of "Principal, Dr.MVSIT", Payable at Moodbidri, Karnataka, India.

The number of participants is restricted to 25 and will be registered on "first come first served basis".

How to apply

Scanned copies of filled registration form and DD should reach the coordinator on or before 11/08/15 by mail to:

yitworkshopece@gmail.com

Intimation/Confirmation by mail:

12th Aug 2015

Contact & Resource persons

Mr. V Arul Selvan, Regional Manager
Mob: 09840609696
Mail-id: v.arul@tridenttechlabs.com

Mr. S Anand, Sr. Field Application Engineer
Mob: 08939942012
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Mr. Shuddhodhan Shetty, Sales Engineer,
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Mail-id: s.shetty@tridenttechlabs.com

Registration Form

Dr. M V Shetty Institute of Technology
(under Yenepoya Group of Colleges)
Dept. of Electronics & Communication
Engineering

Two day workshop on
"FPGA and ASIC design using
Mentor Graphics Tools"

1. Name:
 2. Designation:
 3. Department:
 4. Institution:
 5. Gender:
 6. Qualification:
 7. Mobile No:
 8. Email:
 9. Accommodation required:
- Payment Details:
DD Number:
Bank:
Date:
(Multiple copies can be used)

Principal/HOD sign
(with seal)

Applicant Sign

Organizing Committee

Chief Patron
Mr. Yenepoya Abdulla Kunhi
Chancellor, Yenepoya University

Patrons
Prof. P.C.M. Kunhi
Chairman, I A E

Mr. Yenepoya Abdulla Javeed
Director Operations, Yenepoya Group

Mr. Farhaad Yenepoya
Director Finance, Yenepoya Group

Advisors
Dr. Vivekanand V. Vernekar
Administrator, Dr.MVSIT

Dr. Sandeep J. Nayak
Principal, Dr.MVSIT

Convener
Prof. Gangadhar G
Head, ECE Dept., Dr.MVSIT

Coordinators
Mr. Bharatesh B
Asst. Prof., ECE Dept., Dr.MVSIT
Mob: 9964116587

Mr. Dinesha Shetty
Asst. Prof., ECE Dept., Dr.MVSIT
Mob: 9964381449

Dr. M V Shetty Institute of Technology
Thodar, Mijar post, Moodbidri – 574225



(Under Yenepoya Group of Colleges)



In association with



Two day workshop on
"FPGA and ASIC design using
Mentor Graphics Tools"



Date
13th and 14th Aug 2015

Organized by
Dept. of Electronics and Communication
Engineering
Dr. M V S I T, Moodbidri.



Dr. M.V. SHETTY INSTITUTE OF TECHNOLOGY

YENI POYA
GROUP

(Approved by AICTE, New Delhi, Recognized by Govt. of Karnataka and Affiliated to VTU, Belagavi.)

Vidyanagar, N.H. 13, Thodar, Moodbidri, Mangaluru - 574225, Karnataka.

Phone : 08258-262733 Fax: 08258-262723 E-mail : drmvst@gmail.com Website : www.mvsit.ac.in

(Under Yenepoya Group of Colleges)

Two day workshop on

“FPGA and ASIC design using Mentor Graphics Tools”

13th & 14th August 2015

Certificate

This is to certify that Mr./Ms./Mrs. Mahendra H. N
of Alva's Institute of Engg. & Tech., Moodbidri has participated in Two-day workshop
on “FPGA and ASIC design using Mentor Graphics Tools”, organized by Department of Electronics and
Communication Engineering, Dr. MVSIT, Moodbidri, in association with Trident Techlabs Pvt. Ltd., Bengaluru.


Mr. Bharatesh B
Coordinator


Prof. Gangadhar G.
HOD, ECE

Dr. Sandeep J. Nayak
Principal

Prof. P.C.M. Kunhi
Patron



Alva's Institute of Engineering & Technology

Shobhavana Campus, Mijar, Moodbidri, D.K - 574225

Phone: 08258-262725, Fax: 08258-262726

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

FDP Permission letter

From,

Prof. Mahendra H.N

Asst. Prof.

Dept. of ECE

AZET.

To,

IQAC Chairman,

A.I.E.T. Mijar

Respected Sir,

Sub: Request to give permission to attend the FDP.

As I want to strengthen my professional development and increase the effectiveness of my teaching, I like to attend Faculty development program as two days.
Workshop on EPC to ASIC design using menton Graphics.
from 13th Aug 2015 to 14th Aug 2015 (02 days) at Dr. M.V. abeth Institute of Technology.
----- I request you to kindly give me the permission.

Place: Moodbidri

Date: 11/08/2015.

Signature

Name and designation

Must be permitted

I.Q.A.C
Dept. of Electronics Communication
Alva's Institute of Engg. & Technology
Mijar MOODBIDRI 574 225

PRINCIPAL
Alva's Institute of Engg. & Technology
Mijar, MOODBIDRI - 574 225, D.K



ALVA'S EDUCATION FOUNDATION®

MOODBIDRI - 574 227, D. K.

No.

DEBIT / CREDIT VOUCHER

12/8/2015

Sl. No	Particular	College	Dr	Cr
	TA and DA for			
	attending workshop		250/-	
			250/-	

Rupees :

Two hundred fifty only

Narration :

Amount paid towards Prof. Mahendra H.N for
2 days workshop on FPGA

D.V. 1000
Dept. Of Electronics & Communication
Alva's Institute of Technology
MOODBIDRI - 574 225

PRINCIPAL
Alva's Institute of Engg. & Technology
MOODBIDRI - 574 225, D.K.
AO FO

Receiver's/Remitter's
Signature