

DATE: 11th Jan 2017

From,

Mangala Kini B
Assistant Professor
Dept. of CSE
A.I.E.T, Mijar

Through,

The H.O.D.
Dept. of CSE
A.I.E.T, Mijar

To,

The Principal
A.I.E.T, Mijar

Respected Sir,

SUBJECT: - Requisition to attend a FDP

As per the above subject, I seek your permission to attend a FDP on "OOP with JAVA for DAA Lab & ARM Microcontroller Programming" in Jyothi Institute of Technology, Bengaluru from 16th to 21st Jan 2017. Kindly do the needful.

Thanking you,

Peri Hal
11/1/17

H. O. D.

Dept. Of Computer Science & Engineering
Alva's Institute of Engg. & Technology
Mijar, MOODBIDRI - 574 225

Yours Sincerely,

Mangala Kini B

(Mangala Kini B)

PRINCIPAL

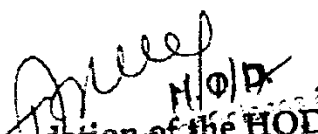
Alva's Institute of Engg. & Technology,
Mijar, MOODBIDRI - 574 225, D.K

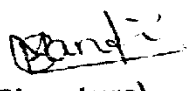
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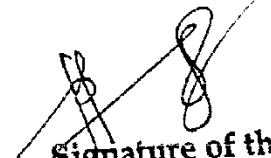
Application for Permission to attend
conference/Seminars/Workshops and Paper Presentation

1. Name of the Staff : MANCHALA KINJI B
2. Designation & Department : ASST. PROFESSOR, CSE
3. Name of the Conference/Seminar Symposium/Workshop : IDP on OOP with Java for DAA Lab
4. Type of conference is International/ National/State Level : NATIONAL
5. Place of Conference /workshop : Bangalore
6. Date of Conference/workshop : 16-01-2017 to 21-01-2017
7. Title of the paper submitted/accepted : -
8. Mention the items enclosed
- a. Copy of the brochure : ☒ (Yes/No)
 - b. Copy of invitation letter : (Yes/No)
 - c. Copy of the abstract of the paper : (Yes/No)
 - d. Acceptance received by Committee with recommendation
9. Rough estimate of expenditure
- a. Registration fee : 500/-
 - b. Train/Air fare : 1200/-
- 10: Details of previous conference/workshop Attended (Date/Place/Type) : Feb 2016, Mysore

Date: 11-01-2017


H/O/D
Recommendation of the HOD
Dept. of Computer Science & Engineering
Jyoti's Institute of Technology
Waran, MOORENDR - 574 225


(Signature)
Name: MANCHALA KINJI
Designation & Dept ASST. PROF, CSE


Signature of the Principal.
Jyoti's Institute of Engg. & Technology,
Waran, MOORENDR - 574 225, D.N

B V Seetha
Managing Trustee, Jyothy Group of
Institutions

on:
V Mahendra
Principal, JIT, Bengaluru

Special Thanks to

Ravi N,
Marketing Manager
Advanced Electronics Systems.

Faculty of Department of CSE / ISE
compiling the Manual for DAA
and MP & MC Lab

Convener:
Mr. Nagaraj A, HoD, Dept of ISE

Co-Conveners:
Mr. Pruthvi Kumar, Asst.Prof., ISE
Mr. Bharatesh C, Asst.Prof., ISE

Organizing Committee:
Mrs. Chhaya R, Asst.Prof., ISE
Mrs. Anusha G E, Asst. Prof, ISE
Mr. Vadiraja A, Asst.Prof., ISE
Mrs. Garima Goyal, Asst.Prof., ISE
Mrs. Anshu Mishra, Asst.Prof., ISE
Mr. Kishore G.R, Asst.Prof., ISE
Mr. Abhishek K, Asst.Prof., ISE
Mr. Niranjana C, Asst.Prof., ISE
Mr. Harsha S, Asst.Prof., ISE

Registration to the FDP is open to
personnel from the academia and Industry.
The selection process is based on the first-
come-first serve. Selected participants will
be informed by e-mail on or before 10th Jan
2017.

Participants will get the participation and
attendance certificates during the
valedictory.

Registration Fee: Rs. 500/-

Payment can be done by IMPS/NEFT/Credit
Card/Debit Card/Cash.

The details of Account for IMPS/NEFT will be
sent to the registered email-ID upon
confirmation.

We encourage online Transfer promoting
cashless economy.

Important Dates:

Last date for Registration: 7th Jan 2016

Venue

The FDP will be held at Jyothy Institute of
Technology at A Block Seminar Hall and
Laboratory 2

Contact:

Pruthvi Kumar
9741410245

Bharatesh C
8095464858

Visit: jyothyit.ac.in/fdp for registration



Jyothy Institute of Technology
Tataguni, Bengaluru - 82

Department of Information Science and
Engineering
Presents

One Week Faculty Development Program
on

**OOP with JAVA for DAA Lab
&
ARM Microcontroller
Programming**

A Hands on Workshop

in Association With

AES Advanced Electronics Systems

From 16th to 21st January 2017





JYOTHY INSTITUTE OF TECHNOLOGY

(APPROVED BY AICTE & AFFILIATED TO VTU)

TATAGUNI, OFF KANAKAPURA ROAD, BENGALURU - 560082

www.jyothyit.ac.in



CERTIFICATE OF PARTICIPATION

*This is to certify that Mr./Ms. **Mangala Kini B** from **Alva's Institute of Engineering and Technology** has participated in One Week Faculty Development Program on **"OOP with Java for DAA Lab & ARM Microcontroller Programming"** organized by Department of Information Science and Engineering in association with Advanced Electronics Systems from 16th to 21st Jan 2017.*

Convener

HoD - ISE

Principal

Report on FDP

A faculty development program on OOP with JAVA for DAA Lab and ARM programming was held from 16-01-2017 to 21-1-2017 at Jyothy Institute of Technology Thataguni, Bengaluru.

Day 1: The event was inaugurated by Mr. Uday M V, senior software engineer IBM-India and Mr. Nagaraj, Head of the department, CSE, JIT. Mr. Uday M v senior software engineer IBM- India gave a keynote talk on “introduction to Algorithm-An Industrial perspective”. He mentioned how algorithm was evolved and how it is used in mathematics and computer science. He also gave brief note on how algorithms are used in industries. He mentioned the languages used to build algorithms, the dimensions of algorithms and how to find the correctness of algorithm i.e. finding a reliable algorithm. The talk continued with an activity in which a problem statement was given and we the participants had to analyze the problem and had to write the pseudo code/algorithm, write the object oriented importance then matching the algorithm with the object oriented concept then finally writing the test cases to match the requirement. Importance of workshop was conveyed to the participants by the speaker.

It was followed by session 2 “Unlearning procedure oriented programming and object oriented thinking”. This session was given by Prof. Nagraj A, HOD, Dept of CSE, JIT, Bengaluru. This session gave brief introduction on types of programming. The concept of procedure oriented programming was explained. A comparative study was made between procedure oriented program and object oriented programming. Then discussion on why object oriented programming should be cultivated instead of procedure oriented programming.

In session 3 DAA lab program 1 and lab program 2 was explained by Prof. Sumana C and then lab program 3 exception handling and multithreading was explained by Prof. Niranjana. Followed by the theory session there was hands on session for both the programs on eclipse.

Day 2: The session began with the talk on “Object Oriented Concepts and Applications in Software Industries” by Kalish, ERP Analyst Aptean India Software. He briefed the concepts of object oriented programming and its applications and how it is used in industries.

In the next session algorithms like quick sort and merge sort were explained by Prof. Adithya B, and followed by hands on session for the same was conducted.

Afternoon session Knapsack problem-greedy and Dynamic Programming was discussed by Prof. Manoj Kumar. Dijkstras algorithm was explained by Prof. Harsha. It was followed by hands on session.

Day 3: The session began with the talk on "Automation and Data Preparation" by Mr. Shashank, FSMK. He briefed the concepts of data warehousing and data cleaning and how these data warehouses are maintained and how the unwanted data are cleaned in software industries.

In the next session algorithms like Prims and Kruskals was discussed by Prof. Anusha G E and Prof. Vadiraja A. It was followed by hands on session.

Afternoon session Floyd algorithm was discussed by Prof. Srinidhi. Travelling salesman problem was explained by Prof. Bharatesh. Sum of subsets and Hamelton cycle was explained by Prof. Saravana. It was followed by hands on session.

Day 4: The session began with the talk on "Introduction to ARM architecture" by Mr. Jivan B Katariya, Director SPJ Embedded technologies pvt ltd. He briefed the concepts of ARM architechture.

Day 5: The day began with the hands on session on ARM architecture design and sample programs were made to execute.

Afternoon there was a hands on session on Arm controller software and hardware.

Day 6: The session began with talk on Microprocessors and Microcontrollers and its in industrial applications by Kiran Desai, Scientist, ISAC.

The session was followed by the valedictory function. Certificates were distributed by Department of Computer science and Engg, JIT, Bengaluru.

This FDP was helpful as we could clear all the doubts in the new subject of CBCS scheme and also can implement these teaching methods to improve the students knowledge.

Deeksha M - *DH*

Mangala kini - *Parth*

Signature of staff

[Signature]
H.O.D.
Signature of HOD

Dept. of Computer Science & Engineering



ALVA'S EDUCATION FOUNDATION (R.)

MOODBIDRI - 574 227, D.K.

DEBIT / CREDIT VOUCHER

No. :

Date 11-01-2017

Sl.No.	Particulars	College Code	Debit (₹)	Credit (₹)
a	FDP registration fees at JIT, Blore	AL	500/-	
b	TA		1200/-	
c				
d				
e				
PAYABLE / RECEIVABLE			1700/-	
TOTAL			1700/-	

Rupees One thousand seven hundred only

Narration Ms. Mangala Kini

Assistant Professor, Dept. of CSE, AIET

Prepared by

Principal

A.O.

Authorised by

Receiver's / Remitter's Signature

Alva's Institute of Engineering & Technology,
Mijur, MOODBIDRI - 574 225, D.K.

Dept. Of Computer Science & Engineering
Alva's Institute of Engineering & Technology,
MOODBIDRI - 574 225