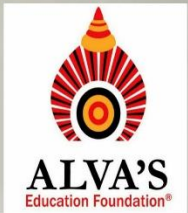
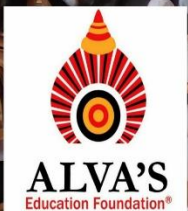


**Department of Electronics & Communication Engineering**



13.02477, 74.96672 ( $\pm 2000$ m)  
Altitude: -29m  
2XF8+GP5, Tenkamijar, Karnataka 574225



13.02404, 74.96636 ( $\pm 1100$ m)  
Altitude: -29m  
2XF8+GP5, Tenkamijar, Karnataka 574225

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**CLASS ROOM NO-301**





**Time Table with effect from 23/11/2020**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Academic Year		Scheme		Semester		Section		Class Coordinator		Room No
2020-21		2018		3 <sup>RD</sup>		A		Mrs. Shruthi k		301
Time Day	9.00 To 9.55	9.55 To 10.50	10.50 To 11.10	11.10 To 12.00	12.00 To 12.55	12.55 To 2.00	2.00 To 3.00	3.00 To 4.00	4.00 To 5.00	
MON	ED	DSD LAB (BG+SSJ) ED LAB (SS+PBG)				L U N C H	DSD	MEMS LAB(BG)		
TUE	COA	NT	B R E A K	PEI	M-3		ED	MINI PROJECT (SHM)		
WED	M-3	ED		COA	DSD		DSD	M-3	PEI	
THU	PEI	ED		CIP	NT		DSD LAB (KS) ED LAB (SS+PBG)			
FRI	COA	NT		PEI	DSD		M-3	SEMINAR (PBG)		
SAT	NT	DSD		COA	M-3					
Allocation of Subjects										
Subjects						Staffs		Staff Code		
M-3	18MAT31	Mathematics - II				Mrs. Kavitha B		KB		
NT	18EC32	Network Theory				Mr. Deepak Raj		DRJ		
ED	18EC33	Electronics Devices				Mr. Parveez Shariff		PBG		
DSD	18EC34	Digital System Design				Dr. D V Manjunatha		DVM		
COA	18EC35	Computer Organization And Architecture				Mrs. Kumari Shruthi		KS		
PE&I	18EC36	Power Electronics and Instrumentation				Dr.Mirnmoy Mishra		MM		
ED LAB	18ECL37	Electronic Devices & Instrumentation Laboratory				Mr. Santhosh S + Mr. Parveez Shariff		SS +PBG		
DSD LAB	18ECL38	Digital System Design Laboratory				Mrs. Kumari Shruthi + Mrs. Bhargavi+Mr. Sree Sankar		KS+ BG+SSJ		
CIP	18CPC39	Constitution of India, Professional Ethics and Cyber Law				Mr. Ajith Kumar		AK		

HOD

H. O. D.

Dept. Of Electronics & Communication  
Alva's Institute of Engg. & Technology  
Mijar, MOODBIDRI - 574 225

Principal

Alva's Institute of Engg. & Technology,  
Mijar, MOODBIDRI - 574 225, D.K

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**CLASS ROOM NO-302**





**Time Table with effect from 23/11/2020**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Academic Year		Scheme		Semester		Section		Class Coordinator		Room No
2020-21		2018		3 <sup>RD</sup>		B		Mrs. Bhargavi		302
Time Day	9.00 To 9.55	9.55 To 10.50	10.50 To 11.10	11.10 To 12.00	12.00 To 12.55	12.55 To 2.00	L U N C H	2.00 To 3.00	3.00 To 4.00	4.00 To 5.00
MON	M-3	PEI	BREAK	NT	DSD	NT		SEMINAR (SS)		
TUE	DSD LAB(BG) ED (YT+PBG)				ED	M-3		COA	PEI	
WED	COA	PEI	B R E A K	DSD	ED	DSD LAB(SHM) ED (YT+PBG)				
THU	DSD	M-3		ED	CIP	NT		MEMS LAB(AJ)		
FRI	PEI	ED		COA	M-3	ED		MINI PROJECT(SHM)		
SAT	M-3	COA		NT	DSD					

**Allocation of Subjects**

Subjects			Staffs		Staff Code
M-3	18MAT31	Mathematics - III	Mrs. Kavitha B		KB
NT	18EC32	Network Theory	Mr. Deepak Raj		DRJ
ED	18EC33	Electronics Devices	Mr. Parveez Shariff		PBG
DSD	18EC34	Digital System Design	Mr. Sudhaker H		SHM
COA	18EC35	Computer Organization And Architecture	Mrs. Kumari Shruthi		KS
PE&I	18EC36	Power Electronics and Instrumentation	Dr. Mirnmoy Mishra		MM
ED LAB	18ECL37	Electronic Devices & Instrumentation Laboratory	Mr. Yuvaraj T+ Mr. Parveez Shariff		YT +PBG
DSD LAB	18ECL38	Digital System Design Laboratory	Mr. Sudhaker H M + Mrs. Bhargavi		SHM+BG
CIP	18CPC39	Constitution of India, Professional Ethics and Cyber Law	Mr. Ajith Kumar		AK

*D.V.T.*

HOD

M. O. D.

*[Signature]*  
Principal  
PRINCIPAL

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**CLASS ROOM NO-303**





**Time Table with effect from 23/11/2020**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Academic Year		Scheme		Semester		Section		Class Coordinator			Room No
2020-21		2017		7 <sup>TH</sup>		A		Mr. Sudhakar. H.M			303
Time Day	9.00 To 9.55	9.55 To 10.50	10.50 To 11.10	11.10 To 12.00	12.00 To 12.55	12.5 5 To 2.00	2.00 To 3.00	3.00 To 4.00	4.00 To 5.00		
MON	MWA	DIP	BREAK	RTS	DSPA	L U N C H	PE	SEMINAR(KS)			
TUE	DIP	PE		MWA	DSPA		RTS	BASIC CLASS			
WED	ADC LAB(SS+ NI) VLSI LAB(DRJ)				DSPA		MWA	PROJECT WORK			
THU	DIP	SEMINAR (BG)	B R E A K	SEMINAR (BG)	PE		MWA	PROJECT WORK			
FRI	RTS	PE		DIP	DSPA		ADC LAB(VTS) VLSI LAB(DRJ)				
SAT	PE	RTS		DIP	MWA		PROJECT WORK				
Allocation of Subjects											
Subjects						Staffs			Staff Code		
MWR	17EC71	Microwave and Antenna				Mr. Sree Sankar			SSJ		
DIP	17EC72	Digital image processing				Mrs. Nishma K			NI		
PE	17EC73	Power electronics				Mr. Sudhakar H M			SHM		
RTS	17EC743	Real time systems				Mr.Sachin K			SK		
DSPAA	17EC751	DSP Algorithm and Architecture				Mrs. Vijetha T S			VTS		
ADC-LAB	17ECL76	Advanced Communication Lab				Mr.Santhosh S +Mrs. Vijetha T S +Mrs. Nishma K			SS+VTS +NI		
VLSI-LAB	17ECL77	VLSI Laboratory				Mr. Deepak Raj			DRJ		
PW	17ECP78	Project Work Phase-I				Mr.Santhosh S			SS		

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H.O.D

H. O. D.

Dept. Of Electronics & Communication  
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*[Signature]*  
Principal

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**CLASS ROOM NO-304**





**Time Table with effect from 23/11/2020**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Academic Year		Scheme	Semester		Section		Class Coordinator		Room No	
2020-21		2017	7 <sup>TH</sup>		B		Mrs. Nishma K		304	
Time Day	9.00 To 9.50	9.50 To 10.40	10.40 To 11.00	11.10 To 11.50	11.50 To 12.40	12.40 To 01.40	1.40 To 2.30	2.30 To 3.20	3.30 To 5.00	
MON	DSPA	PE	B R E A K	MWA	DIP	L U N C H	RTS	PROJECT WORK		
TUE	MWA	DSPA		DIP	PE		ADC LAB(VTS+ NI) VLSI LAB(AJ)			
WED	PE	SEMINAR (SSJ)		SEMINAR (SSJ)	RTS		DIP	PROJECT WORK		
THU	ADC LAB(SS+ SSJ) VLSI LAB(AJ)				RTS		DIP	BASIC CLASS		
FRI	MWA	DSPA		BREAK	RTS		PE	PE	DSPA	MWA
SAT	DIP	MWA			SEMINAR(BG)					
Allocation of Subjects										
Subjects						Staffs		Staff Code		
MWR	17EC71	Microwave and Antenna				Mr. Sree Sankar		SSJ		
DIP	17EC72	Digital image processing				Mrs. Nishma K		NI		
PE	17EC73	Power electronics				Mr. Sudhakar H M		SHM		
RTS	17EC743	Real time systems				Mr. Sachin K		SK		
DSPAA	17EC751	DSP Algorithm and Architecture				Mr. Sushanth Anil Lobo		SAL		
ADC-LAB	17ECL76	Advanced Communication Lab				Mr.Santhosh S +Mrs. Vijetha T S +Mrs. Nishma K + Mr. Sree Sankar		SS+VT S+NI+SSJ		
VLSI-LAB	17ECL77	VLSI Laboratory				Mr. Anish Jain		AJ		
PW	17ECP78	Project Work Phase-I				Mr.Santhosh S		SS		

  
H.O.D.

H. O. D.

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