

in

for the thesis entitled

in recognition of the fulfillment of requirements
for the said degree

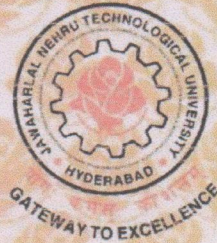
BELGAUM
DATE : MAY 09, 2015


VICE CHANCELLOR

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, TELANGANA STATE, INDIA

TS 703322



H.T No.: 0803PH0899

*Mr. Dathathreya
N. Satyanarayan Rao*

having fulfilled the academic requirements in *December, 2015*
has this day been admitted by the Executive Council to the Degree of

*Doctor Of Philosophy
Electronics & Communication Engineering*

Topic : "Intelligent Image Compression and Authentication Using Artificial Neural Networks".

Given under the Seal of the University

Date: 21-04-2017



PRINCIPAL

Abra's Institute of Engg. & Technology,
Mijar, MOODBIDRI - 574 225, D.K

DIRECTOR OF EVALUATION

REGISTRAR

VICE CHANCELLOR



461061406349

PC No
315000090

UNIVERSITY OF MYSORE
ಮೈಸೂರು ವಿಶ್ವವಿದ್ಯಾನಿಲಯ



*We, the Chancellor, the Pro-Chancellor, the Vice-Chancellor and
Members of the Syndicate of the University of Mysore
do hereby certify that*

ಮೈಸೂರು ವಿಶ್ವವಿದ್ಯಾನಿಲಯದ ಕುಲಾಧಿಪತಿಗಳು, ಸಮಕುಲಾಧಿಪತಿಗಳು, ಕುಲಪತಿಗಳು
ಹಾಗೂ ಸಿಂಡಿಕೇಟಿನ ಸದಸ್ಯರಾದ ನಾವು

PRAVEEN.J

has been awarded the Degree of
ಅವರು ನಿಗದಿತ ಪರೀಕ್ಷೆಯಲ್ಲಿ ಉತ್ತೀರ್ಣರಾಗಿರುವುದರಿಂದ

DOCTOR OF PHILOSOPHY
in
Electronics

ಡಾಕ್ಟರ್ ಆಫ್ ಫಿಲಾಸಫಿ

on being duly certified to have passed the prescribed examination

ಪದವಿಯನ್ನು ಅವರಿಗೆ ಪ್ರದಾನ ಮಾಡಲಾಗಿದೆಯೆಂದು ಪ್ರಮಾಣೀಕರಿಸುತ್ತೇವೆ

Title of the Thesis

"Bist Based Low Power Transition Test Pattern Generation for Minimizing Test Power in VLSI
Circuit"

[Signature]

Prepared the thesis in English Language as per 2004 Regulation

Under the guidance of

Dr. M.N.Shanmukha Swamy

Given under the seal of the University

ವಿಶ್ವವಿದ್ಯಾನಿಲಯದ ಮೊಹರಿನೊಂದಿಗೆ ನೀಡಲಾಗಿದೆ



Date of Convocation

ಘಟಿಕೋತ್ಸವದ ದಿನಾಂಕ

12/03/2018

Mysore / ಮೈಸೂರು

Sl.No. / ಕ್ರಮ ಸಂಖ್ಯೆ 2191

[Signature]
Vice-Chancellor
ಕುಲಪತಿ

*We, the Chancellor, Vice-Chancellor, and
Fellows of the University of Bombay, certify.*

that the Degree of

Doctor of Philosophy

(Science)

*has been conferred on the twenty-eighth day of
the month of March in the year one thousand
nine hundred and seventy-two at Bombay,
on Richard Pinto*

who has been found duly qualified for the same.

*In Testimony whereof are set the Seal of the
said University and the Signature of the said
Vice-Chancellor.*

T. K. Tole

Vice-Chancellor



PRINCIPAL

Govt's Institute of Engg. & Technology,

Warananagar, Mumbai - 400 025, D.R.

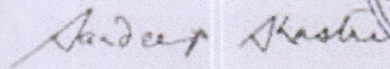
9th February 2019

TO WHOMSOEVER IT MAY CONCERN

This is to certify Mr. Shankar B Bandiwaddar has successfully completed the Viva and open defense on 9th February 2019 for Ph.D. in Electronics Engineering titled "ADVENTITIOUS SOUND ANALYSIS: AN EXPLORATION OF HHT" under the guidance of Dr. D Jayadevappa.

He has also complied with all the criteria of his Ph.D Degree. The Notification and the Provisional Degree Certificate will be issued after approval gained from the authorizing committee and Boards in the due course of time. He will be eligible for applying for the convocation certificate on upcoming Convocation Day.

With warm regards,



Dr. Sandeep Shastri
Pro Vice Chancellor
JainUniversity
Atria Towers, No. 1, Palace Road,
Bengaluru - 560001



PRINCIPAL
Atria's Institute of Engg. & Technology,
Mijur, MOODBIDRI - 574 225, D.K