

**VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI**  
**Scheme of Teaching and Examination 2018 - 19**  
**Outcome Based Education (OBE) and Choice Based Credit System (CBCS)**  
**(Effective from the academic year 2018 - 19)**

**Programme: B.E: Electronics & Communication Engineering**

**III SEMESTER**

III SEMESTER													
Sl. No	Course and Course Code		Course Title	Teaching Department	Teaching Hours/Week			Examination				Credits	
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks		
					L	T	P						
1	BSC	18MAT31	Transform Calculus, Fourier Series and Numerical Techniques	Mathe matics	2	2	--	03	40	60	100	3	
2	PCC	18EC32	Network Theory		3	2	--	03	40	60	100	4	
3	PCC	18EC33	Electronic Devices		3	0	--	03	40	60	100	3	
4	PCC	18EC34	Digital System Design		3	0	--	03	40	60	100	3	
5	PCC	18EC35	Computer Organization & Architecture		3	0	--	03	40	60	100	3	
6	PCC	18EC36	Power Electronics & Instrumentation		3	0	--	03	40	60	100	3	
7	PCC	18ECL37	Electronic Devices & Instrumentation Laboratory		--	2	2	03	40	60	100	2	
8	PCC	18ECL38	Digital System Design Laboratory		--	2	2	03	40	60	100	2	
9	HSMC	18KVK39/49	Vyavaharika Kannada (Kannada for Communication)/	HSMC	--	2	--	--	100	--	100	1	
		18KAK39/49	Aadalitha Kannada (Kannada for Administration)										
		OR											
		18CPC39/49	Constitution of India, Professional Ethics and Cyber Law										
		Examination is by objective type questions											
TOTAL					17	10	04	24	420	480	900	24	
					OR	OR		OR	OR				
					18	08		26	360	540			

  
**H. O. D.**

Dept. Of Electronics & Communication  
 Alva's Institute of Engg. & Technology  
 Mijar, MOODSIDRI - 574 225

  
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**IV SEMESTER**

IV SEMESTER													
Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits	
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks		
					L	T	P						
1	BSC	18MAT41	Complex Analysis, Probability and Statistical Methods	Mathematics	2	2	--	03	40	60	100	3	
2	PCC	18EC42	Analog Circuits		3	2	--	03	40	60	100	4	
3	PCC	18EC43	Control Systems		3	0	--	03	40	60	100	3	
4	PCC	18EC44	Engineering Statistics & Linear Algebra		3	0	--	03	40	60	100	3	
5	PCC	18EC45	Signals & Systems		3	0	--	03	40	60	100	3	
6	PCC	18EC46	Microcontroller		3	0	--	03	40	60	100	3	
7	PCC	18ECL47	Microcontroller Laboratory		--	2	2	03	40	60	100	2	
8	PCC	18ECL48	Analog Circuits Laboratory		--	2	2	03	40	60	100	2	
9	HSMC	18KVK39/49	Vyavaharika Kannada (Kannada for Communication)	HSMC	--	2	--	--	100	--	100	1	
		18KAK39/49	Aadalitha Kannada (Kannada for Administration)										
		OR											
		18CPC39/49	Constitution of India, Professional Ethics and Cyber Law		1	--	--	02	40	60			
		Examination is by objective type questions											
TOTAL					17	10	04	24	420	480	900	24	
					OR	OR		OR	OR				
					18	08		26	360	540			

  
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**V SEMESTER**

V SEMESTER												
Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
1	HSMC	18ES51	Technological Innovation Management and Entrepreneurship		3	0	--	03	40	60	100	3
2	PCC	18EC52	Digital Signal Processing		3	2	--	03	40	60	100	4
3	PCC	18EC53	Principles of Communication Systems		3	2	--	03	40	60	100	4
4	PCC	18EC54	Information Theory & Coding		3	--	--	03	40	60	100	3
5	PCC	18EC55	Electromagnetic Waves		3	--	--	03	40	60	100	3
6	PCC	18EC56	Verilog HDL		3	--	--	03	40	60	100	3
7	PCC	18ECL57	Digital Signal Processing Laboratory		--	2	2	03	40	60	100	2
8	PCC	18ECL58	HDL Laboratory		--	2	2	03	40	60	100	2
9	HSMC	18CIV59	Environmental Studies	Civil/Environmental	1	--	--	02	40	60	100	1
				[Paper setting: Civil Engineering Board]								
TOTAL					19	8	4	26	360	540	900	25

**Note:** PCC: Professional Core, HSMC: Humanity and Social Science.

**AICTE activity Points:** In case students fail to earn the prescribed activity Points, Eighth semester Grade Card shall be issued only after earning the required activity Points. Students shall be admitted for the award of degree only after the release of the Eighth semester Grade Card.

  
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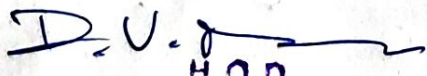
**VI SEMESTER**


SL No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	PCC	18EC61	Digital Communication		3	2	--	03	40	60	100	4
2	PCC	18EC62	Embedded Systems		3	2	--	03	40	60	100	4
3	PCC	18EC63	Microwave and Antennas		3	2	--	03	40	60	100	4
4	PEC	18XX64X	Professional Elective -1		3	--	--	03	40	60	100	3
5	OEC	18XX65X	Open Elective -A		3	--	--	03	40	60	100	3
6	PCC	18ECL66	Embedded Systems Laboratory		--	2	2	03	40	60	100	2
7	PCC	18ECL67	Communication Laboratory		--	2	2	03	40	60	100	2
8	MP	18ECMP68	Mini-project		--	--	2	03	40	60	100	2
9	Internship	--	Internship	To be carried out during the vacation/s of VI and VII semesters and /or VII and VIII semesters.								
<b>TOTAL</b>					<b>15</b>	<b>10</b>	<b>6</b>	<b>24</b>	<b>320</b>	<b>480</b>	<b>800</b>	<b>24</b>

**Note: PCC: Professional core, PEC: Professional Elective, OE: Open Elective, MP: Mini-project.**

**Professional Elective -1**

Course code under 18XX64X	Course Title
18EC641	Operating System
18EC642	Artificial Neural Networks
18EC643	Data Structures using C++
18EC644	Digital System Design Using Verilog
18EC645	Nanoelectronics
18EC646	Python Application Programming

  
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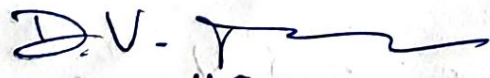
**VII SEMESTER**


Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination			Credits	
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks		Total Marks
					L	T	P					
1	PCC	18EC71	Computer Networks		3	--	--	03	40	60	100	3
2	PCC	18EC72	VLSI Design		3	--	--	03	40	60	100	3
3	PEC	18XX73X	Professional Elective - 2		3	--	--	03	40	60	100	3
4	PEC	18XX74X	Professional Elective - 3		3	--	--	03	40	60	100	3
5	OEC	18XX75X	Open Elective -B		3	--	--	03	40	60	100	3
6	PCC	18ECL76	Computer Networks Lab		--	2	2	03	40	60	100	2
7	PCC	18ECL77	VLSI Laboratory		--	2	2	03	40	60	100	2
8	Project	18ECP78	Project Work Phase - 1		--	--	2	--	100	--	100	1
9	Internship	--	Internship	(If not completed during the vacation of VI and VII semesters, it shall be carried out during the vacation of VII and VIII semesters)								
TOTAL					15	04	06	21	38	420	800	20

**Note: PCC: Professional core, PEC: Professional Elective.**

**Professional Elective - 2**

Course code under 18XX73X	Course Title
18EC731	Real Time Systems
18EC732	Satellite Communication
18EC733	Digital Image Processing
18EC734	DSP Algorithms & Architecture

  
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**VIII SEMESTER**


VIII SEMESTER												
Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	PCC	18EC81	Wireless and Cellular Communication		3	--	--	03	40	60	100	3
2	PEC	18XX82X	Professional Elective - 4		3	--	--	03	40	60	100	3
3	Project	18ECP83	Project Work Phase - 2		--	--	2	03	40	60	100	8
4	Seminar	18ECS84	Technical Seminar		--	--	2	03	100	--	100	1
5	Internship	18ECI85	Internship	Completed during the vacation/s of VI and VII semesters and /or VII and VIII semesters.)				03	40	60	100	3
TOTAL					06	--	04	15	260	240	500	18

**Note:** PCC: Professional Core, PEC: Professional Elective.

**Professional Elective - 4**

Course code under 18XX82X	Course Title
18EC821	Network Security
18EC822	Micro Electro Mechanical Systems
18EC823	Radar Engineering
18EC824	Optical Communication Networks
18EC825	Biomedical Signal Processing

  
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