MICROPROCESS			LERS		
_ <b>_</b>	•	em (CBCS) scheme]			
(Effective from	m the academic y	*			
	SEMESTER -				
Subject Code	15CS44	IA Marks	20	20	
Number of Lecture Hours/Week	04	Exam Marks	80	80	
Total Number of Lecture Hours	50	Exam Hours	03	3	
	CREDITS –	04			
Course objectives: This course will en			<u>- ii</u>		
Make familiar with importance		•	microcontr	ollers	
• Expose architecture of 8086 m	•	ARM processor			
• Familiarize instruction set of A	ARM processor				
Module 1				Teaching	
The96 Dei-6 1:	: - 4	C family India day	0000/07	Hours	
The x86 microprocessor: Brief his				10 Hour	
Introduction to assembly programmin Flag register, x86 Addressing Modes.					
a Sample Program, Assemble, Link &	• •	0.0			
Transfer Instructions, Data Types					
Flowcharts and Pseudo code.	and Data Denni	tion, I un beginent I	cillition,		
Text book 1: Ch 1: 1.1 to 1.7, Ch 2:	2.1 to 2.7		30:00		
Module 2					
x86: Instructions sets description, Ar	rithmetic and log	gic instructions and p	rograms:	10 Hour	
Unsigned Addition and Subtraction	_	•	_		
Instructions, BCD and ASCII convers	ion, Rotate Instru	ctions. INT 21H and	INT 10H		
<b>Programming:</b> Bios INT 10H Progra	amming, DOS In	terrupt 21H. 8088/86	Interrupts,		
x86 PC and Interrupt Assignment.					
Text book 1: Ch 3: 3.1 to 3.5, Ch 4: 4	<b>4.1</b> , <b>4.2</b> Chapter 1	14: 14.1 and 14.2			
Module 3					
<b>Signed Numbers and Strings:</b> Signed			•	10 Hour	
Memory and Memory interfacing:	•	•			
and ROM, 16-bit memory interfacing		amming: I/O addresse	s MAP of		
x86 PC's, programming and interfacing	0	. 11. 11 1 4. 11 4			
Text book 1: Ch 6: 6.1, 6.2. Ch 10: 10  Module 4	0.2, 10.4, 10.5. Cn	111: 11.1 to 11.4			
Microprocessors versus Microcontroll	ore ADM Ember	Idad Systems The DI	SC design	10 Hour	
philosophy, The ARM Design Philo			_	10 Hour	
System Software, ARM Processor Fu		•			
Register, Pipeline, Exceptions, Interru		-			
Text book 2:Ch 1:1.1 to 1.4, Ch 2:2.1	-				
Module 5					
Introduction to the ARM Instruct	ion Set : Data F	Processing Instructions	, Branch	10 Hour	
				10 Hour	
Introduction to the ARM Instruct	tructions, Program	n Status Register Ins		10 Hour	

Course Outcomes: After studying this course, students will be able to

- Differentiate between microprocessors and microcontrollers
- Design and develop assembly language code to solve problems
- Gain the knowledge for interfacing various devices to x86 family and ARM processor
- Demonstrate design of interrupt routines for interfacing devices

## **Graduate Attributes**

- Engineering Knowledge
- Problem Analysis
- Design/Development of Solutions

## Question paper pattern:

The question paper will have ten questions.

There will be 2 questions from each module.

Each question will have questions covering all the topics under a module.

The students will have to answer 5 full questions, selecting one full question from each module.

## Text Books:

- 1. Muhammad Ali Mazidi, Janice Gillispie Mazidi, Danny Causey, The x86 PC Assembly Language Design and Interfacing, 5<sup>th</sup> Edition, Pearson, 2013.
- 2. **ARM system developers guide**, Andrew N Sloss, Dominic Symes and Chris Wright, Elsevier, Morgan Kaufman publishers, 2008.

## Reference Books:

- 1. Douglas V. Hall: Microprocessors and Interfacing, Revised 2<sup>nd</sup> Edition, TMH, 2006.
- 2. K. Udaya Kumar & B.S. Umashankar: Advanced Microprocessors & IBM-PC Assembly Language Programming, TMH 2003.
- 3. Ayala: The 8086 Microprocessor: programming and interfacing 1st edition, Cengage Learning
- 4. The Definitive Guide to the ARM Cortex-M3, by Joseph Yiu, 2nd Edition, Newnes, 2009
- 5. The Insider's Guide to the ARM7 based microcontrollers, Hitex Ltd., 1<sup>st</sup> edition, 2005
- 6. ARM System-on-Chip Architecture, Steve Furber, Second Edition, Pearson, 2015
- 7. Architecture, Programming and Interfacing of Low power Processors- ARM7, Cortex-M and MSP430, Lyla B Das Cengage Learning, 1<sup>st</sup> Edition