VISVESVARAYA TECHNOLOGICAL UNIVERSITY

"Jnana Sangama" Belagavi - 590 010



PROJECT REPORT ON

"DESIGN OF MEMORY INTERFACE AND PROGRAMMER FOR FeRAM"

Submitted in partial fulfillment of the requirements for the award of degree

BACHELOR OF ENGINEERING IN ELECTRONICS & COMMUNICATION ENGINEERING

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ALVA'S INSTITUTE OF ENGINEERING & TECHNOLOGY **MOODBIDRI - 574 225**

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CERTIFICATE

Certified that the project work entitled "DESIGN OF MEMORY INTERFACE AND PROGRAMMER FOR FeRAM" is a bona fide work carried out by

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in partial fulfillment for the award of BACHELOR OF ENGINEERING in ELECTRONICS & COMMUNICATION ENGINEERING of the VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI during the year 2018-2019. It is certified corrections/suggestions indicated for Internal Assessment have been incorporated in the report deposited in the departmental library. The project report has been approved as it satisfies the academic requirements in respect of Project work prescribed for the Bachelor of Engineering Degree.

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ABSTRACT

The On-board computer of satellite has a PROM (Programmable Read only Memory) which is volatile. PROM in On Board Computer is used to store the Program data for the operation of the various sensors and devices in satellite. Since Software requires constant improvisation, using PROM to store the program data is not feasible. A unique approach is proposed to replace PROM in On-board computer. A Memory interface is designed for the targeted FcRAM for storing the program data. FcRAM is a non-volatile memory and has faster access time compared to PROM. Memory Interface circuit is designed using the OrCAD Capture tool with the proper analysis of power requirements. FcRAM is programmed using the Microsemi smart fusion Board. Libero SOC and Soft Console software are used to program the FcRAM. For embedded systems, being able to run native TCP/IP makes it possible to connect the system directly to an intranet or even the global Internet. FcRAM can be programmed by a remote PC connected to internet. To authenticate the data, Checksum is verified after writing the data into FcRAM.

FeRAM has faster access time compared to PROM and high data retention time. This will make the FeRAM ideal to store the program data. The memory interface circuit derives its signal from On-board computer through 78 pin connector and it is designed with the concern of safety of the On-board computer. This unique approach to store program data in FeRAM makes it feasible to use in On-board computer.