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U. R. Rao Satellite Centre
Department of Space
Government of India
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DATE: 30-04-2019

HRDD PPEG

This is certify that	Mr. Vasanth Kumar M	
of A	Alvas Institute of Engineering and Technology, Mijar	
Project training in_	Control and Digital Electronics Group	at this Centre
during the period fro	om 16/01/2019 to 30/04/2019.	

COURSE : B.E (Electronics and Communication)

PROJECT TITLE : Design of Memory Interface for FRAM

PROJECT MEMBERS : 1. Shruthi I T 2. Gowda Rachita B V 3. Sreecharan B R

PERFORMANCE : Excellent

(Prakash K S) Head, HRD Division, URSC em

(Rajendra Hulyal)
Group Head PPEG, URSC