

REFERENCE BOOKS:

1. "Algorithms for VLSI Physical Design Automation", N. A. Shervani, 1999.
2. "Algorithms for VLSI Design Automation", S. H. Gerez, 1998.

APPLIED EMBEDDED SYSTEM DESIGN

Subject Code	: 10EC755	IA Marks	: 25
No. of Lecture Hrs/Week	: 04	Exam Hours	: 03
Total no. of Lecture Hrs.	: 52	Exam Marks	: 100

1. **Embedded Systems : Architecture, Programming, and Design**, Raj Kamal, 2nd Edn. TMH, 2008.

REFERENCE BOOKS:

1. **Embedded System Design – A certified Hardware / Software Introduction**, Frank Vahid, John Wikey & Sons, 2002.
2. **An embedded Software Primer** by David E Simon, Pearson Edition 1999.

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