HDL LAB
(Common to EC/TC/IT/BM/ML)

Sub Code	:	10ECL48	IA Marks	:	25
Hrs/ Week	:	03	Exam Hours	:	03
Total Hrs.	:	42	Exam Marks	:	50

Note: Programming can be done using any compiler. Download the programs on a FPGA/CPLD boards such as Apex/Acex/Max/Spartan/Sinfi/TK Base or equivalent and performance testing may be done using 32 channel pattern generator and logic analyzer apart from verification by simulation with tools such as Altera/Modelsim or equivalent.

PROGRAMMING (using VHDL /Verilog)

1. Write HDL code to realize all the logic gates
2. Write a HDL program for the following combinational designs
   a. 2 to 4 decoder
   b. 8 to 3 (encoder without priority & with priority)
   c. 8 to 1 multiplexer
   d. 4 bit binary to gray converter
   e. Multiplexer, de-multiplexer, comparator.
2. Write a HDL code to describe the functions of a Full Adder Using three modeling styles.

3. Write a model for 32 bit ALU using the schematic diagram shown below
A (31:0)   B (31:0)

```
┌─────────────────────┐
│                      │
│  Opcode (3:0)        │
│                      │
│                      │
│  Enable              │
│                      │
└─────────────────────┘
```
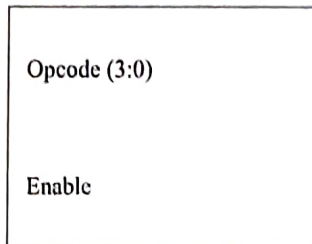
- ALU should use combinational logic to calculate an output based on the four bit op-code input.
- ALU should pass the result to the out bus when enable line in high, and tri-state the out bus when the enable line is low.

2

D.V.

- ALU should decode the 4 bit op-code according to the given in example below.

OPCODE	ALU OPERATION
1.	A + B
2.	A – B
3.	A Complement
4.	A * B
5.	A AND B
6.	A OR B
7.	A NAND B
8.	A XOR B

4. Develop the HDL code for the following flip-flops, SR, D, JK, T.
5. Design 4 bit binary, BCD counters (Synchronous reset and Asynchronous reset) and "any sequence" counters

INTERFACING (at least four of the following must be covered using VHDL/Verilog)

1. Write HDL code to display messages on the given seven segment display and LCD and accepting Hex key pad input data.

2. Write HDL code to control speed, direction of DC and Stepper motor.

3. Write HDL code to accept 8 channel Analog signal, Temperature sensors and display the data on LCD panel or Seven segment display.

4. Write HDL code to generate different waveforms (Sine, Square, Triangle, Ramp etc.,) using DAC change the frequency and amplitude.

5. Write HDL code to simulate Elevator operations
6. Write HDL code to control external lights using relays.

2

D_1V_9

H. O. D.
Dept. Of Electronics & Communication
Alva' Institute of Engg. Technology
Mijar, MOODBIDRI - 574 22.