MICROPROCESSO			LEKS		
As per Choice Ba	sed Credit Syst	em (CBCS) scheme]			
(Effective from		/ear 2016 -2017)			
	SEMESTER	AND DESCRIPTION OF THE PARTY OF	20	and the second second	
Subject Code	15CS44	IA Marks	20		
Number of Lecture Hours/Week	04	Exam Marks		80	
Total Number of Lecture Hours	50	Exam Hours	03		
	CREDITS -	· 04			
Course objectives: This course will end	able students to		Laslanaante	alles	
Make familiar with importance	and applications	of microprocessors and	microcontro	oners	
 Expose architecture of 8086 min 		ARM processor			
 Familiarize instruction set of A 	RM processor			Teaching	
Module 1				Hours	
	0.11	or Carlle Incide the	8088/86	10 Hours	
The x86 microprocessor: Brief his	story of the x	86 family, fiside the	The Stack	10 11001	
Introduction to assembly programming	g, introduction	o Program Segments,	rectives &		
Flag register, x86 Addressing Modes. a Sample Program, Assemble, Link &	Assembly lange	More Sample program	s. Control		
Transfer Instructions, Data Types a	nd Data Defi	nition Full Segment	Definition,		
Flowcharts and Pseudo code.	uid Data Dem	ation, run sogmen			
Text book 1: Ch 1: 1.1 to 1.7, Ch 2: 2	2.1 to 2.7				
	10 2				
Module 2 x86: Instructions sets description, Ar	ithmetic and le	gic instructions and	programs:	10 Hour	
Unsigned Addition and Subtraction.	Unsigned Mu	ltiplication and Divisi	ion, Logic		
Instructions BCD and ASCII conversi	ion, Rotate Insti	uctions. INT 21H and	INI IOH		
Programming: Bios INT 10H Progra	umming , DOS	nterrupt 21H. 8088/86	Interrupts,		
x86 PC and Interrupt Assignment.					
Text book 1: Ch 3: 3.1 to 3.5, Ch 4: 4	.1 , 4.2 Chapter	· 14: 14.1 and 14.2			
Module 3					
Signed Numbers and Strings: Signed number Arithmetic Operations, String operations.				10 Hour	
Memory and Memory interfacing:	Memory address	decoding, data integri	ty in KAM		
and ROM, 16-bit memory interfacing.	8255 I/O prog	ramming: I/O address	es MAP of		
v86 PC's programming and interfacing	g the 8255.				
Text book 1: Ch 6: 6.1, 6.2. Ch 10: 10	0.2, 10.4, 10.5.	Ch 11: 11.1 to 11.4			
Module 4		TI D	TCC design	10 Hou	
Microprocessors versus Microcontroll	ers, ARM Emb	edded Systems : The K	Embedded	10 Hou	
philosophy, The ARM Design Philo	sophy, Embedo	led System Flardware,	rom Statue		
System Software, ARM Processor Fu	indamentals:	kegisters, Current Prop	one		
Register, Pipeline, Exceptions, Interru	pts, and the vec	for Table, Cole Extensi	Olis		
Text book 2:Ch 1:1.1 to 1.4, Ch 2:2.1	10 2.5				
Module 5 Introduction to the ARM Instruct	ion Sat . Data	Processing Instruction	s Branch	10 Hou	
Introduction to the ARM Instructions, Software Interrupt Inst	ton Set : Data	am Status Register I	nstructions.	10 1104	
Instructions, Software Interrupt Inst	atenta Cimple	an Samo Register			
Commentary I and inc Com		ILIDALATION OVER CYCLOSES.			
Coprocessor Instructions, Loading Cor Text book 2: Ch 3:3.1 to 3.6 (Exclud	ling 3.5.21	orogramming exercises.			

- Differentiate between microprocessors and microcontrollers
- Design and develop assembly language code to solve problems
- Gain the knowledge for interfacing various devices to x86 family and ARM processor
- Demonstrate design of interrupt routines for interfacing devices

Graduate Attributes

- Engineering Knowledge
- Problem Analysis
- Design/Development of Solutions

Question paper pattern:

The question paper will have ten questions.

There will be 2 questions from each module.

Each question will have questions covering all the topics under a module.

The students will have to answer 5 full questions, selecting one full question from each module.

Text Books:

- Muhammad Ali Mazidi, Janice Gillispie Mazidi, Danny Causey, The x86 PC Assembly Language Design and Interfacing, 5th Edition, Pearson, 2013.
- 2. ARM system developers guide, Andrew N Sloss, Dominic Symes and Chris Wright, Elsevier, Morgan Kaufman publishers, 2008.

Reference Books:

- 1. Douglas V. Hall: Microprocessors and Interfacing, Revised 2nd Edition, TMH, 2006.
- 2. K. Udaya Kumar & B.S. Umashankar: Advanced Microprocessors & IBM-PC Assembly Language Programming, TMH 2003.
- Ayala: The 8086 Microprocessor: programming and interfacing 1st edition, Cengage Learning
- 4. The Definitive Guide to the ARM Cortex-M3, by Joseph Yiu, 2nd Edition, Newnes, 2009
- 5. The Insider's Guide to the ARM7 based microcontrollers, Hitex Ltd.,1st edition, 2005
- 6. ARM System-on-Chip Architecture, Steve Furber, Second Edition, Pearson, 2015
- Architecture, Programming and Interfacing of Low power Processors- ARM7, Cortex-M and MSP430, Lyla B Das Cengage Learning, 1st Edition

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