

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 11/11/2020

Circular

It is hereby informed to III, V and VII semester students of Electronics & Communication Engineering that I-IA will be held from **17/11/20 to 19/11/20**. IA time table & syllabus are displayed on the Notice Board.

IA Coordinators

1. Mr. Yuvaraj T
2. Mrs. Nishma



Dr. D. V. Manjunatha

**Professor & Head
H. O. D.**

**Dept. Of Electronics & Communication
Alva's Institute of Engg. & Technology
Mijar, MOODBIDRI - 574 225**

III-A

III-B

V-A

VII - A

VII - B

Department of Electronics and Communication Engineering

I-IA TIME TABLE NOVEMBER-2020

Date	Time	III Sem	V Sem	VII Sem
17/11/20	09:30am To 11:00am	Mathematics - II	Technological Innovation Management and Entrepreneurship	Microwave and Antenna
	03:00pm To 04:30pm	Network Theory	Digital Signal Processing	Digital Image Processing
18/11/20	09:30am To 11:00am	Electronics Devices	Principles of Communication Systems	Power Electronics
	03:00pm To 04:30pm	Digital System Design	Information Theory & Coding	Real Time Systems
19/11/20	09:30am To 11:00am	Computer Organization and Architecture	Electromagnetic Waves	DSP Algorithm and Architecture
	03:00pm To 04:30pm	Power Electronics and Instrumentation	Verilog HDL	

IA Coordinators

Mr. Yuvaraj T 

Mrs. Nishma 



Dr. D.V. Manjunatha
Professor & Head

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

15.12.2020

Circular

It is hereby informed all the 3rd, 5th and 7th semester students that IInd Internal Assessments will be conducted from 21/12/2020 to 23/12/2020.

IA Coordinators

1. Mr. Yuvraj T
2. Mrs. Nishma



Dr. D V Manjunatha
Professor & Head



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II-IA TIME TABLE DECEMBER-2020

Date	Time	III Sem	V Sem	VII Sem
21/12/20	09:30am To 11:00am	Mathematics - II	Technological Innovation Management and Entrepreneurship	Microwave and Antenna
	03:00pm To 04:30pm	Network Theory	Digital Signal Processing	Digital Image Processing
22/12/20	09:30am To 11:00am	Power Electronics and Instrumentation	Principles of Communication Systems	Power Electronics
	03:00pm To 04:30pm	Electronics Devices	Information Theory & Coding	Real Time Systems
23/12/20	09:30am To 11:00am	Digital System Design	Electromagnetic Waves	DSP Algorithm and Architecture
	03:00pm To 04:30pm	Computer Organization and Architecture	Verilog HDL	

IA Coordinators

Mr. Yuvaraj T 
Mrs. Nishma 



Dr. D.V. Manjunatha
Professor & Head

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING


Date: 08/01/2021

Circular

It is hereby informed to III, V and VII semester students of Electronics & Communication Engineering that III-IA will be held from **13/01/21 to 16/01/21**. IA time table & syllabus are displayed on the Notice Board.

IA Coordinators

1. Mr. Yuvaraj T
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III-A

III-B

V-A

VII - A

VII - B

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III-IA TIME TABLE JAN-21

Date	Time	V Sem	VII Sem
13/01/21	9:30am To 11:00am	Technological Innovation Management and Entrepreneurship	Microwave and Antenna
	3:00pm To 4:30pm	Digital Signal Processing	Digital Image Processing
15/01/21	9:30am To 11:00am	Principles of Communication Systems	Power Electronics
	3:00pm To 4:30pm	Information Theory & Coding	Real Time Systems
16/01/21	9:30am To 11:00am	Electromagnetic Waves	DSP Algorithm and Architecture
	3:00pm To 4:30pm	Verilog HDL	

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Mrs. Nishma

D.V.V.

Dr. D.V. Manjunatha
Professor & Head