

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 10/04/2020

Circular

It is hereby informed to all students that Ist IA Test is scheduled from 17/04/2020 to 19/04/2020. **The test will be conducted through online mode due to COVID-19 Lock down.**



Dr. D. V. Manjunatha
Professor & Head



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

First IA -Time Table of ONLINE EXAM

DATE	TIME	IV-SEMESTER	VI-SEMESTER	VIII-SEMESTER
17/04/2020 (THURSDAY)	9.30AM TO 10.30AM	Complex Analysis, Probability and Statistical Methods	Digital Communication	Wireless Cellular and LTE 4G & Broadband
	3.00PM TO 4.00PM	Analog circuits	ARM Microcontroller & Embedded Systems	Fiber optics and network
18/04/2020 (FRIDAY)	9.30AM TO 10.30AM	Control system	VLSI Design	Network and cyber security
	3.00PM TO 4.00PM	Engineering Statistics & Linear Algebra	Computer Communication Networks	
19/04/2020 (SATURDAY)	9.30AM TO 10.30AM	Signals & Systems	Digital Switching System	
	3.00PM TO 4.00PM	Microcontroller	DSD Using VERILOG	

Dr. D V Manjunatha

HOD

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 12/05/2020

Circular

It is hereby informed to all students that IInd IA Test is scheduled from 19/05/2020 to 21/05/2020. **The test will be conducted through online mode due to COVID-19 Lock down.**



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Second IA -Time Table of ONLINE EXAM

DATE	TIME	IV-SEMESTER	VI-SEMESTER	VIII-SEMESTER
19/05/2020 (MONDAY)	9.30AM TO 10.30AM	Complex Analysis, Probability and Statistical Methods	Digital Communication	Wireless Cellular and LTE 4G & Broadband
	3.00PM TO 4.00PM	Analog circuits	ARM Microcontroller & Embedded Systems	Fiber optics and network
20/05/2020 (FRIDAY)	9.30AM TO 10.30AM	Control system	VLSI Design	Network and cyber security
	3.00PM TO 4.00PM	Engineering Statistics & Linear Algebra	Computer Communication Networks	
21/05/2020 (WEDNESDAY)	9.30AM TO 10.30AM	Signals & Systems	Digital Switching System	
	3.00PM TO 4.00PM	Microcontroller	DSD Using VERILOG	

Dr. D V Manjunatha

HOD

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 05/06/2020

Circular

It is hereby informed to all students that **III- IA Test** is scheduled from 12/06/2020 to 14/06/2020. **The test will be conducted through online mode due to COVID-19 Lock down.**



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Third IA -Time Table of ONLINE EXAM

DATE	TIME	IV-SEMESTER	VI-SEMESTER	VIII-SEMESTER
12/06/2020 (THURSDAY)	9.30AM TO 10.30AM	Complex Analysis, Probability and Statistical Methods	Digital Communication	Wireless Cellular and LTE 4G & Broadband
	3.00PM TO 4.00PM	Analog circuits	ARM Microcontroller & Embedded Systems	Fiber optics and network
13/06/2020 (TUESDAY)	9.30AM TO 10.30AM	Control system	VLSI Design	Network and cyber security
	3.00PM TO 4.00PM	Engineering Statistics & Linear Algebra	Computer Communication Networks	
14/06/2020 (SATURDAY)	9.30AM TO 10.30AM	Signals & Systems	Digital Switching System	
	3.00PM TO 4.00PM	Microcontroller	DSD Using VERILOG	

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