DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 13/09/2019

Circular

It is hereby informed to III, V and VII semester students of Electronics & Communication Engineering that I-IA will be held from **19/09/19** to **21/09/19**. IA time table & syllabus are displayed on the Notice Board.

Students are informed to submit the blue books to the respective subject faculties by 16/09/19.

IA Coordinators
1. Mr.Yuvaraj T
2. Mrs.Nishma

Dr. D. V. Manjunatha
Professor & Head

III-A

V-A

V-B

VII - A

VII - B

Department of Electronics and Communication Engineering

I-IA TIME TABLE SEP-2019

Date	Time	III Sem	V Sem	VII Sem
19/09/19	09:30am To 11:00am	Engineering Mathematics III	Management and Entrepreneurship	Microwave and Antennas
	03:00pm To 04:30pm	Network Theory	Digital Signal Processing	Digital Image Processing
20/09/19	09:30am To 11:00am	Electronics Devices	Verilog HDL	Power Electronics
	03:00pm To 04:30pm	Digital System Design	Information theory and Coding	Real Time Systems
21/09/19	09:30am To 11:00am	Computer Organization And Architecture	Operating System	DSP Algorithms and Architecture
	03:00pm To 04:30pm	Power Electronics and Instrumentation	Object Oriented Programming using C++	

IA Coordinators
Mr. Yuvaraj T
Mrs. Nishma

Dr. D.V. Manjunatha
Professor & Head

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 21/10/2019

Circular

It is hereby informed to III, V and VII semester students of Electronics & Communication Engineering that II-IA will be held from **24/10/19 to 26/10/19**. IA time table & syllabus are displayed on the Notice Board.

IA Coordinators
1. Mr.Yuvaraj T
2. Mrs.Nishma

Dr. D. V. Manjunatha
Professor & Head

III-A —

III-B

V-A —

V-B —

VII - A —

VII - B —

Alva's Institute of Engineering & Technology
Shobhavana Campus, Mijar, Moodbidri, D.K – 574225
Phone: 08258-262725, Fax: 08258-262726

Department of Electronics and Communication Engineering
II-IA TIME TABLE OCT-2019

Date	Time	III Sem	V Sem	VII Sem
24/10/19	09:30am To 11:00am	Engineering Mathematics III	Management and Entrepreneurship	Microwave and Antennas
	03:00pm To 04:30pm	Network Theory	Digital Signal Processing	Digital Image Processing
25/10/19	09:30am To 11:00am	Electronics Devices	Verilog HDL	Power Electronics
	03:00pm To 04:30pm	Digital System Design	Information theory and Coding	Real Time Systems
26/10/19	09:30am To 11:00am	Computer Organization And Architecture	Operating System	DSP Algorithms and Architecture
	03:00pm To 04:30pm	Power Electronics and Instrumentation	Object Oriented Programming using C++	

IA Coordinators
Mr. Yuvaraj T
Mrs. Nishma

Dr. D.V. Manjunatha
Professor & Head

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Date: 23/11/2019

Circular

It is hereby informed to III, V and VII semester students of Electronics & Communication Engineering that III-IA will be held from **26/11/19 to 28/11/19**. IA time table & syllabus are displayed on the Notice Board.

IA Coordinators
1. Mr.Yuvaraj T
2. Mrs.Nishma

Dr. D. V. Manjunatha
Professor & Head

III-A

III-B

V-A

V-B

VII - A

VII - B

Alva's Institute of Engineering & Technology
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Phone: 08258-262725, Fax: 08258-262726

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Department of Electronics and Communication Engineering

III-IA TIME TABLE NOV-2019

Date	Time	III Sem	V Sem	VII Sem
26/11/19	09:30am To 11:00am	Engineering Mathematics III	Management and Entrepreneurship	Microwave and Antennas
	03:00pm To 04:30pm	Network Theory	Digital Signal Processing	Digital Image Processing
27/11/19	09:30am To 11:00am	Electronics Devices	Verilog HDL	Power Electronics
	03:00pm To 04:30pm	Digital System Design	Information theory and Coding	Real Time Systems
28/11/19	09:30am To 11:00am	Computer Organization And Architecture	Operating System	DSP Algorithms and Architecture
	03:00pm To 04:30pm	Power Electronics and Instrumentation	Object Oriented Programming using C++	

IA Coordinators,
Mr. Yuvaraj T
Mrs. Nishma

Dr. D.V. Manjunatha
Professor & Head