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| **Sl. No** | **Syllabus** | **Curriculum** | **Deployment Strategy and****Tool** | **Cross-cutting issues****integrated** | **PO, PSO and CO** | **Attainments** | **Attainment Verification** |
| 1. | ANALOG AND DIGITAL ELECTRONICS LABORATORY | A student who successfully fulfills the course requirements will have demonstrated: 1. An ability to operate laboratory equipment. 2. An ability to construct, analyze, and troubleshoot simple combinational and sequential circuits. 3. An ability to design and troubleshoot a simple state machine. 4. An ability to measure and record the experimental data, analyze the results, and prepare a formal laboratory report. | 1. Chalk and

Talk method1. PPT
 | * Business

 Ethics | PO1:Engineering KnowledgePO2:Problem AnalysisPO3:Design/Development Of SolutionsPO4:Conduct Investigations Of Complex ProblemsPO5:Modern Tool UsagePO6: Engineer and SocietyPO7:Environment And SustainabilityPO9:INDIVIDUAL AND TEAM WORKPO10:COMMUNICATIONPO11:Project Management and Finance. PO12: Life-longLearning. |  |  |
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|  |  | PSO1:Professional SkillsPSO2:Problem Solving Skill |  |
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|  |  | CO1:Classify and List various Electronic Devices like Cathode ray Oscilloscope, Signal generators, Digital Trainer Kit, Multimeter and components like Resistors, Capacitors, Op amp and Integrated Circuit. Explain Verilog HDL concept using IT tools. CO2:Make use of the simulation package to study analog and digital circuits behaviour with the help of modern tools using various mapping and mathematical methods. CO3:Interpret various analog circuits using IC’s and digital logic circuits using basic gates in the domain of economy and examine the performance and efficiency.CO4:Design and implement various types of counters using Flip-flops within the realm of economic, performance, efficiency, user friendly and environmental constraints.CO5:Explain the working of converter and analyze its performance and implement ALU in the domain of efficiency. |  |
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