

(Unit of Alva's Education Foundation (R), Moodbidri)

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Shobhavana Campus, MIJAR-574225, Moodbidri, D.K., Karnataka

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DEPARTMENT OF
ELECTRONICS



COMMUNICATION ENGINEERING

(Unit of Alva's Education Foundation (R), Moodbidri) Shobhavana Campus, MIJAR-574225, Moodbidri, D.K., Karnataka S Affiliated to VTU, Belagavi & Approved by AICTE New Delhi. Recognized by Govt. of Karnataka.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING **ACADEMIC YEAR 2023-24**

Report on 4 Weeks Internship Training Program

On

CADENCE AND LabVIEW







Mr. Vivek Alva

Managing Trustee, AEF

Time

9:30 am

9.40 am

950 am

10:00 am

10:05 am

10:10 am

10:20 am



Program Schedule

Program

Lighting of Lamp

Welcoming the Gathering

Dr Siddesh G K

Briefing of Programme

Dr D V Manjunatha Addressing the Gathering

Dr Peter Fernandes Presidential Address .

Mr Vivek Alva

Unveiling Alva's LED Bulbs and

Tube Lights

Vote of Thanks

Dr Ganesh V N

Dr. Peter Fernandes

Principal ,AIET , Mijar

Alva's Institute of Engineering and Technology, Mijar, D.K Department Of Electronics and Communication Engineering

CORDIALLY INVITES FOR THE INAUGURATION OF

INTERNSHIP PROGRAMME

CADENCE AND LABVIEW

- for next generation electronics

UNVEILING ALVA'S LED BULBS AND TUBE LIGHTS





In the Gracious Presence

Mr. Vivek Alva Managing Trustee, AEF

Principal ,AIET , Mijar

Venue: MBA Seminar Hall, AIET Date: 27/10/2023

Timings: 9:30 AM to 10:30 AM

Dr. Peter Fernandes

Invitation of Inauguration of Internship Programme on CADENCE and Labview



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ACADEMIC YEAR 2023-24

4 WEEK INTERNSHIP TRAINING SCHEDULE

S S	Date	9:00 to 10:45am	10:45 to 11:10am (Break)	11:10 to 12:45Pm	12:45 to 1:45PM (Lunch Break)	1:45 to 3:20Pm	3:20 to 3:30PM (Break)	3:30 to 4:39 pm
1	27-10-2023	Inaugural function		Dr. D.V Manjonatha		Dr. Siddesh G K		Dr. Siddesh G K
7	28-10-2023	Mr. Napolean A		Mr. Sudhakara H M				
3	29-10-2023			Su	Sunday			
4	30-10-2023	Dr. Ganesh V N		Mr. Sudhakara H M		Dr. Ganesh V N		Dr. Ganesh V N
5	31-10-2023	Mr. Sudhakara H M		Dr. Ganesh V.N.		Dr. D	Dr. D.V Manjunatha	itha
9	01-11-2023			Kannada	Kannada Rajostava			
7	02-11-2023	Design	of Electronic	Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara	gn Using Ca	dence by Dr. Srinivas	sa Rao Udar	r,
∞	03-11-2023	Design	of Electronic	Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara	gn Using Ca	dence by Dr. Sriniva	sa Rao Udar	e.
6	04-11-2023	Design	of Electronic	Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara	gn Using Ca	dence by Dr. Sriniva	sa Rao Udar	g.
10	05-11-2023			Su	Sunday.			
111	06-11-2023			LABVIEW by Dr. Ravikumar	Dr. Raviku	mar		
12	07-11-2023			LABVIEW by Dr. Ravikumar	Dr. Raviku	mar		
13	08-11-2023			LABVIEW by Dr. Ravikumar	Dr. Raviku	mar		
14	09-11-2023			LABVIEW by Dr. Ravikumar	Dr. Raviku	mar	, pl	
15	10-11-2023			LABVIEW by Dr. Ravikumar	Dr. Raviku	mar		
91	11-11-2023			LABVIEW by Dr. Ravikumar and Dr. Napolean A	cumar and I	r. Napolean A		



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	28 23-11-2023 Report Preparation	27 22-11-2023 Industrial Visit to Karmic Design Pvt. Ltd, Manipal	207-11-01		17-11-2023 Design of Electronics Circuits, Physical Design Using Cadence by Dr. Crinivasa Boo Habita	Dr. Ganesh V N Dr. Ganesh V N Dr. Ganesh V N	Dr. Ganesh V N			12-11-2023 13-11-2023 14-11-2023 16-11-2023 17-11-2023 18-11-2023 19-11-2023 20-11-2023 21-11-2023 22-11-2023
	24-11-2023					Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara Sunday Blockchain & its Application by Prof. Lohith J J Blockchain & its Application by Prof. Lohith J J Report Preparation Presentation	Dr. Ganesh V N Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara Sunday Blockchain & its Application by Prof. Lohith J J Blockchain & its Application by Prof. Lohith J J Report Preparation Report Preparation Presentation	Dr. Ganesh V N Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara Sunday Blockchain & its Application by Prof. Lohith J J Blockchain & its Application by Prof. Lohith J J Report Preparation Presentation Presentation	Dr. Ganesh V N Dr. Ganesh V N Dr. Ganesh V N Dr. Ganesh V N Dr. Ganesh V N Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara Sunday Sunday Sunday Blockchain & its Application by Prof. Lohith J J Blockchain & its Application by Prof. Lohith J J Report Preparation Presentation	24-11-2023
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Dept. Internship training Co-ordinator Mrs. Bhagyashree K

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Session of the Internship Training by In-House faculty

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Date	Timings	Faculty	Topics
27-10-2023	11:10AM-12:45PM	Dr. D V Manjunatha	Combinational Circuits
10 2023	1:45 PM-4:30PM	Dr. Siddesh G K	Introduction to Digital Electronics
78 10 2023	9:00AM-10:45AM	Dr. Napolen A	Advanced Memory Devices
70-10-7073	11:10AM-12:45PM	Mr. Sudhakara H M	Overview on Analog Programming
	9:00AM-10:45AM	Dr. Ganesh V N	Semiconductor Technology and its recent trends
30-10-2023	11:10AM-12:45PM	Mr. Sudhakara H M	Analog and Power Devices
71	1:45 PM-4:30PM	Dr. Ganesh V N	Hands on training using Multisim
		Mr. G. Halana H.M.	Digital system design and Verification using
	9:00AM-10:45AM	Mr. Sudhakara H M	Verilog
31-10-2023	11:10AM-12:45PM	Dr. Ganesh V N	Hands on training using Multisim
	1.45 PM-4:30PM	Dr. D V Manjunatha	Introduction to CMOS Design
	9:00AM-10:45AM		Semiconductor manufacturing process
15 11 2022	11.10AM-17.45PM	Dr. Ganesh V N	Introduction to MATLAB
13-11-2023	1.45 PM_4.30PM		Certification course on MATLAB ONRAMP
	L'17 LIM III CL'I		Cartification course on MATLAB Circuit
16-11-2023	9:00AM-4:30PM	Dr. Ganesh V N	simulation and Signal processing

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Aiva's Institute of Enon. & Transment

Dept. Internship Kraining Co-ordinator Mrs. Bhagyashree K

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Inauguration ceremony of the 4 Weeks Internship Training Program

The Internship training begun with a formal inaugural ceremony at MBA seminar hall, AIET on 27th October 2023at 9:30am in the presence of Dr. Siddesh G K, HOD, Department of ECE, AIET and internship was coordinated by Dr. D V Manjunatha, Professor, Dept. of ECE, AIET. The inaugural program proceeded with lighting the lamp and the presidential address by Dr. Siddesh G.K, where he emphasized on the scope of attending such Internships to update their skills on current technologies. Total 44 interested students of 5th semester ECE were registered for this program. Dr. D.V Manjuntha, Professor, Dept. of ECE, briefed about the internship program and informed the students to actively participate and gain the maximum knowledge. The program was hosted by Mr. Nagarjuna, final year student, Dept. of ECE. Finally, Dr. Ganesh V N, Assoc. Professor Department of ECE proposed vote of thanks.



Inaugural function of Internship training program

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Internship Training by In-house Faculty Members

Date: 27/10/2023 to 31/10/2023 and 15/11/2023 to 16/11/2023

The in-house academic members reviewed the fundamentals with the students as a requirement for internship training on the topics Combinational Circuits, Introduction to Digital Electronics, Introduction to Digital Electronics, Advanced Memory Devices, Overview on Analog Programming, Semiconductor Technology and its recent trends, Analog and Power Devices, Hands on training using Multisim, Digital system design and Verification using Verilog, Hands on training using Multisim, Introduction to CMOS Design, Semiconductor manufacturing process, Introduction to MATLAB, Certification course on MATLAB ONRAMP, Certification course on MATLAB Circuit simulation and Signal processing from 27/10/2023 to 31/10/2023 and 15/11/2023 to 16/11/2023.



Training by Mr. Sudhakara H M – Inhouse Faculty



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Topic: "Design of Electronic Circuits, Physical Design Using Cadence"

Resource Person : Dr. Srinivasa Rao Udara,

Associate Professor, Dean Research

Department of Electronics & Communication

S.T.J Institute of Technology, Ranebennur.

Date : 02.11.2023 to 04.11.2023 and 17.11.2023 to 18.11.2023

The training session conducted by Dr. Srinivasa Rao Udara provided a comprehensive understanding of VLSI design, with a focus on electronic circuits and physical design using Cadence tools. The session covered a range of key topics essential for participants to gain insights into the rapidly evolving field of VLSI design. Dr. Udara initiated the session by explaining the foundations of Very Large Scale Integration (VLSI) and its pivotal role in shaping the future of electronics. Emphasis was placed on how VLSI enables the development of smaller, more powerful, and energyefficient integrated circuits to meet the growing demands of various industries. The training delved into the fundamental concepts of FPGA, CPLD, PLC, DSP, and Microcontrollers. Dr. Udara provided a clear understanding of each concept, including their applications and distinctive features. This foundational knowledge equipped participants to discern the suitability of these concepts for different design scenarios. Dr. Udara emphasized the importance of the Universal Verification Methodology (UVM) in VLSI design. Participants learned about UVM as a standardized approach for creating modular and reusable verification environments, streamlining the verification process. Dr. Udara provided a detailed overview of Cadence Tool, underscoring its significance in the VLSI design flow. Participants gained insights into the capabilities, features, and applications of Cadence tools, both in the digital and analog design domains. Practical application of knowledge was demonstrated through simulations of essential components such as counters, NAND gates, adders, and multiplexers. This hands-on approach allowed participants to validate the functionality of these components before proceeding further in the design process. The traditional synthesis procedure was explained, outlining the transformation of a high-level hardware description into a gate-level netlist. This step was highlighted as a crucial phase in transitioning from conceptual design to a more

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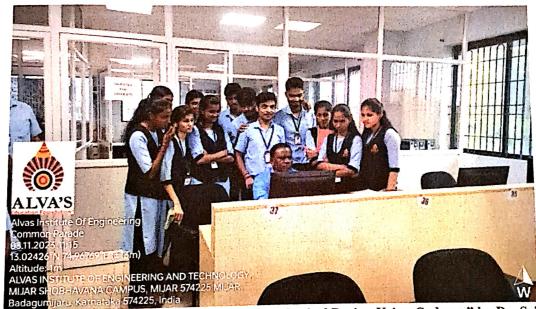
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concrete representation suitable for physical implementation. The training touched upon synthesis using a shortcut method, indicating a more optimized or automated approach in the synthesis process. This could involve leveraging pre-optimized IP blocks or design patterns to enhance the efficiency of the design flow. The session concluded with a comprehensive discussion on physical design, emphasizing the conversion of the synthesized netlist into a physical layout. The role of graphical design tools, presumably within the Cadence suite, was underscored for visualizing and manipulating circuit elements effectively.

In summary, Dr. Srinivasa Rao Udara's training session provided participants with a well-rounded understanding of VLSI design, covering theoretical concepts, practical simulations, and insights into essential tools and methodologies. The session equipped participants with the knowledge and skills necessary to navigate the intricate landscape of electronic circuit design using Cadence tools.



Internship Training on "Design of Electronic Circuits, Physical Design Using Cadence" by Dr. Srinivasa Rao
Udara

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Internship Training onDesign of Electronic Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara

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Topic: "Lab View"

Resource Person

: Dr. Ravikumar A. V,

Associate Professor

Department of Electronics & Communication

S.J.B.T Institute of Technology, Bangalore.

Date

: 06/11/2023 to 11/11/2023

The LabVIEW training conducted by Dr. Ravikumar provided a comprehensive understanding of LabVIEW software and its applications. The training covered both theoretical concepts and practical hands-on sessions to ensure a holistic learning experience.

Topics covered

1. Lab View Basics

- Introduction to LabVIEW environment.
- Understanding the Front Panel and Block Diagram panels.
- Overview of Controls and Indicators.

2. Controls and Indicators

- Numeric Control and Indicator.
- Boolean Control and Indicator.
- String Control and Indicator.
- Data Containers.
- Graphs.
- Enums.
- Decorations.

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3. Built-In Functions

- Structures for control flow
- Arrays and their applications.
- Clusters for data organization
- Numeric, Boolean, and String functions.
- Comparison functions.
- Timing functions.
- Mathematical operations.

4. Add-On Tools

- Integration with Maker Hub.
- Deployment of LabVIEW code to Arduino UNO.

5. Arduino Integration

- Basics of interfacing LabVIEW with Arduino.
- LED Blinking example.
- Working with various sensors.
- Building complete projects using LabVIEW and Arduino.

Participants applied the knowledge gained through the training to develop practical projects. These projects ranged from simple LED blinking exercises to more complex applications involving sensor integration. The training emphasized hands-on experience, enabling participants to confidently use LabVIEW for real-world applications.

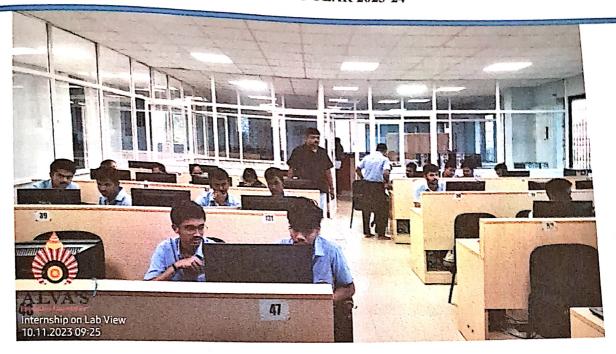
Dr. Ravikumar's LabVIEW training successfully equipped participants with the skills and knowledge necessary to work with LabVIEW in various engineering and scientific applications. The combination of theoretical insights, practical demonstrations, and hands-on projects contributed to a well-rounded learning experience.

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Hands on session in Lab View by Dr. Ravikumar A.V

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ACADEMIC YEAR 2023-24

Topic: Industrial Visit to Karmic Design Pvt. Ltd, Manipal

Date: 22/11/2023

About Karmic Design Pvt. Ltd

Founded in 1999, KarMic is a pioneer and leader in the semiconductor design and development industry. They have expertise in Analog, Radio Frequency, Mixed Signal, Memory and High Performance Digital domains; providing design services and solutions globally. Company was started by two visionaries, Mr. Mark Harward and Dr. ShivalingMahantShetti in the year 1999. Today, KarMic Design has workforce of 500+ employees. The company was established with the below founding principles:

- Enable members to continually improve their knowledge to enhance value of services and solutions being offered, and
- Demonstrate corporate social responsibility enabling society to benefit at local, state, national and global levels.

KarMic has multiple design centers located in Manipal, Bangalore, and Dallas.

Projects Handling by Karmik

- Modeling and simulation of entire chip functionality using HDL.
- Top level verification by co-simulation.
- Spec validation.
- Module-level verification of Analog/RF design.
- RTL verification.

About the industrial Visit

Students received a brief introduction to layout design for USB, wireless, and power management modules of the chip, which included drivers, data converters, PLLs, DLL, low drop out regulators, DCDC converters, reference generators, battery chargers, charge pumps, and filters.

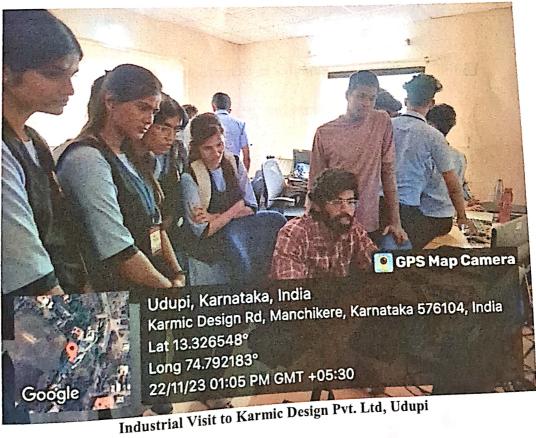
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING **ACADEMIC YEAR 2023-24**

Topic: "Blockchain and its Application"

Resource Person

: Prof. Lohith J J

Assistant Professor

Department of Computer Science and Engineering

B.M.S College of Engineering, Bangalore.

Date

: 20/11/2023 and 21/11/2023

Prof. Lohit J J delivered an engaging lecture on blockchain technology, focusing on fundamental concepts, the role of cryptocurrency, and practical applications. The presentation provided a comprehensive overview, emphasizing hands-on aspects such as the simulation of Bitcoin, creation of blocks, and the essential role of miners in the blockchain network.

Key Topics Covered

- 1. Introduction to Blockchain
 - Definition and core concepts of blockchain as a distributed ledger technology.
 - Emphasis on decentralization, transparency, and immutability.
- 2. Introduction to Cryptocurrency
 - Explanation of cryptocurrency as a digital or virtual form of currency.
 - Overview of cryptographic principles ensuring security in cryptocurrency transactions.

3. Simulation of Bitcoin:

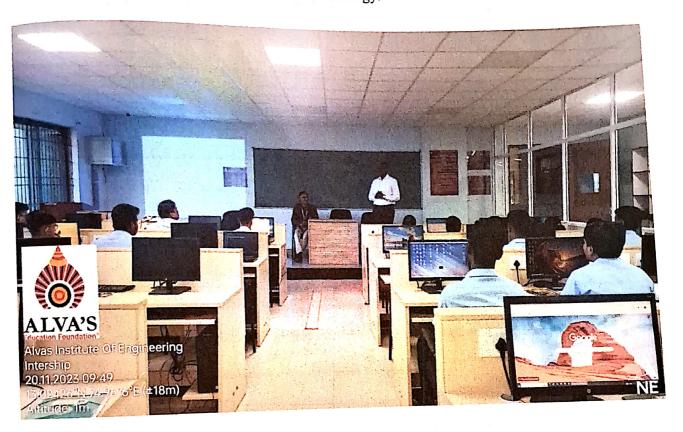
- Practical demonstration of a Bitcoin transaction using a simulated environment. Understanding the key components, including wallets, transactions, and blocks.

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ACADEMIC YEAR 2023-24

prof. Lohit J J's lecture provided a hands-on exploration of blockchain technology, offering participants valuable insights into the intricacies of Bitcoin simulation, block creation, and the vital role of miners. The presentation effectively bridged theoretical concepts with practical applications, enabling a holistic understanding of blockchain technology.



Internship training on Blockchain & its application by Prof. Lohith J J

Total number of Students benefited by the Internship training program: 44

Outcome: Students learned how to use Labview software and CADAENCE Tool.

Dept. Internship Training Cooklinator Mus. Bhagyashree.

Siddesh

Dept. Of Electronics & Communication Alva's Institute of Engg. & Technology Mijar, MOODBIĐRI - 574 228

INTERNSHIP CERTIFICATE SAMPLE



CERTIFICATE

OF INTERNSHIP



cādence







PROUDLY PRESENTED TO:

This is to certify that Student has successfully completed an Internship Programme gaining hands-on experience in Cadence, LabVIEW for next Generation Electronics, and Blockchain Technology, demonstrated exceptional skills and dedication in applying theoretical knowledge to real-time applications during October/November 2023 Organised by Department of Electronics & Communication Engineering.

Head of the Department Principal

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

4 Weeks Internship Training on Cadence and Labview

Attendance Sheet

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

4 Weeks Internship Training on Cadence and Labview

Attendance Sheet

#	USN	Student Name	00/44/22	09/11/23	10/11/23	4	11/11/23	15/11/23	3, 6	16/11/23		17/11/23	4.	18/11/23	20/11/23		21/11/23		22/11/23		23/11/23	24/11/23	25/11/23	
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Dept. Training Coordinator Mrs. Bhagyashree K HoD

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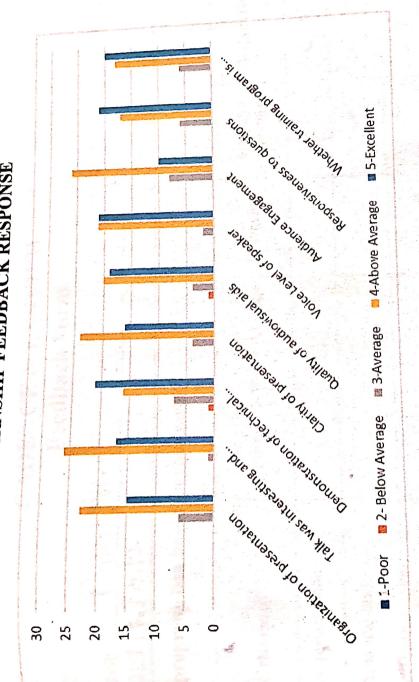
Dr. Siddesh G & Electronics & Communication
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4 WEEKS INTERNSHIP FEEDBACK RESPONSE



Dept. Internship Training Coordinator Mrs. Bhagyashree K

HoD

Dr. Siddesh GK ロらげ

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INTERNSHIP CERTIFICATE SAMPLE



CERTIFICATE

OF INTERNSHIP



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PROUDLY PRESENTED TO:

GONTHAM M.A

This is to certify that Student has successfully completed an Internship Programme gaining hands-on experience in Cadence, LabVIEW for next Generation Electronics, and Blockchain Technology, demonstrated exceptional skills and dedication in applying theoretical knowledge to real-time applications during October/November 2023 Organised by Department of Electronics & Communication Engineering.

Head of the Department

Principal

Alva's Institute Of Engineering and Technology

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