

ALVA'S INSTITUTE OF ENGINEERING & TECHNOLOGY

(Unit of Alva's Education Foundation (R), Moodbidri)

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Shobhavana Campus, MIJAR-574225, Moodbidri, D.K., Karnataka

Ph: 08258-262725; Mob: 722262724, 7026262725; mail: principal@iet08@gmail

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

5TH SEMESTER INTERNSHIP TRAINING REPORT

2023-24



ALVA'S INSTITUTE OF ENGINEERING & TECHNOLOGY

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC YEAR 2023-24

Report on 4 Weeks Internship Training Program

On

CADENCE AND LabVIEW



Alva's Institute of Engineering and Technology, Mijar, D.K
Department Of Electronics and Communication Engineering

**CORDIALLY INVITES
FOR THE INAUGURATION OF**

INTERNSHIP PROGRAMME on CADENCE AND LABVIEW

- for next generation electronics
&

UNVEILING ALVA'S LED BULBS AND TUBE LIGHTS



In the Gracious Presence

Mr. Vivek Alva
Managing Trustee, AEF

Dr. Peter Fernandes
Principal, AIET, Mijar

Venue : MBA Seminar Hall, AIET

Date : 27/10/2023

Timings : 9:30 AM to 10:30 AM



Mr. Vivek Alva
Managing Trustee, AEF



In the Gracious Presence

Dr. Peter Fernandes
Principal, AIET, Mijar

Program Schedule

Time	Program
9:30 am	Lighting of Lamp
9:40 am	Welcoming the Gathering Dr Siddesh G K
9:50 am	Briefing of Programme Dr D V Manjunatha
10:00 am	Addressing the Gathering Dr Peter Fernandes
10:05 am	Presidential Address - Mr Vivek Alva
10:10 am	Unveiling Alva's LED Bulbs and Tube Lights
10:20 am	Vote of Thanks Dr Ganesh V N


Invitation of Inauguration of Internship Programme on CADENCE and Labview

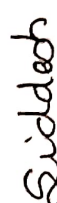


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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC YEAR 2023-24

17	12-11-2023	Sunday					
18	13-11-2023	Holiday(Deepavali)					
19	14-11-2023	Holiday(Deepavali)					
20	15-11-2023	Dr. Ganesh V N		Dr. Ganesh V N		Dr. Ganesh V N	Dr. Ganesh V N
21	16-11-2023	Dr. Ganesh V N		Dr. Ganesh V N		Dr. Ganesh V N	Dr. Ganesh V N
22	17-11-2023	Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara					
23	18-11-2023	Design of Electronics Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara					
24	19-11-2023	Sunday					
25	20-11-2023	Blockchain & its Application by Prof. Lohith J J					
26	21-11-2023	Blockchain & its Application by Prof. Lohith J J					
27	22-11-2023	Industrial Visit to Karnic Design Pvt. Ltd, Manipal					
28	23-11-2023	Report Preparation					
29	24-11-2023	Presentation					
30	25-11-2023	Presentation					


Dept. Internship Training Co-ordinator
Mrs. Bhagyashree K


H.O.D.
Dept. Of Electronics & Communication
Alva's Institute of Engg. & Technology
MIJAR, MOODBIDRI - 574 225

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ACADEMIC YEAR 2023-24

Session of the Internship Training by In-House faculty

Date	Timings	Faculty	Topics
27-10-2023	11:10AM-12:45PM	Dr. D V Manjunatha	Combinational Circuits
	1:45 PM-4:30PM	Dr. Siddesh G K	Introduction to Digital Electronics
28-10-2023	9:00AM-10:45AM	Dr. Napolen A	Advanced Memory Devices
	11:10AM-12:45PM	Mr. Sudhakara H M	Overview on Analog Programming
30-10-2023	9:00AM-10:45AM	Dr. Ganesh V N	Semiconductor Technology and its recent trends
	11:10AM-12:45PM	Mr. Sudhakara H M	Analog and Power Devices
	1:45 PM-4:30PM	Dr. Ganesh V N	Hands on training using Multisim
31-10-2023	9:00AM-10:45AM	Mr. Sudhakara H M	Digital system design and Verification using Verilog
	11:10AM-12:45PM	Dr. Ganesh V N	Hands on training using Multisim
	1:45 PM-4:30PM	Dr. D V Manjunatha	Introduction to CMOS Design
15-11-2023	9:00AM-10:45AM	Dr. Ganesh V N	Semiconductor manufacturing process
	11:10AM-12:45PM		Introduction to MATLAB
	1:45 PM-4:30PM		Certification course on MATLAB ONRAMP
16-11-2023	9:00AM-4:30PM	Dr. Ganesh V N	Certification course on MATLAB Circuit simulation and Signal processing



Dept. Internship Training Co-ordinator
Mrs. Bhagyashree K

Siddesh
H.D.D.

Dept. Of Electronics & Communication
Alva's Institute of Enon. & Tech. Moodbidri
MIJAR, MOODBIDRI, D.K., KARNATAKA

Inauguration ceremony of the 4 Weeks Internship Training Program

The Internship training begun with a formal inaugural ceremony at MBA seminar hall, AIET on 27th October 2023 at 9:30am in the presence of Dr. Siddesh G K, HOD, Department of ECE, AIET and internship was coordinated by Dr. D V Manjunatha, Professor, Dept. of ECE, AIET. The inaugural program proceeded with lighting the lamp and the presidential address by Dr. Siddesh G.K, where he emphasized on the scope of attending such Internships to update their skills on current technologies. Total 44 interested students of 5th semester ECE were registered for this program. Dr. D.V Manjuntha, Professor, Dept. of ECE, briefed about the internship program and informed the students to actively participate and gain the maximum knowledge. The program was hosted by Mr. Nagarjuna, final year student, Dept. of ECE. Finally, Dr. Ganesh V N, Assoc. Professor Department of ECE proposed vote of thanks.



Inaugural function of Internship training program



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Internship Training by In-house Faculty Members

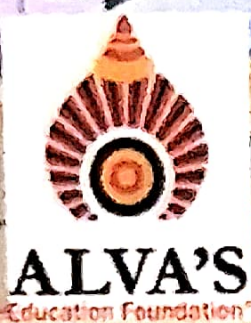
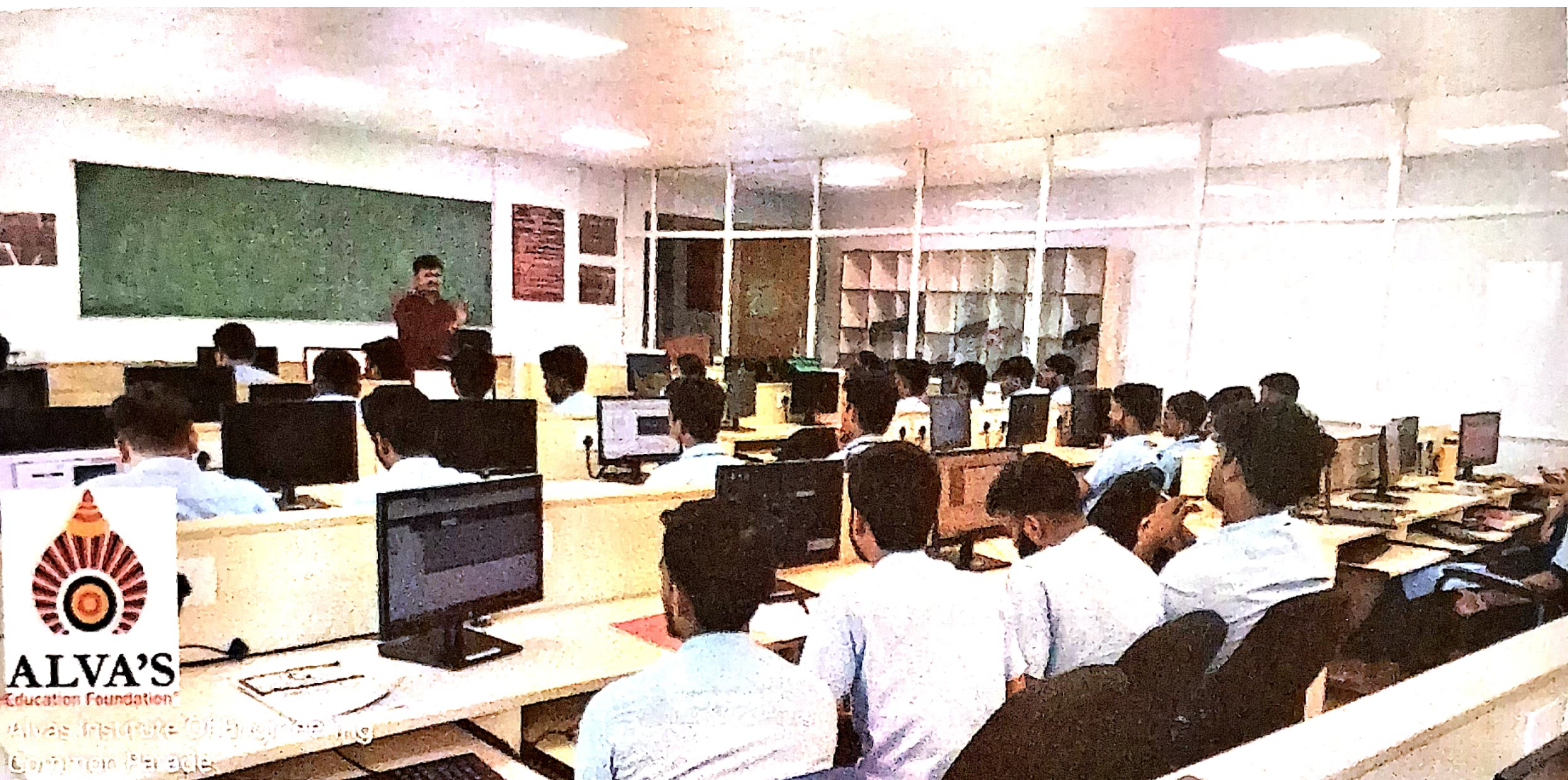
Date: 27/10/2023 to 31/10/2023 and 15/11/2023 to 16/11/2023

The in-house academic members reviewed the fundamentals with the students as a requirement for internship training on the topics Combinational Circuits, Introduction to Digital Electronics, Introduction to Digital Electronics, Advanced Memory Devices, Overview on Analog Programming, Semiconductor Technology and its recent trends, Analog and Power Devices, Hands on training using Multisim, Digital system design and Verification using Verilog, Hands on training using Multisim, Introduction to CMOS Design, Semiconductor manufacturing process, Introduction to MATLAB, Certification course on MATLAB ONRAMP, Certification course on MATLAB Circuit simulation and Signal processing from 27/10/2023 to 31/10/2023 and 15/11/2023 to 16/11/2023.



Alvas Institute of Engineering & Technology
Common Parade
28.10.2023 12:34
13.02425°N 74.96762°E (±16m)
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2XF9+M29, Badagumijaru, Karnataka 574225, India

Training by Mr. Sudhakara H M – Inhouse Faculty



Alva's Institute of Engineering
Commonwealth



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ACADEMIC YEAR 2023-24

Topic: "Design of Electronic Circuits, Physical Design Using Cadence"

Resource Person : Dr. Srinivasa Rao Udara,
Associate Professor, Dean Research

Department of Electronics & Communication

S.T.J Institute of Technology, Ranebennur.

Date : 02.11.2023 to 04.11.2023 and 17.11.2023 to 18.11.2023

The training session conducted by Dr. Srinivasa Rao Udara provided a comprehensive understanding of VLSI design, with a focus on electronic circuits and physical design using Cadence tools. The session covered a range of key topics essential for participants to gain insights into the rapidly evolving field of VLSI design. Dr. Udara initiated the session by explaining the foundations of Very Large Scale Integration (VLSI) and its pivotal role in shaping the future of electronics. Emphasis was placed on how VLSI enables the development of smaller, more powerful, and energy-efficient integrated circuits to meet the growing demands of various industries. The training delved into the fundamental concepts of FPGA, CPLD, PLC, DSP, and Microcontrollers. Dr. Udara provided a clear understanding of each concept, including their applications and distinctive features. This foundational knowledge equipped participants to discern the suitability of these concepts for different design scenarios. Dr. Udara emphasized the importance of the Universal Verification Methodology (UVM) in VLSI design. Participants learned about UVM as a standardized approach for creating modular and reusable verification environments, streamlining the verification process. Dr. Udara provided a detailed overview of Cadence Tool, underscoring its significance in the VLSI design flow. Participants gained insights into the capabilities, features, and applications of Cadence tools, both in the digital and analog design domains. Practical application of knowledge was demonstrated through simulations of essential components such as counters, NAND gates, adders, and multiplexers. This hands-on approach allowed participants to validate the functionality of these components before proceeding further in the design process. The traditional synthesis procedure was explained, outlining the transformation of a high-level hardware description into a gate-level netlist. This step was highlighted as a crucial phase in transitioning from conceptual design to a more



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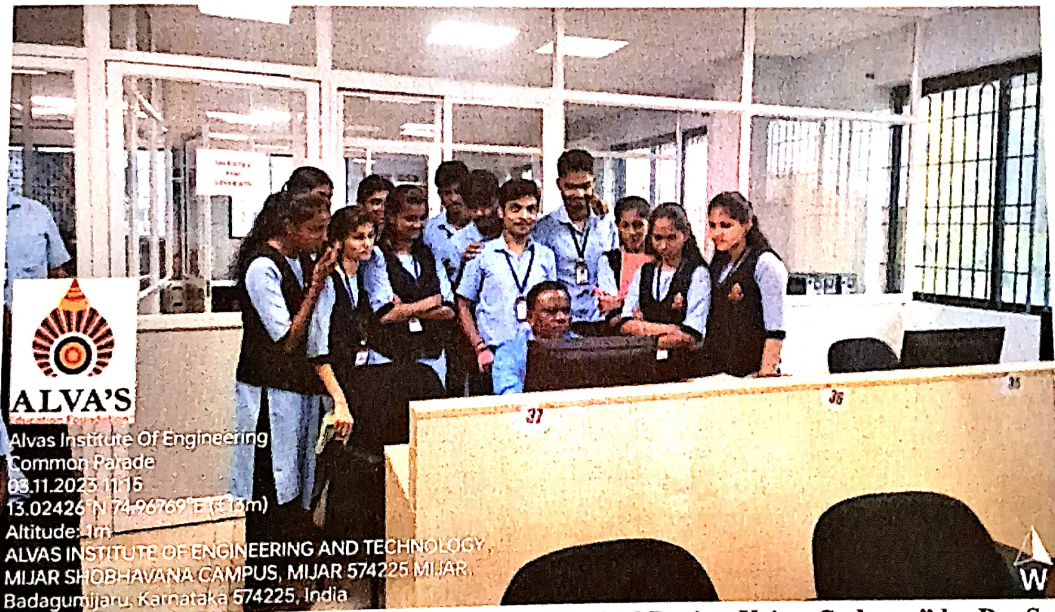
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concrete representation suitable for physical implementation. The training touched upon synthesis using a shortcut method, indicating a more optimized or automated approach in the synthesis process. This could involve leveraging pre-optimized IP blocks or design patterns to enhance the efficiency of the design flow. The session concluded with a comprehensive discussion on physical design, emphasizing the conversion of the synthesized netlist into a physical layout. The role of graphical design tools, presumably within the Cadence suite, was underscored for visualizing and manipulating circuit elements effectively.

In summary, Dr. Srinivasa Rao Udara's training session provided participants with a well-rounded understanding of VLSI design, covering theoretical concepts, practical simulations, and insights into essential tools and methodologies. The session equipped participants with the knowledge and skills necessary to navigate the intricate landscape of electronic circuit design using Cadence tools.



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Alvas Institute Of Engineering

Common Parade

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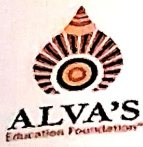
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ALVAS INSTITUTE OF ENGINEERING AND TECHNOLOGY

MIJAR SHOBHAVANA CAMPUS, MIJAR 574225 MIJAR,

Badagumijaru, Karnataka 574225, India

Internship Training on "Design of Electronic Circuits, Physical Design Using Cadence" by Dr. Srinivasa Rao Udara



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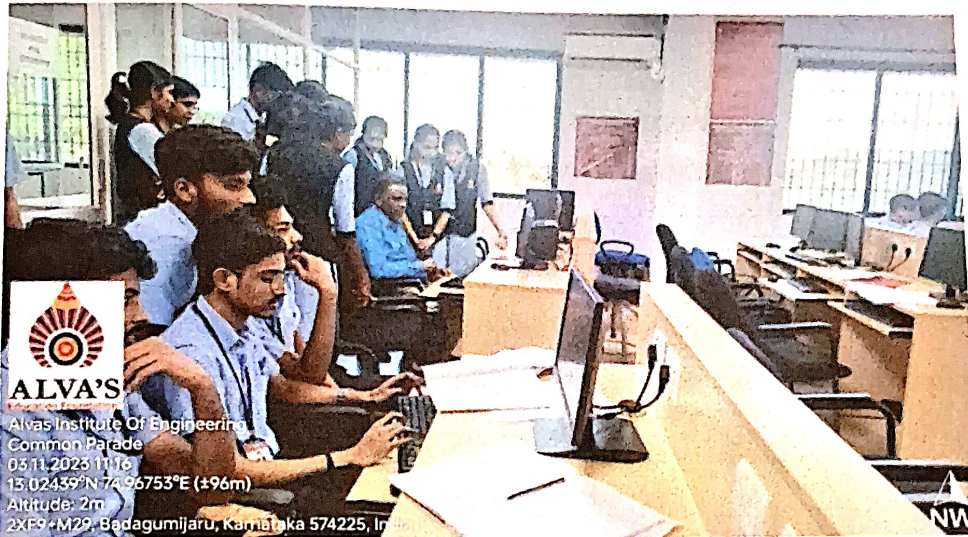
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ACADEMIC YEAR 2023-24



Internship Training on Design of Electronic Circuits, Physical Design Using Cadence by Dr. Srinivasa Rao Udara



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Topic: "Lab View"

Resource Person : Dr. Ravikumar A. V,
Associate Professor
Department of Electronics & Communication
S.J.B.T Institute of Technology, Bangalore.

Date : 06/11/2023 to 11/11/2023

The LabVIEW training conducted by Dr. Ravikumar provided a comprehensive understanding of LabVIEW software and its applications. The training covered both theoretical concepts and practical hands-on sessions to ensure a holistic learning experience.

Topics covered

1. Lab View Basics

- Introduction to LabVIEW environment.
- Understanding the Front Panel and Block Diagram panels.
- Overview of Controls and Indicators.

2. Controls and Indicators

- Numeric Control and Indicator.
- Boolean Control and Indicator.
- String Control and Indicator.
- Data Containers.
- Graphs.
- Enums.
- Decorations.



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3. Built-In Functions

- Structures for control flow
- Arrays and their applications.
- Clusters for data organization
- Numeric, Boolean, and String functions.
- Comparison functions.
- Timing functions.
- Mathematical operations.

4. Add-On Tools

- Integration with Maker Hub.
- Deployment of LabVIEW code to Arduino UNO.

5. Arduino Integration

- Basics of interfacing LabVIEW with Arduino.
- LED Blinking example.
- Working with various sensors.
- Building complete projects using LabVIEW and Arduino.

Participants applied the knowledge gained through the training to develop practical projects. These projects ranged from simple LED blinking exercises to more complex applications involving sensor integration. The training emphasized hands-on experience, enabling participants to confidently use LabVIEW for real-world applications.

Dr. Ravikumar's LabVIEW training successfully equipped participants with the skills and knowledge necessary to work with LabVIEW in various engineering and scientific applications. The combination of theoretical insights, practical demonstrations, and hands-on projects contributed to a well-rounded learning experience.



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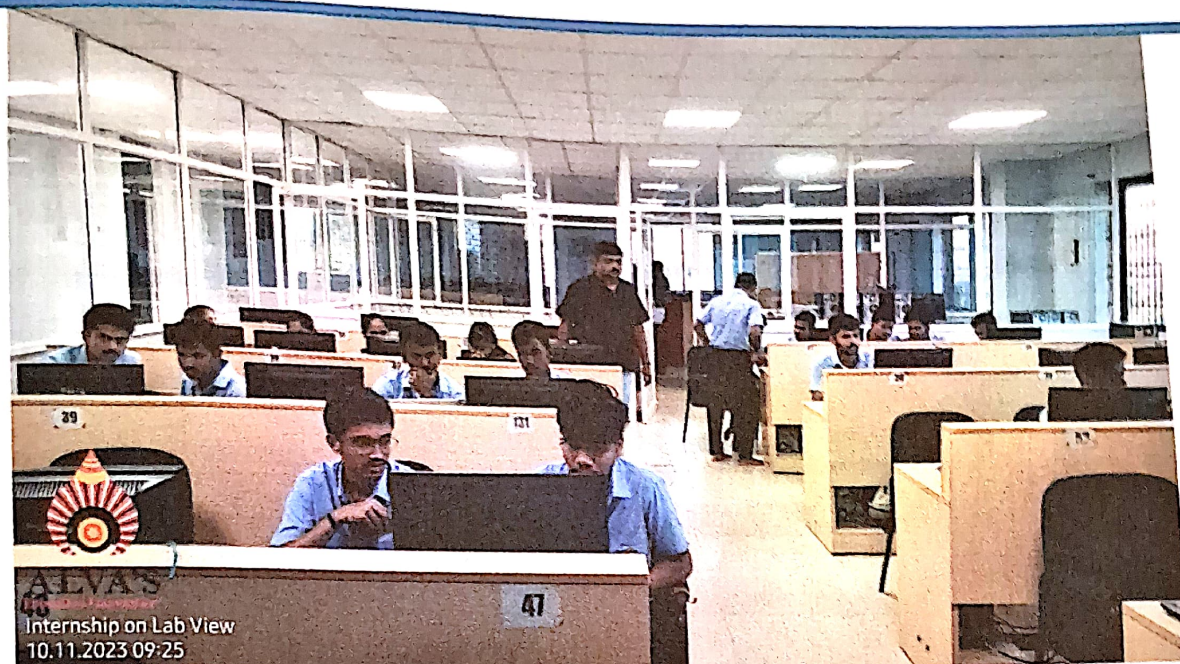
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Hands on session in Lab View by Dr. Ravikumar A.V



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ACADEMIC YEAR 2023-24

Topic: Industrial Visit to Karmic Design Pvt. Ltd, Manipal

Date: 22/11/2023

About Karmic Design Pvt. Ltd

Founded in 1999, KarMic is a pioneer and leader in the semiconductor design and development industry. They have expertise in Analog, Radio Frequency, Mixed Signal, Memory and High Performance Digital domains; providing design services and solutions globally. Company was started by two visionaries, Mr. Mark Harward and Dr. ShivalingMahantShetti in the year 1999. Today, KarMic Design has workforce of 500+ employees. The company was established with the below founding principles:

- Enable members to continually improve their knowledge to enhance value of services and solutions being offered, and
- Demonstrate corporate social responsibility enabling society to benefit at local, state, national and global levels.

KarMic has multiple design centers located in Manipal, Bangalore, and Dallas.

Projects Handling by Karmik

- Modeling and simulation of entire chip functionality using HDL.
- Top level verification by co-simulation.
- Spec validation.
- Module-level verification of Analog/RF design.
- RTL verification.

About the industrial Visit

Students received a brief introduction to layout design for USB, wireless, and power management modules of the chip, which included drivers, data converters, PLLs, DLL, low drop out regulators, DCDC converters, reference generators, battery chargers, charge pumps, and filters.



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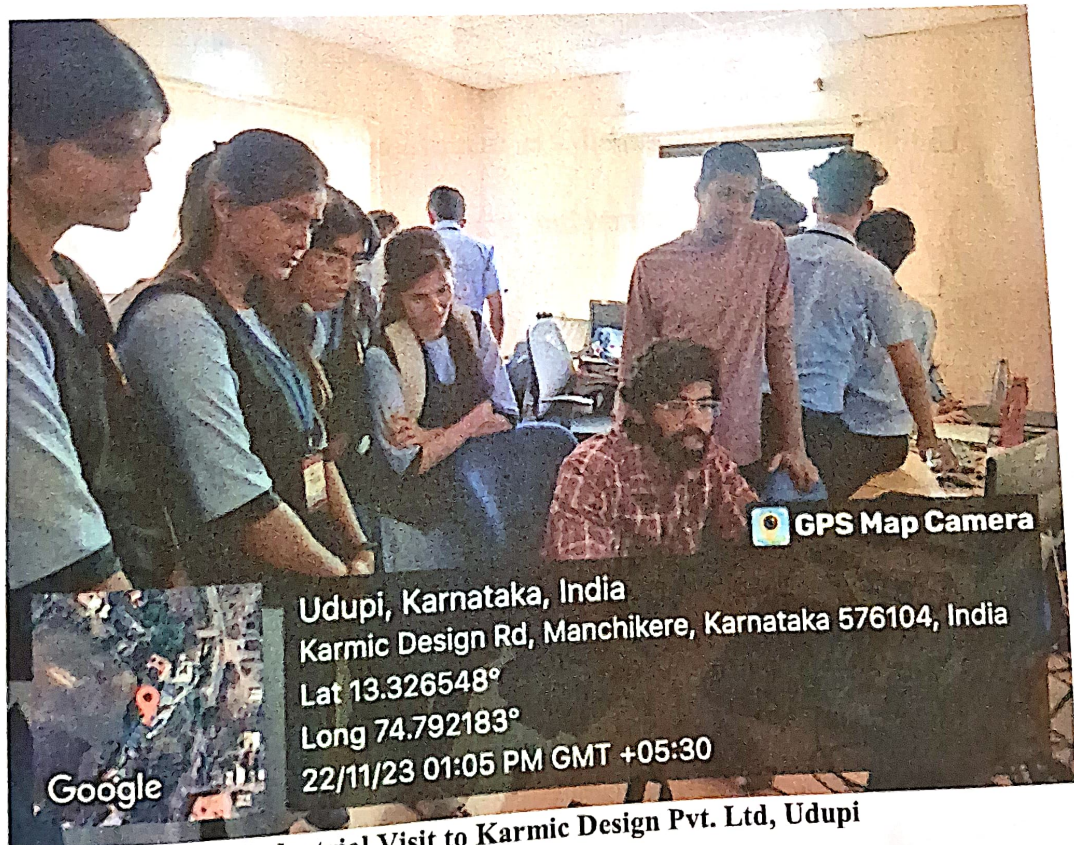
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ACADEMIC YEAR 2023-24



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Industrial Visit
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Udupi, Karnataka, India
Karmic Design Rd, Manchikere, Karnataka 576104, India
Lat 13.326548°
Long 74.792183°
22/11/23 01:05 PM GMT +05:30

Industrial Visit to Karmic Design Pvt. Ltd, Udupi



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ACADEMIC YEAR 2023-24

Topic: "Blockchain and its Application"

Resource Person : Prof. Lohith J J

Assistant Professor

Department of Computer Science and Engineering

B.M.S College of Engineering, Bangalore.

Date : 20/11/2023 and 21/11/2023

Prof. Lohith J J delivered an engaging lecture on blockchain technology, focusing on fundamental concepts, the role of cryptocurrency, and practical applications. The presentation provided a comprehensive overview, emphasizing hands-on aspects such as the simulation of Bitcoin, creation of blocks, and the essential role of miners in the blockchain network.

Key Topics Covered

1. Introduction to Blockchain

- Definition and core concepts of blockchain as a distributed ledger technology.
- Emphasis on decentralization, transparency, and immutability.

2. Introduction to Cryptocurrency

- Explanation of cryptocurrency as a digital or virtual form of currency.
- Overview of cryptographic principles ensuring security in cryptocurrency transactions.

3. Simulation of Bitcoin:

- Practical demonstration of a Bitcoin transaction using a simulated environment.
- Understanding the key components, including wallets, transactions, and blocks.



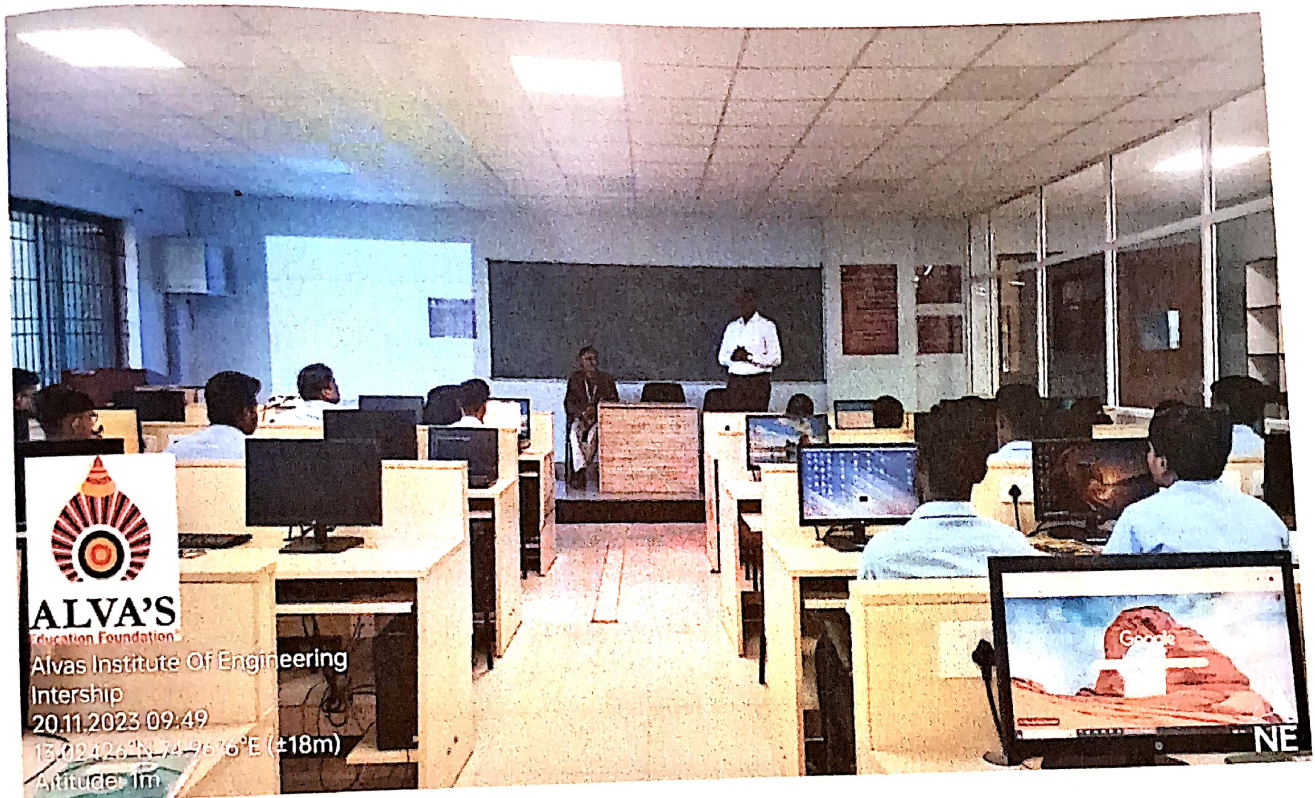
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Prof. Lohit J J's lecture provided a hands-on exploration of blockchain technology, offering participants valuable insights into the intricacies of Bitcoin simulation, block creation, and the vital role of miners. The presentation effectively bridged theoretical concepts with practical applications, enabling a holistic understanding of blockchain technology.



Internship training on Blockchain & its application by Prof. Lohith J J

Total number of Students benefited by the Internship training program: 44

Outcome: Students learned how to use Labview software and CADAENCE Tool.


Dept. Internship Training Coordinator
Mrs. Bhagyashree K.


H.O.D.
Dept. Of Electronics & Communication
Alva's Institute of Engg. & Technology
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INTERNSHIP CERTIFICATE SAMPLE



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CERTIFICATE

OF INTERNSHIP



cādence



PROUDLY PRESENTED TO:

This is to certify that Student has successfully completed an Internship Programme gaining hands-on experience in Cadence , LabVIEW for next Generation Electronics, and Blockchain Technology , demonstrated exceptional skills and dedication in applying theoretical knowledge to real-time applications during October/November 2023 Organised by Department of Electronics & Communication Engineering.

Head of the
Department

Principal

Alva's Institute Of Engineering and Technology
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<https://aiet.org.in>

4 Weeks Internship Training on Cadence and Labview

Attendance Sheet

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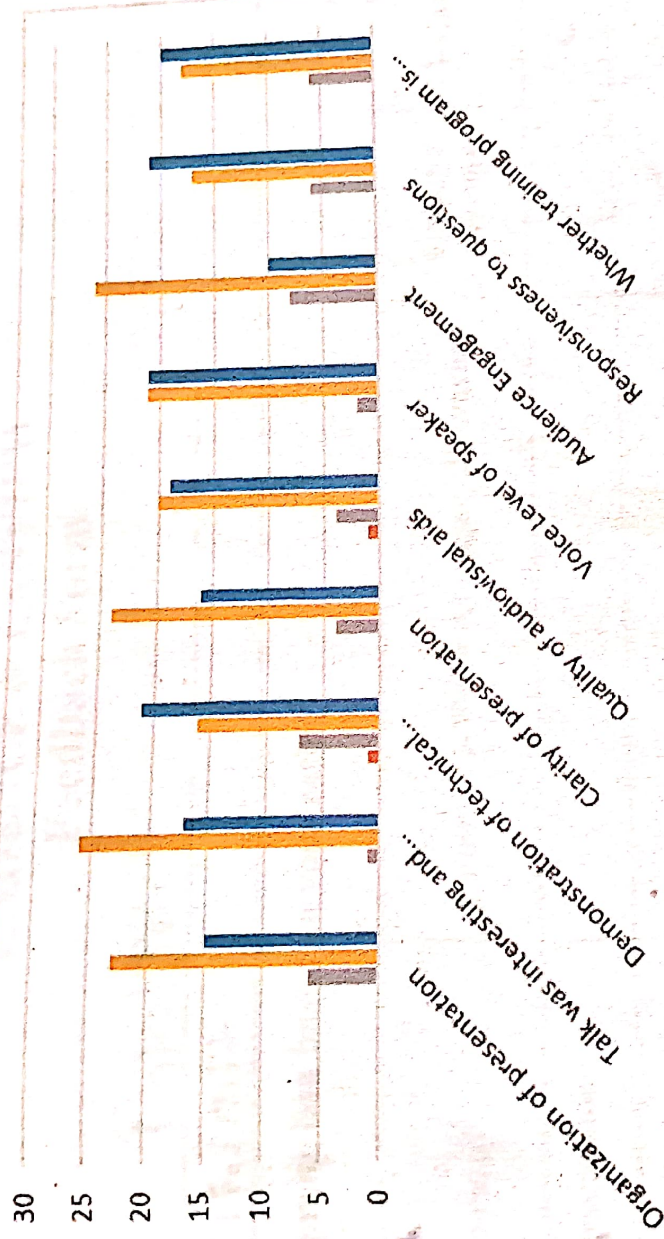
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15	4AL21EC030	Harshitha B S	
16	4AL21EC031	Hemanth R	
17	4AL21EC034	Inchara S Shetty	
18	4AL21EC035	Jeevan K G	
19	4AL21EC039	Keerthan S	
20	4AL21EC040	Kiran Kashyap M	
21	4AL21EC042	Lakshan	
22	4AL21EC044	Lekhan T	
23	4AL21EC046	Mahantesh Shidaray T	
24	4AL21EC047	Mailaragouda N P	
25	4AL21EC051	Mohammed Iqbal	
26	4AL21EC052	Muhammad Razi	
27	4AL21EC053	Nagabhushan H K	
28	4AL21EC057	Prajyot Rajgonda Patil	
29	4AL21EC058	Pavan	
30	4AL21EC059	Pooja Venkatesh Naik	
31	4AL21EC061	Prajwal Malabagi	
32	4AL21EC062	Prajwal S Das	
33	4AL21EC067	Rakshith	

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4 WEEKS INTERNSHIP FEEDBACK RESPONSE



Dept. Internship Training Coordinator
Mrs. Bhagyashree K

HoD
Dr. Siddesh G K

H. O. D.

Dept. Of Electronics & Communication
Alva's Institute of Engg. & Technology
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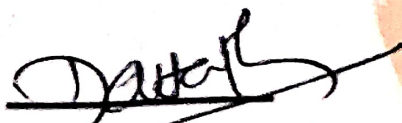
ethereum



PROUDLY PRESENTED TO:

GONTHAM M.A

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Head of the
Department

Principal

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