

ALVA'S INSTITUTE OF ENGINEERING & TECHNOLOGY

(Unit of Alva's Education Foundation (R), Moodbidri)

Affiliated to Visvesvaraya Technological University, Belagavi &

Approved by AICTE, New Delhi. Recognized by Government of Karnataka.

Accredited by NAAC with A+ & NBA accreditation for ECE & CSE

DEPARTMENT OF ECE TECHNICAL TALK REPORT

MAY-2023

TECHNICAL TALK REPORT



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Shobhavana Campus, MIJAR-574225, Moodbidri, D.K., Karnataka

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Report of Technical Talk-I

Topic: "DESIGN YOUR CAREER IN VLSI"

Resource Person: Mr. Gourav shet, Physical Verification Engineer,

Synopsys Inc, Bangalore.

Date: 13-05-2023

Time: 10:00AM to 11:30 AM.



Department of ECE, conducted the first technical talk of the even semester 2022-23 on "DESIGN YOUR CAREER IN VLSI" for the students of 4th and 6th semester - ECE on 13th May 2023.

Resource person for the talk was Mr. Gourav shet, Physical Verification engineer, Synopsys Inc, Bangalore. Mr. Gourav shet is an alumnus of our college and passed out in the



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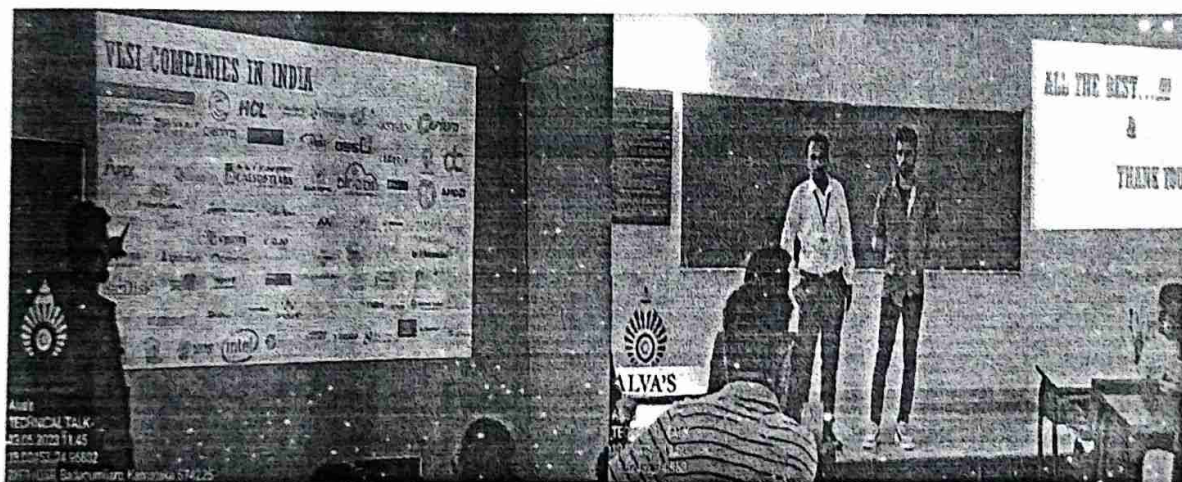
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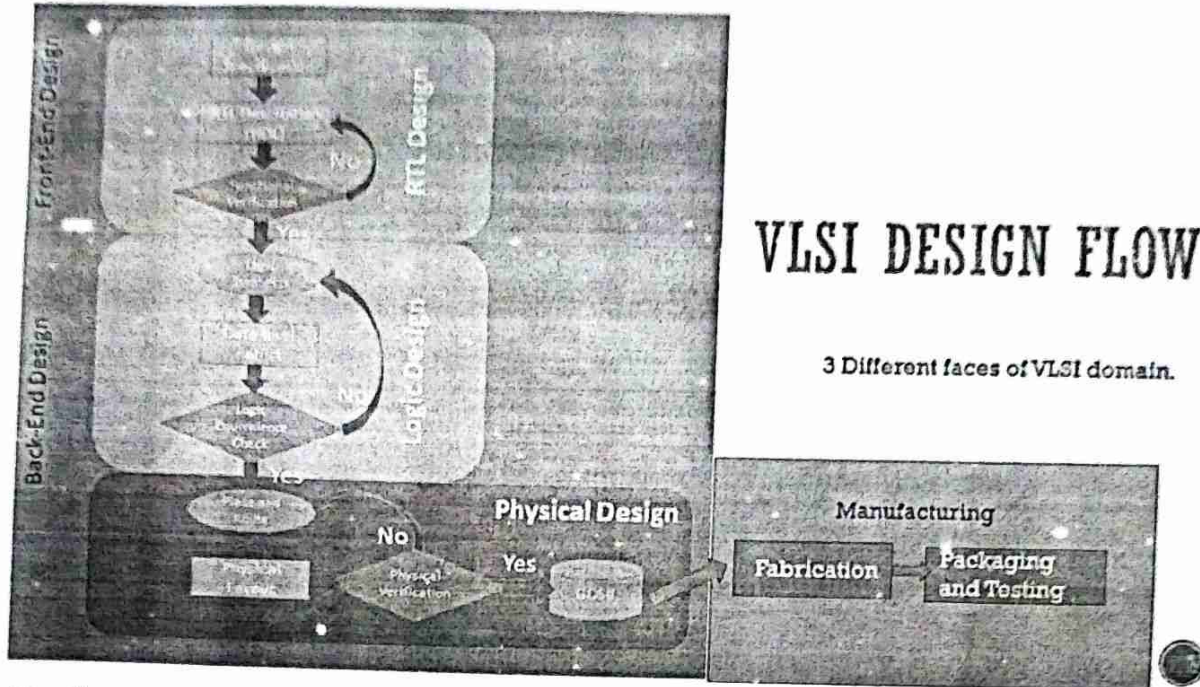
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year 2021-22. He was interested to work in Electronic core companies and now he is working in Synopsis Bangalore.



Talk was started with welcoming of Mr. Gourav shet by Prof. Sudhakara H M Associate Professor, Department of ECE. Talk on **“Design your Career in VLSI”** was presented with Introduction to VLSI, 3 Different faces of VLSI domain, Career Opportunities in VLSI, Plans for Preparations and Usefull Resources. He defined VLSI as the process of creating an integrated circuit (IC) by combining millions or billions of MOS transistors onto a single chip. He explained Current VLSI Technology trends and optimization of Area, Power & Speed. He described VLSI design flow and steps such as functional design, logic design, circuit design, and physical design. The design is verified for accuracy by the process of simulation.



VLSI DESIGN FLOW

3 Different faces of VLSI domain.

He discussed about Major VLSI Companies in India such as Intel, Nvidia, Qualcomm, Broadcom, Samsung, Texas Instruments, AMD, ARM, IBM, Cisco, Juniper Networks, Analog Devices Inc, ST Micro, MediaTek, Microchip, Xilinx etc.

He recommended Career opportunities as,

- AMS (Analog Mixed Signal) designer
- AMS verification engineer
- Layout design engineer
- ASIC front-end designer
- ASIC verification engineer
- Physical design engineer
- DFT engineer
- Application engineer technical support
- Board validation engineer
- Corporate Application Engineer (CAE)
- EDA/CAD engineer
- EDA tool validation engineer
- Fab/Foundry Engineer
- Field Application Engineer (FAE)



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- FPGA Back-end verification engineer
- Front-end verification engineer
- IP design engineer Verification Engineers
- IP verification engineer
- Library developer
- The physical design verification engineer
- Product Application Engineer (PAE)
- Reliability Engineer

He concluded his talk by giving links of most useful resources in VLSI domain.

Finally Prof. Sudhakara HM Associate Professor, department of ECE expressed gratitude for giving such an informative talk to the students.

ib
Technical Talk
Coordination

Siddesh
HOD

H. O. D.

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Principal
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PRINCIPAL

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Technical talk Attendance

Class: 6th Semester (ECE)

Topic: "DESIGN YOUR CAREER IN VLSI"

Resource Person: **Gourav Shet,**
Synopsis Bangalore

Date: 13/05/2023

Sl No	USN	Student Name	Signature
1	4AL20EC001	Aishwarya A P	
2	4AL20EC002	Aishwarya Panchagavi	
3	4AL20EC004	Akshay	
4	4AL20EC005	Akshay Sonar	
5	4AL20EC006	Aravinda Jogi	
6	4AL20EC007	Bhavana M	
7	4AL20EC008	Chaithrashree M G	
8	4AL20EC010	Chethana.M	
9	4AL20EC011	Darshan Madival	
10	4AL20EC012	Disha H	
11	4AL20EC013	Harshitha D K	
12	4AL20EC014	Jyoti Phakkirannanavar	
13	4AL20EC015	K.Vijay Bhanu	
14	4AL20EC016	Karthik G Shet	
15	4AL20EC017	Kishan Kumar	
16	4AL20EC018	Krathika	
17	4AL20EC019	Kshama Tc	
18	4AL20EC020	Libina Lal	
19	4AL20EC021	Manjunath Sajjan	
20	4AL20EC023	Mohammed Sharfuddin	
21	4AL20EC024	Mokshith	
22	4AL20EC025	Monika. D. B.	
23	4AL20EC026	Monisha P	
24	4AL20EC027	Nagarjuna GS	
25	4AL20EC028	Padala Sattibabu	



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26	4AL20EC029	P V Likhitha	<i>P.V.</i>
27	4AL20EC030	Pavan Kh	<i>Pavan</i>
28	4AL20EC031	Pooja P	<i>Pooja</i>
29	4AL20EC032	Prajaktha Poojary	<i>Pooja</i>
30	4AL20EC033	Prajwal R	<i>Pra</i>
31	4AL20EC034	Prashanth VK	
32	4AL20EC035	Pratham Puthran	<i>Pratham</i>
33	4AL20EC036	Prathish G Nayak	<i>Prathish</i>
34	4AL20EC037	Praveen Vishwakarma	<i>Praveen</i>
35	4AL20EC038	Preeti G M	<i>Preeti</i>
36	4AL20EC039	Rakesh Sangshetty	<i>Rakesh</i>
37	4AL20EC040	Rakshitha S	<i>Rakshitha</i>
38	4AL20EC041	Ramya K	<i>Ramya</i>
39	4AL20EC042	Ranjitha R	<i>Ranjitha</i>
40	4AL20EC043	Rohan H	<i>Rohan</i>
41	4AL20EC045	Sangamesh Biradar	<i>Sangamesh</i>
42	4AL20EC046	Sanjay Gk	
43	4AL20EC048	Santhripty C S	
44	4AL20EC049	Shravan V Upadhyaya	<i>Shravan</i>
45	4AL20EC050	Shridevi Rao	<i>Shridevi</i>
46	4AL20EC051	Shruthi	<i>Shruthi</i>
47	4AL20EC052	Shubhashri S	<i>Shubhashri</i>
48	4AL20EC053	Shwetha	<i>Shwetha</i>
49	4AL20EC054	Soumya N	<i>Soumya</i>
50	4AL20EC055	spoorthi salaskar	<i>Spoorthi</i>
51	4AL20EC056	Srujan	<i>Srujan</i>
52	4AL20EC057	Umesh	
53	4AL20EC058	Varshini K L	<i>Varshini</i>
54	4AL20EC059	Veena Talawar	<i>Veena</i>
55	4AL20EC060	Vinod Raj G N	<i>Vinod</i>
56	4AL20EC061	Vinyashree Jain	<i>Vinyashree</i>
57	4AL20EC062	Vishanth	<i>Vishanth</i>
58	4AL20EC063	Vishwanath	<i>Vishwanath</i>

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