

<b>HIGH PERFORMANCE COMPUTING</b> <b>(Effective from the academic year 2018 -2019)</b> <b>SEMESTER – VII</b>			
<b>Course Code</b>	<b>18CS732</b>	<b>CIE Marks</b>	<b>40</b>
<b>Number of Contact Hours/Week</b>	<b>3:0:0</b>	<b>SEE Marks</b>	<b>60</b>
<b>Total Number of Contact Hours</b>	<b>40</b>	<b>Exam Hours</b>	<b>03</b>
<b>CREDITS –3</b>			
<b>Course Learning Objectives:</b> This course (18CS732) will enable students to: <ul style="list-style-type: none"> <li>• Introduce students the design, analysis, and implementation, of high performance computational science and engineering applications.</li> <li>• Illustrate on advanced computer architectures, parallel algorithms, parallel languages, and performance-oriented computing.</li> </ul>			
<b>Module – 1</b>			<b>Contact Hours</b>
<b>Introduction to Parallel Computing:</b> Motivating Parallelism, Scope of Parallel Computing, <b>Parallel Programming Platforms:</b> Implicit Parallelism: Trends in Microprocessor Architectures, Limitations of Memory System Performance, Dichotomy of Parallel Computing Platforms, Physical Organization of Parallel Platforms, Communication Costs in Parallel Machines, Routing Mechanisms for Interconnection Networks, Impact of Process-Processor Mapping and Mapping Techniques. <b>T1: Ch: 1.1, 1.2, 2.1 – 2.7</b> <b>RBT: L1, L2</b>			08
<b>Module – 2</b>			
<b>Principles of Parallel Algorithm Design:</b> Preliminaries, Decomposition Techniques, Characteristics of Tasks and Interactions, Mapping Techniques for Load Balancing, Methods for Containing Interaction Overheads, Parallel Algorithm Models <b>Basic Communication Operations:</b> One-to-All Broadcast and All-to-One Reduction, All-to-All Broadcast and Reduction, All-Reduce and Prefix-Sum Operations, Scatter and Gather, All-to-All Personalized Communication, Circular Shift, Improving the Speed of Some Communication Operations <b>T1: Ch 3, 4</b> <b>RBT: L1, L2</b>			08
<b>Module – 3</b>			
<b>Analytical Modeling of Parallel Programs:</b> Sources of Overhead in Parallel Programs, Performance Metrics for Parallel Systems, The Effect of Granularity on Performance, Scalability of Parallel Systems. Minimum Execution Time and Minimum Cost-Optimal Execution Time, Asymptotic Analysis of Parallel Programs Section 5.7. Other Scalability Metrics, <b>Programming Using the Message-Passing Paradigm:</b> Principles of Message-Passing Programming, The Building Blocks: Send and Receive Operations, MPI: the Message Passing Interface, Topologies and Embedding, Overlapping Communication with Computation, Collective Communication and Computation Operations, Groups and Communicators <b>T1: Ch 5, 6</b> <b>RBT: L1, L2, L3</b>			08
<b>Module – 4</b>			
<b>Programming Shared Address Space Platforms:</b> Thread Basics, Why Threads?, The POSIX Thread API, Thread Basics: Creation and Termination, Synchronization Primitives in Pthreads, Controlling Thread and Synchronization Attributes, Thread Cancellation,			08



<p>Composite Synchronization Constructs, Tips for Designing Asynchronous Programs, OpenMP: a Standard for Directive Based Parallel Programming  Dense Matrix Algorithms: Matrix-Vector Multiplication, Matrix-Matrix Multiplication, Solving a System of Linear Equations  Sorting: Issues in Sorting on Parallel Computers, Sorting Networks, Bubble Sort and its Variants, Quicksort, Bucket and Sample Sort.  <b>T1: Ch 7, 8 9</b>  <b>RBT: L1, L2</b></p>	
<p><b>Module – 5</b></p>	
<p>Graph Algorithms: Definitions and Representation, Minimum Spanning Tree: Prim's Algorithm, Single-Source Shortest Paths: Dijkstra's Algorithm, All-Pairs Shortest Paths, Transitive Closure, Connected Components, Algorithms for Sparse Graphs,  Search Algorithms for Discrete Optimization Problems: Definitions and Examples, Sequential Search Algorithms, Search Overhead Factor, Parallel Depth-First Search, Parallel Best-First Search, Speedup, Anomalies in Parallel Search Algorithms  <b>T1: Ch10, 11</b>  <b>RBT: L1, L2</b></p>	08
<p><b>Course outcomes:</b> The students should be able to:</p>	
<ul style="list-style-type: none"> <li>• Illustrate the key factors affecting performance of CSE applications</li> <li>• Illustrate mapping of applications to high-performance computing systems</li> <li>• Apply hardware/software co-design for achieving performance on real-world applications</li> </ul>	
<p><b>Question paper pattern:</b></p>	
<ul style="list-style-type: none"> <li>• The question paper will have ten questions.</li> <li>• There will be 2 questions from each module.</li> <li>• Each question will have questions covering all the topics under a module.</li> <li>• The students will have to answer 5 full questions, selecting one full question from each module.</li> </ul>	
<p><b>Text Books:</b></p>	
<p>1. Introduction to Parallel Computing, AnanthGrama, Anshul Gupta, George Karypis, and Vipin Kumar, 2nd edition, Addison-Wesley, 2003.</p>	
<p><b>Reference Books:</b></p>	
<ol style="list-style-type: none"> <li>1. Grama, A. Gupta, G. Karypis, V. Kumar, An Introduction to Parallel Computing, Design and Analysis of Algorithms: 2/e, Addison-Wesley, 2003.</li> <li>2. G.E. Karniadakis, R.M. Kirby II, Parallel Scientific Computing in C++ and MPI: A Seamless Approach to Parallel Algorithms and their Implementation, Cambridge University Press, 2003.</li> <li>3. Wilkinson and M. Allen, Parallel Programming: Techniques and Applications Using Networked Workstations and Parallel Computers, 2/E, Prentice Hall, 2005.</li> <li>4. M.J. Quinn, Parallel Programming in C with MPI and OpenMP, McGraw-Hill, 2004.</li> <li>5. G.S. Almasi and A. Gottlieb, Highly Parallel Computing, 2/E, Addison-Wesley, 1994.</li> <li>6. David Culler Jaswinder Pal Singh, "Parallel Computer Architecture: A hardware/Software Approach", Morgan Kaufmann, 1999.</li> <li>7. Kai Hwang, "Scalable Parallel Computing", McGraw Hill 1998.</li> </ol>	

  
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