21INT49 Inter/Intra Institutional Internship: All the students admitted to engineering programs under the lateral entry category shall have to undergo a mandatory 21INT49 Inter/Intra Institutional Internship of 03 weeks during the intervening period of III and IV semesters. The internship shall be slated for CIE only and will not have SEE. The letter grade earned through CIE shall be included in the IV semester grade card. The internship shall be considered as a head of passing and shall be considered for vertical progression and for the award of degree. Those, who do not take up / complete the internship shall be declared fail and shall have to complete during subsequently after satisfying the internship requirements. The faculty coordinator or mentor shall monitor the students' internship progress and interact with them for the successful completion of the internship.

Non–credit mandatory courses (NCMC):

(A) Additional Mathematics I and II:

(1) These courses are prescribed for III and IV semesters respectively to lateral entry Diploma holders admitted to III semester of B.E./B.Tech., programs. They shall attend the classes during the respective semesters to complete all the formalities of the course and appear for the Continuous Internal Evaluation (CIE). In case, any student fails to register for the said course/fails to secure the minimum 40 % of the prescribed CIE marks, he/she shall be deemed to have secured an F grade. In such a case, the student has to fulfill the course requirements during subsequent semester/s to earn the qualifying CIE marks. These courses are slated for CIE only and has no SEE.

(2) Additional Mathematics I and II shall not be considered for vertical progression as well as for the calculation of SGPA and CGPA, but completion of the courses shall be mandatory for the award of degree.

(3) Successful completion of the courses Additional Mathematics I and II shall be indicated as satisfactory in the grade card. Non-completion of the courses Additional Mathematics I and II shall be indicated as Unsatisfactory.

(B) National Service Scheme/Physical Education (Sport and Athletics)/ Yoga:

(1) Securing 40 % or more in CIE,35 % or more marks in SEE and 40 % or more in the sum total of CIE + SEE leads to successful completion of the registered course.

(2) In case, students fail to secure 35 % marks in SEE, they have to appear for SEE during the subsequent examinations conducted by the University.

(3) In case, any student fails to register for NSS, PE or Yoga/fails to secure the minimum 40 % of the prescribed CIE marks, he/she shall be deemed to have not completed the requirements of the course. In such a case, the student has to fulfill the course requirements during subsequent semester/s to earn the qualifying CIE marks.

(4) Successful completion of the course shall be indicated as satisfactory in the grade card. Non-completion of the course shall be indicated as Unsatisfactory.

(5) These courses shall not be considered for vertical progression as well as for the calculation of SGPA and CGPA, but completion of the courses shall be mandatory for the award of degree.

Ability Enhancement Course - III			
21CSL381	Mastering Office	21CS383	
21CS382	Programming IN C++	21CS384	

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Choice Based Credit System (CBCS) and Outcome-Based Education (OBE)

SEMESTER - III

TRANSFORM CALCULUS, FOURIER SERIES AND NUMERICAL TECHNIQUES

Course Code	21MAT 31	CIE Marks	50
Teaching Hours/Week (L:T:P:S)	2:2:0:0	SEE Marks	50
Total Hours of Pedagogy	40	Total Marks	100
Credits	03	Exam Hours	03

Course objectives: The goal of the course Transform Calculus, Fourier series and Numerical techniques 21MAT 31 is

➤ To have an insight into solving ordinary differential equations by using Laplace transform techniques

➤ Learn to use the Fourier series to represent periodical physical phenomena in engineering analysis.

➤ To enable the students to study Fourier Transforms and concepts of infinite Fourier Sine and Cosine transforms and to learn the method of solving difference equations by the z-transform method.

➤ To develop proficiency in solving ordinary and partial differential equations arising in engineering applications, using numerical methods

Module-1: Laplace Transform

Definition and Laplace transforms of elementary functions (statements only). Problems on Laplace'sTransform of $e^{at}f(t)$, $t^n f(t)$, $\frac{f(t)}{t}$. Laplace transforms of Periodic functions (statement only) and unit-step function – problems.

Inverse Laplace transforms definition and problems, Convolution theorem to find the inverse Laplace transforms (without Proof) problems.Laplace transforms of derivatives, solution ofdifferential equations.**(8 Hours)**

Self-study: Solution of simultaneous first-order differential equations.

(RBT Levels: L1, L2 and L3)

Teaching-Learning Process	Chalk and talk method / PowerPoint Presentation

Module-2: Fourier Series

Introduction toinfinite series, convergence and divergence. Periodic functions, Dirichlet's condition. Fourier series of periodic functions with period 2π and arbitrary period. Half range Fourier series. Practical harmonic analysis.**(8 Hours)**

Self-study: Convergence of series by D'Alembert's Ratio test and, Cauchy'sroot test.

(RBT Levels: L1, L2 and L3)

Teaching-Learning Process	Chalk and talk method / PowerPoint Presentation

Module-3: Infinite Fourier Transforms and Z-Transforms

Infinite Fourier transforms definition, Fourier sine and cosine transforms. Inverse Fourier transforms, Inverse Fourier cosine and sine transforms. Problems.

Difference equations, z-transform-definition, Standard z-transforms, Damping and shifting rules, Problems. Inverse z-transform and applications to solve difference equations.**(8 Hours)**

Self Study: Initial value and final value theorems, problems.

(RBT Levels: L1, L2 and L3)

Teaching-Learning Process	Chalk and talk method / PowerPoint Presentation

Module-4: Numerical Solution of Partial Differential Equations	
Classifications of second-order partial differential equations, finite difference approximations to derivatives, Solution of Laplace's equationusing standard five-point formula. Solution of heat equation by Schmidt explicit formula and Crank- Nicholson method, Solution of the Wave equation. Problems. **(8 Hours)** **Self Study**: Solution of Poisson equations using standard five-point formula. **(RBT Levels: L1, L2 and L3)**	
Teaching-Learning Process	Chalk and talk method / PowerPoint Presentation
Module-5: Numerical Solution of Second-Order ODEs and Calculus of Variations	
Second-order differential equations - Runge-Kutta method and Milne's predictor and corrector method. (No derivations of formulae). Calculus of Variations:Functionals, Euler's equation, Problems on extremals of functional. Geodesics on a plane,Variationalproblems.**(8 Hours)** **Self Study:** Hanging chain problem **(RBT Levels: L1, L2 and L3)**	
Course outcomes: After successfully completing the course, the students will beable : ➢ To solve ordinary differential equations using Laplace transform. ➢ Demonstrate the Fourier series to study the behaviour of periodic functions and their applications in system communications, digital signal processing and field theory. ➢ To use Fourier transforms to analyze problems involving continuous-time signals and to apply Z-Transform techniques to solve difference equations ➢ To solve mathematical models represented by initial or boundary value problems involving partial differential equations ➢ Determine the extremals of functionals using calculus of variations and solve problems arising in dynamics of rigid bodies and vibrational analysis.	

Suggested Learning Resources:
Text Books:
1. **B.S.Grewal**:"HigherEngineeringMathematics",Khannapublishers,44thEd.2018
2. **E.Kreyszig**:"AdvancedEngineeringMathematics",JohnWiley&Sons,10thEd.(Reprint),2016.

Reference Books
1. **V.Ramana**:"HigherEngineeringMathematics"McGraw-HillEducation,11thEd.
2. **SrimantaPal&SubodhC.Bhunia**:"EngineeringMathematics"OxfordUniversityPress,3rdReprint, 2016.
3. **N.P Bali and Manish Goyal**: "A textbook of Engineering Mathematics" Laxmi Publications, Latest edition.
4. **C. Ray Wylie, Louis C. Barrett:** "Advanced Engineering Mathematics" McGraw – Hill Book Co.Newyork, Latested.
5. **Gupta C.B, Sing S.R and Mukesh Kumar:** "Engineering Mathematic for Semester I and II", Mc-Graw Hill Education(India) Pvt. Ltd2015.
6. **H.K.DassandEr.RajnishVerma:**"HigherEngineeringMathematics"S.ChandPublication(2014).
7. **JamesStewart:**"Calculus"Cengagepublications,7thedition,4thReprint2019.

Web links and Video Lectures (e-Resources):	
• http://.ac.in/courses.php?disciplineID=111	
• http://www.class-central.com/subject/math(MOOCs)	
• http://academicearth.org/	
• http://www.bookstreet.in.	
• VTU e-ShikshanaProgram	
• VTU EDUSATProgram	
Activity-Based Learning (Suggested Activities in Class)/ Practical Based learning	
• Quizzes	
• Assignments	
• Seminars	

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum marks (20 marks out of 50). A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 35% (18 Marks out of 50)in the semester-end examination(SEE), and a minimum of 40% (40 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together

Continuous Internal Evaluation:

1. Three Unit Tests each of **20 Marks (duration 01 hour)**
2. First test at the end of 5th week of the semester
3. Second test at the end of the 10th week of the semester
4. Third test at the end of the 15th week of the semester

Two assignments each of 10 Marks

5. First assignment at the end of 4th week of the semester
6. Second assignment at the end of 9th week of the semester

Group discussion/Seminar/quiz any one of three suitably planned to attain the COs and POs for **20 Marks (duration 01 hours)**

7. At the end of the 13th week of the semester

The sum of three tests, two assignments, and quiz/seminar/group discussion will be out of 100 marks and will be **scaled down to 50 marks**

(to have less stressed CIE, the portion of the syllabus should not be common /repeated for any of the methods of the CIE. Each method of CIE should have a different syllabus portion of the course).

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

Theory SEE will be conducted by University as per the scheduled timetable, with common question papers for the subject (**duration 03 hours)**

- The question paper will be set for 100 marks and marks scored will be proportionally scaled down to 50 marks
- The question paper will have ten questions. Each question is set for 20 marks.
- There will be 2 questions from each module. Each of the two questions under a module (with a maximum of 3 sub-questions), **should have a mix of topics** under that module.
- The students have to answer 5 full questions, selecting one full question from each module

	Support the program with functions for each of the above operations.
Teaching-Learning Process	Problem based learning (Implementation of different programs to illustrate application of arrays and structures. https://www.youtube.com/watch?v=3Xo6P_V-qns&t=201s
	https://ds2-iiith.vlabs.ac.in/exp/selection-sort/index.html https://ds1-iiith.vlabs.ac.in/data-structures-1/List%20of%20experiments.html

Module-2

Stacks: Definition, Stack Operations, Array Representation of Stacks, Stacks using Dynamic Arrays. Different representation of expression. Stack Applications: Infix to postfix conversion, Infix to prefix conversion, evaluation of postfix expression, recursion.

Queues: Definition, Array Representation of Queues, Queue Operations, Circular Queues, Queues and Circular queues using Dynamic arrays, Dequeues, Priority Queues.

Textbook 1: Chapter 3: 3.1 -3.4, 3.6 Textbook 2: Chapter 6: 6.1 -6.4, 6.5, 6.7-6.13

Laboratory Component:

1. Design, Develop and Implement a menu driven Program in C for the following operations on STACK of Integers (Array Implementation of Stack with maximum size MAX)
   a. *Push* an Element on to Stack
   b. *Pop* an Element from Stack
   c. Demonstrate *Overflow* and *Underflow* situations on Stack
   d. Display the status of Stack
   e. Exit
   Support the program with appropriate functions for each of the above operations
2. Design, Develop and Implement a Program in C for the following Stack Applications
   a. Evaluation of Suffix expression with single digit operands and operators: +, -, *, /, %, ^
   b. Solving Tower of Hanoi problem with n disks

Teaching-Learning Process	Active Learning, Problem based learning https://nptel.ac.in/courses/106/102/106102064/
	https://ds1-iiith.vlabs.ac.in/exp/stacks-queues/index.html

Module-3

Linked Lists: Definition, classification of linked lists. Representation of different types of linked lists in Memory, Traversing, Insertion, Deletion, Searching, Sorting, and Concatenation Operations on Singly linked list, Doubly Linked lists, Circular linked lists, and header linked lists. Linked Stacks and Queues. Applications of Linked lists – Polynomials, Sparse matrix representation. Programming Examples.

Textbook 1: Chapter 4: 4.1 – 4.4, 4.5.2, 4.7, 4.8, Textbook 2: Chapter 5: 5.1 – 5.9

Laboratory Component:

1. Singly Linked List (SLL) of Integer Data
   a. Create a SLL stack of N integer.
   b. Display of SLL
   c. Linear search. Create a SLL queue of N Students Data Concatenation of two SLL of integers.
2. Design, Develop and Implement a menu driven Program in C for the following operationson Doubly Linked List (DLL) of Professor Data with the fields: ID, Name, Branch, Area of specialization
   a. Create a DLL stack of N Professor's Data.
   b. Create a DLL queue of N Professor's Data
   Display the status of DLL and count the number of nodes in it.

Teaching-Learning Process	MOOC, Active Learning, Problem solving based on linked lists.
	https://nptel.ac.in/courses/106/102/106102064/
	https://ds1-iiith.vlabs.ac.in/exp/linked-list/basics/overview.html
	https://ds1-iiith.vlabs.ac.in/List%20of%20experiments.html
	https://ds1-iiith.vlabs.ac.in/exp/linked-list/basics/overview.html
	https://ds1-iiith.vlabs.ac.in/List%20of%20experiments.html

Module-4

Trees 1: Terminologies, Binary Trees, Properties of Binary trees, Array and linked Representation of Binary Trees, Binary Tree Traversals - Inorder, postorder, preorder; Threaded binary trees, Binary Search Trees – Definition, Insertion, Deletion, Traversal, and Searching operation on Binary search tree. Application of Trees-Evaluation of Expression.

Textbook 1: Chapter 5: 5.1 –5.5, 5.7; Textbook 2: Chapter 7: 7.1 – 7.9

Laboratory Component:

1. Given an array of elements, construct a complete binary tree from this array in level order fashion. That is, elements from left in the array will be filled in the tree level wise starting from level 0. Ex: Input :
arr[] = {1, 2, 3, 4, 5, 6}
Output : Root of the following tree

```
        1
       /\
      2  3
     /\ /\
    4  5 6
```

2. Design, Develop and Implement a menu driven Program in C for the following operations on Binary Search Tree (BST) of Integers
   a. Create a BST of N Integers
   b. Traverse the BST in Inorder, Preorder and Post Order

Teaching-Learning Process	Problem based learning
	http://www.nptelvideos.in/2012/11/data-structures-and-algorithms.html
	https://ds1-iiith.vlabs.ac.in/exp/tree-traversal/index.html
	https://ds1-iiith.vlabs.ac.in/exp/tree-traversal/depth-first-traversal/dft-practice.html

Module-5

Trees 2: AVL tree, Red-black tree, Splay tree, B-tree.

Graphs: Definitions, Terminologies, Matrix and Adjacency List Representation of Graphs, Traversal methods: Breadth First Search and Depth FirstSearch.

Hashing: Hash Table organizations, Hashing Functions, Static and Dynamic Hashing.

Textbook 1: Chapter 10:10.2, 10.3, 10.4, Textbook 2:7.10 – 7.12, 7.15 Chapter 11: 11.2, Textbook 1: Chapter 6 : 6.1–6.2, Chapter 8 : 8.1-8.3, Textbook 2: 8.1 – 8.3, 8.5, 8.7

Textbook 3: Chapter 15:15.1, 15.2,15.3, 15.4,15.5 and 15.7

Laboratory Component:

1. Design, Develop and implement a program in C for the following operations on Graph (G) of cities

a. Create a Graph of N cities using Adjacency Matrix.
b. Print all the nodes reachable from a given starting node in a diagraph using DFS/BFS method.

2. Design and develop a program in C that uses Hash Function H:K->L as H(K)=K mod m(reminder method) and implement hashing technique to map a given key K to the address space L. Resolve the collision (if any) using linear probing.

Teaching-Learning Process	NPTL, MOOC etc. courses on trees and graphs. http://www.nptelvideos.in/2012/11/data-structures-and-algorithms.html

Course Outcomes (Course Skill Set)

At the end of the course the student will be able to:

CO 1. Identify different data structures and their applications.
CO 2. Apply stack and queues in solving problems.
CO 3. Demonstrate applications of linked list.
CO 4. Explore the applications of trees and graphs to model and solve the real-world problem.
CO 5. Make use of Hashing techniques and resolve collisions during mapping of key value pairs

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum marks (20 marks). A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 35% (18 Marks out of 50) in the semester-end examination (SEE), and a minimum of 40% (40 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together

Continuous Internal Evaluation:

Three Unit Tests each of **20 Marks (duration 01 hour)**
1. First test at the end of 5th week of the semester
2. Second test at the end of the 10th week of the semester
3. Third test at the end of the 15th week of the semester

Two assignments each of **10 Marks**
4. First assignment at the end of 4th week of the semester
5. Second assignment at the end of 9th week of the semester

Practical Sessions need to be assessed by appropriate rubrics and viva-voce method. This will contribute to **20 marks**.

- Rubrics for each Experiment taken average for all Lab components – 15 Marks.
- Viva-Voce– 5 Marks (more emphasized on demonstration topics)

The sum of three tests, two assignments, and practical sessions will be out of 100 marks and will be **scaled down to 50 marks**

(to have a less stressed CIE, the portion of the syllabus should not be common /repeated for any of the methods of the CIE. Each method of CIE should have a different syllabus portion of the course).

CIE methods /question paper has to be designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

Theory SEE will be conducted by University as per the scheduled timetable, with common question papers for the subject (**duration 03 hours**)
1. The question paper will have ten questions. Each question is set for 20 marks.
2. There will be 2 questions from each module. Each of the two questions under a module (with a

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III Semester

ANALOG AND DIGITAL ELECTRONICS

Course Code	21CS33	CIE Marks	50
Teaching Hours/Week (L:T:P: S)	3:0:2:0	SEE Marks	50
Total Hours of Pedagogy	40 T + 20 P	Total Marks	100
Credits	04	Exam Hours	03

Course Learning Objectives:

CLO 1. Explain the use of photo electronics devices, 555 timer IC, Regulator ICs and uA741

CLO 2. Make use of simplifying techniques in the design of combinational circuits.

CLO 3. Illustrate combinational and sequential digital circuits

CLO 4. Demonstrate the use of flipflops and apply for registers

CLO 5. Design and test counters, Analog-to-Digital and Digital-to-Analog conversion techniques.

Teaching-Learning Process (General Instructions)

These are sample Strategies, which teachers can use to accelerate the attainment of the various course outcomes.

1. Lecturer method (L) does not mean only traditional lecture method, but different type of teaching methods may be adopted to develop the outcomes.
2. Show Video/animation films to explain functioning of various concepts.
3. Encourage collaborative (Group Learning) Learning in the class.
4. Ask at least three HOT (Higher order Thinking) questions in the class, which promotes critical thinking.
5. Adopt Problem Based Learning (PBL), which fosters students' Analytical skills, develop thinking skills such as the ability to evaluate, generalize, and analyze information rather than simply recall it.
6. Topics will be introduced in a multiple representation.
7. Show the different ways to solve the same problem and encourage the students to come up with their own creative ways to solve them.
8. Discuss how every concept can be applied to the real world - and when that's possible, it helps improve the students' understanding.

Module-1

BJT Biasing: Fixed bias, Collector to base Bias, voltage divider bias

Operational Amplifier Application Circuits: Peak Detector, Schmitt trigger, Active Filters, Non-Linear Amplifier, Relaxation Oscillator, Current-to-Voltage and Voltage-to-Current Converter, Regulated Power Supply Parameters, adjustable voltage regulator, D to A and A to D converter.

Textbook 1: Part A: Chapter 4 (Sections 4.2, 4.3, 4.4), Chapter 7 (Sections 7.4, 7.6 to 7.11), Chapter 8 (Sections 8.1 and 8.5), Chapter 9.

Laboratory Component:

1. Simulate BJT CE voltage divider biased voltage amplifier using any suitable circuit simulator.
2. Using ua 741 Opamp, design a 1 kHz Relaxation Oscillator with 50% duty cycle
3. Design an astable multivibrator circuit for three cases of duty cycle (50%, <50% and >50%) using NE 555 timer IC.
4. Using ua 741 opamap, design a window comparator for any given UTP and LTP.

Teaching-Learning Process	1. Demonstration of circuits using simulation. 2. Project work: Design a integrated power supply and function generator operating at audio frequency. Sine, square and triangular functions are to be generated. 3. Chalk and Board for numerical

Module-2

Karnaugh maps: minimum forms of switching functions, two and three variable Karnaugh maps, four

maximum of 3 sub-questions), **should have a mix of topics** under that module.

The students have to answer 5 full questions, selecting one full question from each module. Marks scoredout of 100 shall be reduced proportionally to 50 marks

Suggested Learning Resources:

Textbooks:

1. Ellis Horowitz and Sartaj Sahni, Fundamentals of Data Structures in C, 2nd Ed, Universities Press, 2014.
2. Seymour Lipschutz, Data Structures Schaum's Outlines, Revised 1st Ed, McGraw Hill, 2014.
3. Reema Thareja, Data Structures using C, 3rd Ed, Oxford press, 2012.

Reference Books:

1. Gilberg and Forouzan, Data Structures: A Pseudo-code approach with C, 2nd Ed, Cengage Learning,2014.
2. Jean-Paul Tremblay & Paul G. Sorenson, An Introduction to Data Structures with Applications,2nd Ed, McGraw Hill, 2013
3. A M Tenenbaum, Data Structures using C, PHI, 1989
4. Robert Kruse, Data Structures and Program Design in C, 2nd Ed, PHI, 1996.

Weblinks and Video Lectures (e-Resources):

1. http://elearning.vtu.ac.in/econtent/courses/video/CSE/06CS35.html
2. https://nptel.ac.in/courses/106/105/106105171/
3. http://www.nptelvideos.in/2012/11/data-structures-and-algorithms.html

Activity Based Learning (Suggested Activities in Class)/ Practical Based learning

- Real world problem solving using group discussion.
- Back/Forward stacks on browsers.
- Undo/Redo stacks in Excel or Word.
- Linked list representation of real-world queues -Music player, image viewer

variable Karnaugh maps, determination of minimum expressions using essential prime implicants, Quine-McClusky Method: determination of prime implicants, the prime implicant chart, Petricks method, simplification of incompletely specified functions, simplification using map-entered variables

Textbook 1: Part B: Chapter 5 (Sections 5.1 to 5.4) Chapter 6 (Sections 6.1 to 6.5)

Laboratory Component:	
1. Given a 4-variable logic expression, simplify it using appropriate technique and inplement the same using basic gates.	
Teaching-Learning Process	1. Chalk and Board for numerical
	2. Laboratory Demonstration

Module-3

Combinational circuit design and simulation using gates: Review of Combinational circuit design, design of circuits with limited Gate Fan-in, Gate delays and Timing diagrams, Hazards in combinational Logic, simulation and testing of logic circuits

Multiplexers, Decoders and Programmable Logic Devices: Multiplexers, three state buffers, decoders and encoders, Programmable Logic devices.

Textbook 1: Part B: Chapter 8, Chapter 9 (Sections 9.1 to 9.6)

Laboratory Component:	
1. Given a 4-variable logic expression, simplify it using appropriate technique and realize the simplified logic expression using 8:1 multiplexer IC.	
2. Design and implement code converter I) Binary to Gray (II) Gray to Binary Code	
Teaching-Learning Process	1. Demonstration using simulator
	2. Case study: Applications of Programmable Logic device
	3. Chalk and Board for numerical

Module-4

Introduction to VHDL: VHDL description of combinational circuits, VHDL Models for multiplexers, VHDL Modules.

Latches and Flip-Flops: Set Reset Latch, Gated Latches, Edge-Triggered D Flip Flop 3,SR Flip Flop, J K Flip Flop, T Flip Flop.

Textbook 1: Part B: Chapter 10(Sections 10.1 to 10.3), Chapter 11 (Sections 11.1 to 11.7)

Laboratory Component:	
1. Given a 4-variable logic expression, simplify it using appropriate technique and simulate the same in HDL simulator	
2. Realize a J-K Master / Slave Flip-Flop using NAND gates and verify its truth table. And implement the same in HDL.	
Teaching-Learning Process	1. Demonstration using simulator
	2. Case study: Arithmetic and Logic unit in VHDL
	3. Chalk and Board for numerical

Module-5

Registers and Counters: Registers and Register Transfers, Parallel Adder with accumulator, shift registers, design of Binary counters, counters for other sequences, counter design using SR and J K Flip Flops.

Textbook 1: Part B: Chapter 12 (Sections 12.1 to 12.5)

Laboratory Component:

1. Design and implement a mod-n (n<8) synchronous up counter using J-K Flip-Flop ICs and demonstrate its working. 2. Design and implement an asynchronous counter using decade counter IC to count up from 0 to n (n<=9) and demonstrate on 7-segment display (using IC-7447)	
Teaching-Learning Process	1. Demonstration using simulator 2. Project Work: Designing any counter, use LED / Seven-segment display to display the output 3. Chalk and Board for numerical

Course outcome (Course Skill Set)

At the end of the course the student will be able to:

CO 1. Design and analyze application of analog circuits using photo devices, timer IC, power supply and regulator IC and op-amp.

CO 2. Explain the basic principles of A/D and D/A conversion circuits and develop the same.

CO 3. Simplify digital circuits using Karnaugh Map, and Quine-McClusky Methods

CO 4. Explain Gates and flip flops and make us in designing different data processing circuits, registers and counters and compare the types.

CO 5. Develop simple HDL programs

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum marks (20 marks). A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 35% (18 Marks out of 50) in the semester-end examination (SEE), and a minimum of 40% (40 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together

Continuous Internal Evaluation:

Three Unit Tests each of **20 Marks (duration 01 hour)**

1. First test at the end of 5th week of the semester
2. Second test at the end of the 10th week of the semester
3. Third test at the end of the 15th week of the semester

Two assignments each of **10 Marks**

4. First assignment at the end of 4th week of the semester
5. Second assignment at the end of 9th week of the semester

Practical Sessions need to be assessed by appropriate rubrics and viva-voce method. This will contribute to **20 marks.**

- Rubrics for each Experiment taken average for all Lab components – 15 Marks.
- Viva-Voce– 5 Marks (more emphasized on demonstration topics)

The sum of three tests, two assignments, and practical sessions will be out of 100 marks and will be **scaled down to 50 marks**

(to have a less stressed CIE, the portion of the syllabus should not be common /repeated for any of the methods of the CIE. Each method of CIE should have a different syllabus portion of the course).

CIE methods /question paper has to be designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

Theory SEE will be conducted by University as per the scheduled timetable, with common question papers for the subject (**duration 03 hours**)

1. The question paper will have ten questions. Each question is set for 20 marks.
2. There will be 2 questions from each module. Each of the two questions under a module (with a

III Semester

COMPUTER ORGANIZATION AND ARCHITECTURE			
Course Code	21CS34	CIE Marks	50
Teaching Hours/Week (L:T:P: S)	3:0:0:0	SEE Marks	50
Total Hours of Pedagogy	40	Total Marks	100
Credits	03	Exam Hours	03

Course Learning Objectives

CLO 1. Understand the organization and architecture of computer systems, their structure and operation

CLO 2. Illustrate the concept of machine instructions and programs

CLO 3. Demonstrate different ways of communicating with I/O devices

CLO 4. Describe different types memory devices and their functions

CLO 5. Explain arithmetic and logical operations with different data types

CLO 6. Demonstrate processing unit with parallel processing and pipeline architecture

Teaching-Learning Process (General Instructions)

These are sample Strategies, which teachers can use to accelerate the attainment of the various course outcomes.

1. Lecturer method (L) need not to be only a traditional lecture method, but alternative effective teaching methods could be adopted to attain the outcomes.
2. Use of Video/Animation to explain functioning of various concepts.
3. Encourage collaborative (Group Learning) Learning in the class.
4. Ask at least three HOT (Higher order Thinking) questions in the class, which promotes critical thinking.
5. Adopt Problem Based Learning (PBL), which fosters students' Analytical skills, develop design thinking skills such as the ability to design, evaluate, generalize, and analyze information rather than simply recall it.
6. Introduce Topics in manifold representations.
7. Show the different ways to solve the same problem with different circuits/logic and encourage the students to come up with their own creative ways to solve them.
8. Discuss how every concept can be applied to the real world - and when that's possible, it helps improve the students' understanding.

Module-1

Basic Structure of Computers: Basic Operational Concepts, Bus Structures, Performance – Processor Clock, Basic Performance Equation, Clock Rate, Performance Measurement.

Machine Instructions and Programs: Memory Location and Addresses, Memory Operations, Instructions and Instruction Sequencing, Addressing Modes

Textbook 1: Chapter1 – 1.3, 1.4, 1.6 (1.6.1-1.6.4, 1.6.7), Chapter2 – 2.2 to 2.5

Teaching-Learning Process	Chalk and board, Active Learning, Problem based learning

Module-2

Input/Output Organization: Accessing I/O Devices, Interrupts – Interrupt Hardware, Direct Memory Access, Buses, Interface Circuits

Textbook 1: Chapter4 – 4.1, 4.2, 4.4, 4.5, 4.6

Teaching-Learning Process	Chalk and board, Active Learning, Demonstration

Module-3

Memory System: Basic Concepts, Semiconductor RAM Memories, Read Only Memories, Speed, Size, and Cost, Cache Memories – Mapping Functions, Virtual memories

Textbook 1: Chapter 5 – 5.1 to 5.4, 5.5 (5.5.1, 5.5.2)

Teaching-Learning Process	Chalk and board, Problem based learning, Demonstration

maximum of 3 sub-questions), **should have a mix of topics** under that module.

The students have to answer 5 full questions, selecting one full question from each module. Marks scoredout of 100 shall be reduced proportionally to 50 marks

Suggested Learning Resources:

Textbooks

1. Charles H Roth and Larry L Kinney, Raghunandan g H, Analog and Digital Electronics, Cengage Learning,2019

Reference Books

1. Anil K Maini, Varsha Agarwal, Electronic Devices and Circuits, Wiley, 2012.
2. Donald P Leach, Albert Paul Malvino & Goutam Saha, Digital Principles and Applications, 8th Edition, Tata McGraw Hill, 2015.
3. M. Morris Mani, Digital Design, 4th Edition, Pearson Prentice Hall, 2008.
4. David A. Bell, Electronic Devices and Circuits, 5th Edition, Oxford University Press, 2008

Weblinks and Video Lectures (e-Resources):

1. Analog Electronic Circuits: https://nptel.ac.in/courses/108/102/108102112/
2. Digital Electronic Circuits: https://nptel.ac.in/courses/108/105/108105132/
3. Analog Electronics Lab: http://vlabs.iitkgp.ac.in/be/
4. Digital Electronics Lab: http://vlabs.iitkgp.ac.in/dec

Activity Based Learning (Suggested Activities in Class)/ Practical Based learning

1. Real world problem solving - applying the design concepts of oscillator, amplifier, switch, Digital circuits using Opamps, 555 timer, transistor, Digital ICs and design a application like tone generator, temperature sensor, digital clock, dancing lights etc.

Module-4
Arithmetic: Numbers, Arithmetic Operations and Characters, Addition and Subtraction of Signed Numbers, Design of Fast Adders, Multiplication of Positive Numbers
Basic Processing Unit: Fundamental Concepts, Execution of a Complete Instruction, Hardwired control, Microprogrammed control
Textbook 1: Chapter2-2.1, Chapter6 – 6.1 to 6.3   **Textbook 1: Chapter7 – 7.1, 7.2,7.4, 7.5**

Teaching-Learning Process	Chalk& board, Problem based learning

Module-5
Pipeline and Vector Processing: Parallel Processing, Pipelining, Arithmetic Pipeline, Instruction Pipeline, Vector Processing, Array Processors
Textbook 2: Chapter 9 – 9.1, 9.2, 9.3, 9.4, 9.6, 9.7

Teaching-Learning Process	Chalk and board, MOOC

Course Outcomes

At the end of the course the student will be able to:

CO 1. Explain the organization and architecture of computer systems with machine instructions and programs

CO 2. Analyze the input/output devices communicating with computer system

CO 3. Demonstrate the functions of different types of memory devices

CO 4. Apply different data types on simple arithmetic and logical unit

CO 5. Analyze the functions of basic processing unit, Parallel processing and pipelining

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum marks (20 marks). A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 35% (18 Marks out of 50) in the semester-end examination (SEE), and a minimum of 40% (40 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together

Continuous Internal Evaluation:

Three Unit Tests each of **20 Marks (duration 01 hour)**
1. First test at the end of 5th week of the semester
2. Second test at the end of the 10th week of the semester
3. Third test at the end of the 15th week of the semester

Two assignments each of **10 Marks**
4. First assignment at the end of 4th week of the semester
5. Second assignment at the end of 9th week of the semester

Group discussion/Seminar/quiz any one of three suitably planned to attain the COs and POs for **20 Marks (duration 01 hours)**
6. At the end of the 13th week of the semester

The sum of three tests, two assignments, and quiz/seminar/group discussion will be out of 100 marks and will be **scaled down to 50 marks**

(to have less stressed CIE, the portion of the syllabus should not be common /repeated for any of the methods of the CIE. Each method of CIE should have a different syllabus portion of the course).

CIE methods /question paper has to be designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination:

Theory SEE will be conducted by University as per the scheduled timetable, with common question papers for the subject (**duration 03 hours**)
1. The question paper will have ten questions. Each question is set for 20 marks.
2. There will be 2 questions from each module. Each of the two questions under a module (with a

maximum of 3 sub-questions), **should have a mix of topics** under that module.

The students have to answer 5 full questions, selecting one full question from each module. Marks scored out of 100 shall be reduced proportionally to 50 marks

Textbooks

1. Carl Hamacher, Zvonko Vranesic, Safwat Zaky, Computer Organization, 5th Edition, Tata McGraw Hill
2. M. Morris Mano, Computer System Architecture, PHI, 3rd Edition

Reference:

1. William Stallings: Computer Organization & Architecture, 9th Edition, Pearson

Weblinks and Video Lectures (e-Resources):

1. https://nptel.ac.in/courses/106/103/106103068/
2. https://nptel.ac.in/content/storage2/courses/106103068/pdf/coa.pdf
3. https://nptel.ac.in/courses/106/105/106105163/
4. https://nptel.ac.in/courses/106/106/106106092/
5. https://nptel.ac.in/courses/106/106/106106166/
6. http://www.nptelvideos.in/2012/11/computer-organization.html

Activity Based Learning (Suggested Activities in Class)/ Practical Based learning

- Discussion and literature survey on real world use cases
- Quizzes

	Program: Write a program to generate the resume. Create 2 Java classes Teacher (data: personal information, qualification, experience, achievements) and Student (data: personal information, result, discipline) which implements the java interface Resume with the method biodata().
8	Aim: Demonstrate creation of threads using Thread class and Runnable interface, multi-threaded programming.
	Program: Write a Java program that implements a **multi-thread** application that has three threads. First thread generates a random integer for every 1 second; second thread computes the square of the number and prints; third thread will print the value of cube of the number.
9	Aim: Introduce java Collections.
	Program: Write a program to perform string operations using ArrayList. Write functions for the following a. Append - add at end b. Insert – add at particular index c. Search d. List all string starts with given letter.
10	Aim: Exception handling in java, introduction to throwable class, throw, throws, finally.
	Program: Write a Java program to read two integers a and b. **Compute** a/b and print, when b is not zero. Raise an exception when b is equal to zero.
11	Aim: Introduce File operations in java.
	Program: Write a java program that reads a file name from the user, displays information about whether the file exists, whether the file is readable, or writable, the type of file and the length of the file in bytes
12	Aim: Introduce java Applet, awt, swings.
	Programs: Develop an applet that displays a simple message in center of the screen. Develop a simple calculator using Swings.
	PART B – Practical Based Learning
01	A problem statement for each batch is to be generated in consultation with the co-examiner and student should develop an algorithm, program and execute the program for the given problem with appropriate outputs.

Course Outcome (Course Skill Set)
At the end of the course the student will be able to:

CO 1. Use Eclipse/NetBeans IDE to design, develop, debug Java Projects.
CO 2. Analyze the necessity for Object Oriented Programming paradigm over structured programming and become familiar with the fundamental concepts in OOP.
CO 3. Demonstrate the ability to design and develop java programs, analyze, and interpret object-oriented data and document results.
CO 4. Apply the concepts of multiprogramming, exception/event handling, abstraction to develop robust programs.
CO 5. Develop user friendly applications using File I/O and GUI concepts.

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum marks (20 marks). A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each course. The student has to secure not less than 35% (18 Marks out of 50) in the semester-end examination (SEE).

Continuous Internal Evaluation (CIE):
CIE marks for the practical course is **50 Marks**.
The split-up of CIE marks for record/ journal and test are in the ratio **60:40**.
- Each experiment to be evaluated for conduction with observation sheet and record write-up. Rubrics for the evaluation of the journal/write-up for hardware/software experiments designed by

III Semester

OBJECT ORIENTED PROGRAMMING WITH JAVA LABORATORY			
Course Code	21CSL35	CIE Marks	50
Teaching Hours/Week (L:T:P: S)	0:0:2:0	SEE Marks	50
Total Hours of Pedagogy	24	Total Marks	100
Credits	1	Exam Hours	03

Course Objectives:

CLO 1. Demonstrate the use of Eclipse/Netbeans IDE to create Java Applications.
CLO 2. Using java programming to develop programs for solving real-world problems.
CLO 3. Reinforce the understanding of basic object-oriented programming concepts.

	Note: two hours tutorial is suggested for each laboratory sessions.
	Prerequisite
	• Students should be familiarized about java installation and setting the java environment. • Usage of IDEs like Eclipse/Netbeans should be introduced.
Sl. No.	**PART A – List of problems for which student should develop program and execute in the Laboratory**
1	Aim: Introduce the java fundamentals, data types, operators in java Program: Write a java program that prints all real solutions to the quadratic equation ax2+bx+c=0. Read in a, b, c and use the quadratic formula.
2	Aim: Demonstrating creation of java classes, objects, constructors, declaration and initialization of variables. Program: Create a Java class called **Student** with the following details as variables within it. USN Name Branch Phone Write a Java program to create n Student objects and print the USN, Name, Branch, and Phone of these objects with suitable headings.
3	Aim: Discuss the various Decision-making statements, loop constructs in java Program: A. Write a program to check prime number B.Write a program for Arithmetic calculator using switch case menu
4	Aim: Demonstrate the core object-oriented concept of Inheritance, polymorphism Design a super class called **Staff** with details as StaffId, Name, Phone, Salary. Extend this class by writing three subclasses namely Teaching (domain, publications), Technical (skills), and Contract (period). Write a Java program to read and display at least 3 staff objects of all three categories.
5	Aim: Introduce concepts of method overloading, constructor overloading, overriding. Program: Write a java program demonstrating Method overloading and Constructor overloading.
6	Aim: Introduce the concept of Abstraction, packages. Program: Develop a java application to implement currency converter (Dollar to INR, EURO to INR, Yen to INR and vice versa), distance converter (meter to KM, miles to KM and vice versa), time converter (hours to minutes, seconds and vice versa) using packages.
7	Aim: Introduction to abstract classes, abstract methods, and Interface in java

III Semester

MASTERING OFFICE
(Practical based)

Course Code	21CSL381	CIE Marks	50
Teaching Hours/Week (L:T:P: S)	0:0:2:0	SEE Marks	50
Total Hours of Pedagogy	12T + 12P	Total Marks	100
Credits	01	Exam Hours	02

Course Objectives:

CLO 1. Understand the basics of computers and prepare documents and small presentations.

CLO 2. Attain the knowledge about spreadsheet/worksheet with various options.

CLO 3. Create simple presentations using templates various options available.

CLO 4. Demonstrate the ability to apply application software in an office environment.

CLO 5. Use MS Office to create projects, applications.

Teaching-Learning Process (General Instructions)

These are sample Strategies, which teachers can use to accelerate the attainment of the various course outcomes.

1. Lecturer method (L) need not to be only traditional lecture method, but alternative effective teaching methods could be adopted to attain the outcomes.
2. Use of Video/Animation to explain functioning of various concepts.
3. Encourage collaborative (Group Learning) Learning in the class.
4. Ask at least three HOT (Higher order Thinking) questions in the class, which promotes critical thinking.
5. Adopt Problem Based Learning (PBL), which fosters students' Analytical skills, develop design thinking skills such as the ability to design, evaluate, generalize, and analyze information rather than simply recall it.
6. Introduce Topics in manifold representations.
7. Show the different ways to solve the same problem with different circuits/logic and encourage the students to come up with their own creative ways to solve them.
8. Discuss how every concept can be applied to the real world - and when that's possible, it helps improve the students' understanding.

Module-1

MS-Word -Working with Files, Text – Formatting, Moving, copying and pasting text, Styles – Lists – Bulleted and numbered lists, Nested lists, Formatting lists. Table Manipulations. Graphics – Adding clip Art, add an image from a file, editing graphics, Page formatting - Header and footers, page numbers, Protect the Document, Mail Merge, Macros – Creating & Saving web pages, Hyperlinks.

Textbook 1: Chapter 2

Teaching-Learning Process	Chalk and board, Active Learning, practical based learning

Module-2

MS-Excel- Modifying a Worksheet – Moving through cells, adding worksheets, rows and columns, Resizing rows and columns, selecting cells, Moving and copying cells, freezing panes - Macros – recording and running. Linking worksheets - Sorting and Filling, Alternating text and numbers with Auto fill, Auto filling functions. Graphics – Adding clip art, add an image from a file, Charts – Using chart Wizard, Copy a chart to Microsoft Word.

Textbook 1: Chapter 3

Teaching-Learning Process	Active Learning, Demonstration, presentation,

Module-3

MS-Power Point -Create a Presentation from a template- Working with Slides – Insert a new slide, applying a design template, changing slide layouts – Resizing a text box, Text box properties, delete a text box - Video and Audio effects, Color Schemes & Backgrounds Adding clip art, adding an image from a file, Save as a web page.

the faculty who is handling the laboratory session and is made known to students at the beginning of the practical session.

- Record should contain all the specified experiments in the syllabus and each experiment write-up will be evaluated for 10 marks.
- Total marks scored by the students are scaled downed to 30 marks (60% of maximum marks).
- Weightage to be given for neatness and submission of record/write-up on time.
- Department shall conduct 02 tests for 100 marks, the first test shall be conducted after the 8th week of the semester and the second test shall be conducted after the 14th week of the semester.
- In each test, test write-up, conduction of experiment, acceptable result, and procedural knowledge will carry a weightage of 60% and the rest 40% for viva-voce.
- The suitable rubrics can be designed to evaluate each student's performance and learning ability. Rubrics suggested in Annexure-II of Regulation book
- The average of 02 tests is scaled down to **20 marks** (40% of the maximum marks).

The Sum of scaled-down marks scored in the report write-up/journal and average marks of two tests is the total CIE marks scored by the student.

Semester End Evaluation (SEE):

- SEE marks for the practical course is 50 Marks.
- SEE shall be conducted jointly by the two examiners of the same institute, examiners are appointed by the University
- All laboratory experiments are to be included for practical examination.
- (Rubrics) Breakup of marks and the instructions printed on the cover page of the answer script to be strictly adhered to by the examiners. **OR** based on the course requirement evaluation rubrics shall be decided jointly by examiners.
- Students can pick one question (experiment) from the questions lot prepared by the internal /external examiners jointly.
- Evaluation of test write-up/ conduction procedure and result/viva will be conducted jointly by examiners.
- General rubrics suggested for SEE are mentioned here, writeup-20%, Conduction procedure and result in -60%, Viva-voce 20% of maximum marks. SEE for practical shall be evaluated for 100 marks and scored marks shall be scaled down to 50 marks (however, based on course type, rubrics shall be decided by the examiners)
- *Students can pick one experiment from the questions lot of PART A with equal choice to all the students in a batch. For PART B examiners should frame a question for each batch, student should develop an algorithm, program, execute and demonstrate the results with appropriate output for the given problem.*
- *Weightage of marks for PART A is 80% and for PART B is 20%. General rubrics suggested to be followed for part A and part B.*
- Change of experiment is allowed only once and Marks allotted to the procedure part to be made zero (Not allowed for Part B).
- The duration of SEE is 03 hours
- Rubrics suggested in Annexure-II of Regulation book

Suggested Learning Resources:

1. E Balagurusamy, Programming with Java, Graw Hill, 6th Edition, 2019.
2. Herbert Schildt, C: Java the Complete Reference, McGraw Hill, 11th Edition, 2020

- SEE shall be conducted jointly by the two examiners of the same institute, examiners are appointed by the University
- All laboratory experiments are to be included for practical examination.
- (Rubrics) Breakup of marks and the instructions printed on the cover page of the answer script to be strictly adhered to by the examiners. **OR** based on the course requirement evaluation rubrics shall be decided jointly by examiners.
- Students can pick one question (experiment) from the questions lot prepared by the internal /external examiners jointly.
- Evaluation of test write-up/ conduction procedure and result/viva will be conducted jointly by examiners.
- General rubrics suggested for SEE are mentioned here, writeup-20%, Conduction procedure and result in -60%, Viva-voce 20% of maximum marks. SEE for practical shall be evaluated for 100 marks and scored marks shall be scaled down to 50 marks (however, based on course type, rubrics shall be decided by the examiners)
- The duration of SEE is 02 hours

Rubrics suggested in Annexure-II of Regulation book

Weblinks and Video Lectures (e-Resources):

1. https://youtu.be/9VRmgC2GRFE
2. https://youtu.be/rJPWi5x0g3I
3. https://youtu.be/tcj2BhhCMN4
4. https://youtu.be/ubmwp8kbfPc
5. https://youtu.be/i6eNvfQ8fTw
6. http://office.microsoft.com/en-us/training/CR010047968.aspx
7. https://gsuite.google.com/learning-center
8. http://spoken-tutorial.org

Activity Based Learning (Suggested Activities in Class)/ Practical Based learning

- Real world problem solving using group discussion.
- Real world examples of Windows Framework.

Dept. of Computer ...
Alva's Institute of Engg. & Technology
Mijar, Moodubidire - 574 225

SOCIAL CONNECT & RESPONSIBILITIES

Course Code	21SCR36	CIE Marks	50
Teaching Hours week (L:T:P:S)	1: 0: 0	SEE Marks	50
Total Hours of Pedagogy	15	Total Marks	100
Credits	01	Exam Hours	03
Department	Management Studies / Engineering Department		
Offered for	3rd Semester		
Prerequisite	Nil		

Objectives: The Course will
- Enable the student to do a deep drive into societal challenges being addressed by NGO(s), social enterprises & The government and build solutions to alleviate these complex social problems through immersion, design & technology.
- Provide a formal platform for students to communicate and connect with their surroundings.
- Enable to create of a responsible connection with society.

Learning Outcomes: The students are expected to have the ability to :
1. Understand social responsibility
2. Practice sustainability and creativity
3. Showcase planning and organizational skills

Contents:

The course is mainly activity-based that will offer a set of activities for the student that enables them to connect with fellow human beings, nature, society, and the world at large. The course will engage studentsinr interactive sessions, open mic, reading groups, storytelling sessions, and semester-long activities conducted by faculty mentors. In the following a set of activities planned for the course have been listed :

Module-I

Plantation and adoption of a tree: Plantation of a tree that will be adopted for four years by a group of B.Tech. students. They will also make an excerpt either as a documentary or a photoblog describing the plant's origin, its usage in daily life, and its appearance in folklore and literature.

Module-II

Heritage walk and crafts corner: Heritage tour, knowing the history and culture of the city, connecting to people around through their history, knowing the city and its craftsman, photoblog and documentary on evolution and practice of various craft forms.

Module-III

Organic farming and waste management: usefulness of organic farming, wet waste management in neighboring villages, and implementation in the campus.

Module-IV

Water Conservation: knowing the present practices in the surrounding villages and

implementation in the campus, documentary or photo blog presenting the current practices.

Module-V

Food Walk City's culinary practices, food lore, and indigenous materials of the region used in cooking.

Activities

Jamming session, open mic, and poetry: Platform to connect to others. Share the stories with others. **Share the experience of Social Connect.** Exhibit the talent like playing instruments, singing, one-act play, art-painting, and fine art.

PEDAGOGY

The pedagogy will include interactive lectures, inspiring guest talks, field visits, social immersion, and a course project. Applying and synthesizing information from these sources to define the social problem to address and take up the solution as the course project, with your group. Social immersion with NGOs/social sections will be a key part of the course. Will all lead to the course project that will address the needs of the social sector?

COURSE TOPICS:

The course will introduce social context and various players in the social space, and present approaches to discovering and understanding social needs. Social immersion and inspiring conversional will culminate in developing an actual, idea for problem-based intervention, based on an in-depth understanding of a key social problem.

A total of 14-20 hrs engagement per semester is required for the 3rd semester of the B.E. /B.Tech. program. The students will be divided into 10 groups of 35 each. Each group will be handled by two **faculty mentors.** Faculty mentors will design the activities (particularly Jamming sessions open mic ,and poetry)

Faculty mentors has to design the evaluation system.

Guideline for Assessment Process:

Continuous Internal Evaluation (CIE)

After completion of, the social connect, the student shall prepare, with daily **diary** as reference, a comprehensive report in consultation with the mentor/s to indicate what he has observed and learned in the social connect period. The report should be signed by the mentor. The report shall be evaluated on the basis of the following criteria and/or other relevant criteria pertaining to the activity completed.

      Marks allotted for the diary are out of 50.

      Planning and scheduling the social connect

      Information/Data collected during the social connect

      Analysis of the information/data and report writing

Considering all above points allotting the marks as mentioned below-

Excellent	80 to 100
Good	60 to 79
Satisfactory	40 to 59
Unsatisfactory and fail	<39

Pedagogy (Guidelines) may differ depending on local resources available for the study

Module	Topic	Content	Group Size	Location	Magnitude	Activity	Reporting	Evaluation
I	Plantation and adoption of a tree	Plantation of a tree that will be adopted for four years by a group of B.Tech. students. They will also make an excerpt either as a documentary or a photoblog describing the plant's origin, its usage in daily life, and its appearance in folklore and literature.	03 – 05	Farmers Land or Road side or Community area or institution's campus, any one location to be selected.	One Students must monitor it for three years	Site selection Select suitable species in consultation with horticulture, forest or agriculture department. Interact with NGO/Industry and community to plant Tag the plant for continuous monitoring	Report shall be handwritten or blog with paintings, sketches, poster, video and/or photograph with Geo tag.	Each module is evaluated for 50 Marks and average of all the five modules will be the final marks. **CIE Rubrics for 50 M** Planning and scheduling the social connect – 15 M Information/Data collected during the social connect – 15 M Analysis of the information/data and report writing – 20 M **SEE 50 M:** Presentation, Jamming session, Open mic, Group
II	Heritage walk and crafts corner	Heritage tour, knowing the history and culture of the city, connecting to people around through their history, knowing the city and its craftsman, photoblog and documentary on evolution and practice of various craft forms.	03 - 05	Preferably Within the city where institution is located or home town of the student group	One or two One can be a structure or a heritage building the other can be heritage custom or practise	Survey in the form of questioner by connecting to the people and asking. No standard questioner to be given by faculty and has to be evolved involving students. Questions during survey can be asked in local language but report language is English.		
III	Waste management	Wet waste management in neighbouring villages, and implementation in the campus.	03 - 05 More than one group can be	Preferably in the nearby villages and within the campus.	One	Report on importance and benefits of Waste management. Report on segregation, collection, transportation and disposal.		

Semester End Examination (SEE)

This Jamming session will be conducted at the end of the course for **50 marks**

Jamming session includes -Platform to connect to others. Share the stories with others. **Share the experience of Social Connect**. Exhibit the talent like playing instruments, singing, one-act play, art painting, and fine art.

Faculty mentor has to design the evaluation system for the Jamming session.

			assigned one task based on magnitude of task.			Suggestion for composting.

Visit nearby village/location to sensitize farmers and public about waste management and also document current practises. | | discussion and debate. |
| III | Organic farming | Usefulness of organic farming in neighbouring villages, and implementation in the campus. | 03 – 05 | Visit to farming lands where organic farming is going on

Campus Garden

Roof top Garden or Vertical Garden or hydroponics if land is scarce. | One | Collect data on organic farming in the vicinity. Like types of crop, methodology etc.,.

Suggestion for implementation at selected locations | | |
| IV | Water Conservation | Knowing the present practices in the surrounding villages and implementation in the campus, documentary or photo blog presenting the current practices. | 03 – 05 | Rain water harvesting demonstration available in the campus or surroundings | One | Visit lakes/pond/river/dry well to involve on rejuvenation activity.

Or

Assessment of Water budget in the campus/village | | |

						Report on traditional water conservation practices (to minimize wastage)		
V	Food Walk	City's culinary practices, food lore, and indigenous materials of the region used in cooking.	03 - 05	Within the city where institution is located Food culture of student's resident region	One	Survey local food centres and identify the speciality Identify and study the food ingredients Report on the regional foods Report on Medicinals values of the local food grains, and plants.		

**Important recommendations requested; Special Appreciation from institution and university for students who take care of plants for three years.

ಬಳಕೆ ಕನ್ನಡ - baLake Kannada (Kannada for Usage)

ಕನ್ನಡ ಕಲಿಕೆಗಾಗಿ <u>ನಿಗದಿಪಡಿಸಿದ</u> ಪಠ್ಯಪುಸ್ತಕ - (Prescribed Textbook to Learn Kannada)

ವಿಷಯ ಸಂಕೇತ (Course Code)	21KBK37/47	ನಿರಂತರ ಆಂತರಿಕ ಮೌಲ್ಯಮಾಪನದ ಅಂಕಗಳು (Continuous Internal Evaluation Marks)	50
ಒಂದು ವಾರಕ್ಕೆ ಬೋಧನಾ ಅವಧಿ (Teaching Hours / Week (L:T:P: S)	0:2:0:1	ಸೆಮಿಸ್ಟರ್ ಅಂತ್ಯದ ಪರೀಕ್ಷೆಯ ಅಂಕಗಳು (Semester End Examination Marks)	50
ಒಟ್ಟು ಬೋಧನಾ ಅವಧಿ Total Hours of Pedagogy	25 ಗಂಟೆಗಳು	ಒಟ್ಟು ಅಂಕಗಳು (Total Marks)	100
ಕ್ರೆಡಿಟ್ಸ್ (Credits)	01	ಪರೀಕ್ಷೆಯ ಅವಧಿ (Exam Hours)	01 ಗಂಟೆ

ಬಳಕೆ ಕನ್ನಡ ಪಠ್ಯದ ಕಲಿಕೆಯ ಉದ್ದೇಶಗಳು (Course Learning Objectives):

- To Create the awareness regarding the necessity of learning local language for comfortable and healthy life.
- To enable learners to Listen and understand the Kannada language properly.
- To speak, read and write Kannada language as per requirement.
- To train the learners for correct and polite conservation.

ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವ್ಯವಸ್ಥೆ (Teaching-Learning Process - General Instructions) :

These are sample Strategies, which teacher can use to accelerate the attainment of the various course outcomes.

1. ಬಳಕೆ ಕನ್ನಡವನ್ನು ತರಗತಿಯಲ್ಲಿ ಶಿಕ್ಷಕರು ಬೋಧಿಸಲು ವಿಟಿಯ ಸೂಚಿಸಿರುವ ಪಠ್ಯಪುಸ್ತಕವನ್ನು ಉಪಯೋಗಿಸಬೇಕು.

2. ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ತಯಾರಿಸಲು ವಿದ್ಯಾರ್ಥಿಗಳನ್ನು ಉತ್ತೇಜಿಸುವುದು ಮತ್ತು ತರಗತಿಯಲ್ಲಿ ಅವುಗಳನ್ನು ಚರ್ಚಿಸಲು ಅವಕಾಶ ಮಾಡಿಕೊಡುವುದು.

3. ಪ್ರತಿ ವಿದ್ಯಾರ್ಥಿ ಪುಸ್ತಕವನ್ನು ತರಗತಿಯಲ್ಲಿ ಬಳಸುವಂತೆ ನೋಡಿಕೊಳ್ಳುವುದು ಮತ್ತು ಪ್ರತಿ ಪಾಠ ಮತ್ತು ಪ್ರವಚನಗಳ ಮೂಲ ಅಂಶಗಳಿಗೆ ಸಂಬಂಧಪಟ್ಟಂತೆ ಪೂರಕ ಚಟುವಟಿಕೆಗಳಿಗೆ ತೊಡಗಿಸತಕ್ಕದ್ದು.

1. ಡಿಜಿಟಲ್ ತಂತ್ರಜ್ಞಾನದ ಮುಖಾಂತರ ಇತ್ತೀಚೆಗೆ ಡಿಜಿಟಲೀಕರಣ ಗೊಂಡಿರುವ ಭಾಷೆ ಕಲಿಕೆಯ ವಿಧಾನಗಳನ್ನು ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ಮುಖಾಂತರ ಚರ್ಚಿಸಲು ಕ್ರಮಕೈಗೊಳ್ಳುವುದು. ಇದರಿಂದ ವಿದ್ಯಾರ್ಥಿಗಳನ್ನು ತರಗತಿಯಲ್ಲಿ ಹೆಚ್ಚು ಏಕಾಗ್ರತೆಯಿಂದ ಪಾಠ ಕೇಳಲು ಮತ್ತು ಅಧ್ಯಯನದಲ್ಲಿ ತೊಡಗಲು ಅನುಕೂಲವಾಗುತ್ತದೆ.

2. ಭಾಷಾಕಲಿಕೆಯ ಪ್ರಯೋಗಾಲಯದ ಮುಖಾಂತರ ಬಹುಬೇಗ ಕನ್ನಡ ಭಾಷೆಯನ್ನು ಕಲಿಯಲು ಅನುಕೂಲವಾಗುವಂತೆ ಕಾರ್ಯಚಟುವಟಿಕೆಗಳನ್ನು ಮತ್ತು ಕ್ರಿಯಾ ಯೋಜನೆಗಳನ್ನು ರೂಪಿಸುವುದು.

Module-1

1. Introduction, Necessity of learning a local language. Methods to learn the Kannada language.
2. Easy learning of a Kannada Language: A few tips. Hints for correct and polite conservation, Listening and Speaking Activities
3. Key to Transcription.
4. ವೈಯಕ್ತಿಕ, ಸ್ವಾಮ್ಯಸೂಚಕ/ಸಂಬಂಧಿತ ಸಾರ್ವನಾಮಗಳು ಮತ್ತು ಪ್ರಶ್ನಾರ್ಥಕ ಪದಗಳು - **Personal Pronouns, Possessive Forms, Interrogative words**

ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

Module-2

1. ನಾಮಪದಗಳ ಸಂಬಂಧಾರ್ಥಕ ರೂಪಗಳು, ಸಂದೇಹಾಸ್ಪದ ಪ್ರಶ್ನೆಗಳು ಮತ್ತು ಸಂಬಂಧವಾಚಕ ನಾಮಪದಗಳು - **Possessive forms of nouns, dubitive question and Relative nouns**

2. ಗುಣ, ಪರಿಮಾಣ ಮತ್ತು ವರ್ಣಬಣ್ಣ ವಿಶೇಷಣಗಳು, ಸಂಖ್ಯಾವಾಚಕಗಳು **Qualitative, Quantitative and Colour Adjectives, Numerals**

3. ಕಾರಕ ರೂಪಗಳು ಮತ್ತು ವಿಭಕ್ತಿ ಪ್ರತ್ಯಯಗಳು – ಸಪ್ತಮಿ ವಿಭಕ್ತಿ ಪ್ರತ್ಯಯ – (ಆ, ಅದು, ಅವು, ಅಲ್ಲಿ) **Predictive Forms, Locative Case**

ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

Module-3

1. ಚತುರ್ಥಿ ವಿಭಕ್ತಿ ಪ್ರತ್ಯಯದ ಬಳಕೆ ಮತ್ತು ಸಂಖ್ಯಾವಾಚಕಗಳು – **Dative Cases, and Numerals**

4. ಸಂಖ್ಯಾಗುಣವಾಚಕಗಳು ಮತ್ತು ಬಹುವಚನ ನಾಮರೂಪಗಳು – **Ordinal numerals and Plural markers**

5. ನ್ಯೂನ / ನಿಷೇಧಾರ್ಥಕ ಕ್ರಿಯಾಪದಗಳು ಮತ್ತು ವರ್ಣ ಗುಣವಾಚಕಗಳು
Defective / Negative Verbs and Colour Adjectives

ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

Module-4

1. ಅಪ್ಪಣೆ / ಒಪ್ಪಿಗೆ, ನಿರ್ದೇಶನ, ಪ್ರೋತ್ಸಾಹ ಮತು ಒತ್ತಾಯ ಅರ್ಥರೂಪ ಪದಗಳು ಮತ್ತು ವಾಕ್ಯಗಳು
Permission, Commands, encouraging and Urging words (Imperative words and sentences)

2. ಸಾಮಾನ್ಯ ಸಂಭಾಷಣೆಗಳಲ್ಲಿ ದ್ವಿತೀಯ ವಿಭಕ್ತಿ ಪ್ರತ್ಯಯಗಳು ಮತ್ತು ಸಂಭವನೀಯ ಪ್ರಕಾರಗಳು
Accusative Cases and Potential Forms used in General Communication

3. "ಇರು ಮತ್ತು ಇರಲ್ಲ" ಸಹಾಯಕ ಕ್ರಿಯಾಪದಗಳು, ಸಂಭಾವ್ಯಸೂಚಕ ಮತ್ತು ನಿಷೇಧಾರ್ಥಕ ಕ್ರಿಯಾ ಪದಗಳು - **Helping Verbs "iru and iralla", Corresponding Future and Negation Verbs**

6. ಹೋಲಿಕೆ (ತರತಮ) , ಸಂಬಂಧ ಸೂಚಕ ಮತ್ತು ವಸ್ತು ಸೂಚಕ ಪ್ರತ್ಯಯಗಳು ಮತ್ತು ನಿಷೇಧಾರ್ಥಕ ಪದಗಳ ಬಳಕೆ- **Comparitive, Relationship, Identification and Negation Words**

ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

Module-5

1. ಕಾಲ ಮತ್ತು ಸಮಯದ ಹಾಗೂ ಕ್ರಿಯಾಪದಗಳ ವಿವಿಧ ಪ್ರಕಾರಗಳು - ifferent types of forms of Tense, Time and Verbs

2. ದ್, -ತ್, - ತು, - ಇತು, - ಆಗಿ, - ಅಲ್ಲ, - ಗ್, -ಕ್, ಇದೆ, ಕ್ರಿಯಾ ಪ್ರತ್ಯಯಗಳೊಂದಿಗೆ ಭೂತ, ಭವಿಷ್ಯತ್ ಮತ್ತು ವರ್ತಮಾನ ಕಾಲ ವಾಕ್ಯ ರಚನೆ - **Formation of Past, Future and Present Tense Sentences with Verb Forms**

3. **Kannada Vocabulary List** : ಸಂಭಾಷಣೆಯಲ್ಲಿ ದಿನೋಪಯೋಗಿ ಕನ್ನಡ ಪದಗಳು - **Kannada Words in Conversation**

ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

ಬಳಕೆ ಕನ್ನಡ ಪಠ್ಯದ ಕಲಿಕೆಯಿಂದ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ಆಗುವ ಅನುಕೂಲಗಳು ಮತ್ತು ಫಲಿತಾಂಶಗಳು: course Outcomes (Course

Skill Set): At the end of the Course, The Students will be able

1. To understand the necessity of learning of local language for comfortable life.
2. To Listen and understand the Kannada language properly.
3. To speak, read and write Kannada language as per requirement.
4. To communicate (converse) in Kannada language in their daily life with kannada speakers.
5. To speak in polite conservation.

Assessment Details (both CIE and SEE)

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum marks (20 marks). A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 35% (18 Marks out of 50)in the semester-end examination(SEE), and a minimum of 40% (40 marks out of 100) in the sum total of the CIE (Continuous Internal Evaluation) and SEE (Semester End Examination) taken together

Continuous Internal Evaluation:

Three Tests each of **20 Marks (duration 01 hour)**

  a. First test at the end of 5th week of the semester
  b. Second test at the end of the 10th week of the semester
  c. Third test at the end of the 15th week of the semester

Two assignments each of **10 Marks : 1.** First assignment at the end of 4th week of the semester

7. Second assignment at the end of 9th week of the semester

Group discussion/Seminar/quiz any one of three suitably planned to attain the COs and POs for **20 Marks (duration 01 hours)**

8. At the end of the 13th week of the semester

The sum of three tests, two assignments, and quiz/seminar/group discussion will be out of 100 marks and will be **scaled down to 50 marks**

CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

ಸೆಮಿಸ್ಟರ್ ಅಂತ್ಯದ ಪರೀಕ್ಷೆಯು ಈ ಕೆಳಗಿನಂತಿರುತ್ತದೆ - **Semester End Exam (SEE):**

SEE will be conducted by University as per the scheduled timetable, with common question papers for the subject.

2. The question paper will have 50 questions. Each question is set for 01 mark.
3. SEE Pattern will be in MCQ Model for 50 marks. Duration of the exam is 01 Hour.

Textbook :

ಬಳಕೆ ಕನ್ನಡ

ಲೇಖಕರು : ಡಾ. ಎಲ್. ತಿಮ್ಮೇಶ

ಪ್ರಸಾರಾಂಗ, ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ.

ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ

ವಿಷಯ ಸಂಕೇತ (Course Code)	21KSK37/47	ನಿರಂತರ ಆಂತರಿಕ ಮೌಲ್ಯಮಾಪನದ ಅಂಕಗಳು	50
ಒಂದು ವಾರಕ್ಕೆ ಬೋಧನಾ ಅವಧಿ (Teaching Hours / Week (L:T:P: S)	0:2:0:1	ಸೆಮಿಸ್ಟರ್ ಅಂತ್ಯದ ಪರೀಕ್ಷೆಯ ಅಂಕಗಳು	50
ಒಟ್ಟು ಬೋಧನಾ ಅವಧಿ Total Hours of Pedagogy	25 ಗಂಟೆಗಳು	ಒಟ್ಟು ಅಂಕಗಳು	100
ಕ್ರೆಡಿಟ್ಸ್ (Credits)	01	ಪರೀಕ್ಷೆಯ ಅವಧಿ	01 ಗಂಟೆ

ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ ಪಠ್ಯದ ಕಲಿಕೆಯ ಉದ್ದೇಶಗಳು:

1. ವೃತ್ತಿಪರ ಪದವಿ ವಿದ್ಯಾರ್ಥಿಗಳಾಗಿರುವುದರಿಂದ ಕನ್ನಡ ಭಾಷೆ, ಸಾಹಿತ್ಯ ಮತ್ತು ಕನ್ನಡದ ಸಂಸ್ಕೃತಿಯ ಪರಿಚಯ ಮಾಡಿಕೊಡುವುದು.

2. ಕನ್ನಡ ಸಾಹಿತ್ಯದ ಪ್ರಧಾನ ಭಾಗವಾದ ಆಧುನಿಕ ಪೂರ್ವ ಮತ್ತು ಆಧುನಿಕ ಕಾವ್ಯಗಳನ್ನು ಸಾಂಕೇತಿಕವಾಗಿ ಪರಿಚಯಿಸಿ ವಿದ್ಯಾರ್ಥಿಗಳಲ್ಲಿ ಸಾಹಿತ್ಯ ಮತ್ತು ಸಂಸ್ಕೃತಿಯ ಬಗ್ಗೆ ಅರಿವು ಹಾಗೂ ಆಸಕ್ತಿಯನ್ನು ಮೂಡಿಸುವುದು.

3. ತಾಂತ್ರಿಕ ವ್ಯಕ್ತಿಗಳ ಪರಿಚಯವನ್ನು ಹಾಗೂ ಅವರುಗಳ ಸಾಧಿಸಿದ ವಿಷಯಗಳನ್ನು ಪರಿಚಯಿಸುವುದು.

4. ಕನ್ನಡ ಶಬ್ದಸಂಪತ್ತಿನ ಪರಿಚಯ ಮತ್ತು ಕನ್ನಡ ಭಾಷೆಯ ಬಳಕೆ ಹಾಗೂ ಕನ್ನಡದಲ್ಲಿ ಪತ್ರ ವ್ಯವಹಾರವನ್ನು ತಿಳಿಸಿಕೊಡುವುದು.

ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವ್ಯವಸ್ಥೆ (Teaching-Learning Process - General Instructions) :

These are sample Strategies, which teacher can use to accelerate the attainment of the course outcomes.

1. ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡವನ್ನು ಬೋಧಿಸಲು ತರಗತಿಯಲ್ಲಿ ಶಿಕ್ಷಕರು ಪ್ರಸ್ತುತ ಪುಸ್ತಕ ಆಧಾರಿಸಿ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನವನ್ನು ಅನುಸರಿಸುವುದು. ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ತಯಾರಿಸಲು ವಿದ್ಯಾರ್ಥಿಗಳನ್ನು ಪ್ರೇರೇಪಿಸುವುದು ಮತ್ತು ತರಗತಿಯಲ್ಲಿ ಅವುಗಳನ್ನು ಚರ್ಚಿಸಲು ಅವಕಾಶ ಮಾಡಿಕೊಡುವುದು.

2. ಇತ್ತೀಚಿನ ತಂತ್ರಜ್ಞಾನದ ಅನುಕೂಲಗಳನ್ನು ಬಳಸಿಕೊಳ್ಳುವುದು - ಅಂದರೆ ಕವಿ-ಕಾವ್ಯ ಪರಿಚಯದಲ್ಲಿ ಕವಿಗಳ ಚಿತ್ರಣ ಮತ್ತು ಲೇಖನಗಳು ಮತ್ತು ಕಥ ಕಾವ್ಯಗಳ ಮೂಲ ಅಂಶಗಳಿಗೆ ಸಂಬಂಧಪಟ್ಟ ಧ್ವನಿ ಚಿತ್ರಗಳು, ಸಂಭಾಷಣೆಗಳು, ಈಗಾಗಲೇ ಇತರ ವಿಮರ್ಶಕರು ಬರೆದಿರುವ ವಿಮರ್ಶಾತ್ಮಕ ವಿಷಯಗಳನ್ನು ಟಿಪಿಟಿ, ಡಿಜಿಟಲ್ ಮಾಧ್ಯಮಗಳ ಮುಖಾಂತರ ವಿಶ್ಲೇಷಿಸುವುದು.

3. ನವೀನ ಮಾದರಿಯ ಸಾಹಿತ್ಯ ಬೋಧನೆಗೆ ಸಂಬಂಧಪಟ್ಟ ವಿಧಾನಗಳನ್ನು ಶಿಕ್ಷಕರು ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ಅನುಕೂಲವಾಗುವ ರೀತಿಯಲ್ಲಿ ಅಳವಡಿಸಿಕೊಳ್ಳಬಹುದು.

ಘಟಕ -1 ಲೇಖನಗಳು

1. ಕರ್ನಾಟಕ ಸಂಸ್ಕೃತಿ - ಹಂಪ ನಾಗರಾಜಯ್ಯ

2. ಕರ್ನಾಟಕದ ಏಕೀಕರಣ : ಒಂದು ಅಪೂರ್ವ ಚರಿತ್ರೆ - ಜಿ. ವೆಂಕಟಸುಬ್ಬಯ್ಯ

3. ಆಡಳಿತ ಭಾಷೆಯಾಗಿ ಕನ್ನಡ - ಡಾ. ಎಲ್. ತಿಮ್ಮೇಶ ಮತ್ತು ಪ್ರೊ. ವಿ. ಕೇಶವಮೂರ್ತಿ

ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

ಘಟಕ -2 ಆಧುನಿಕ ಪೂರ್ವದ ಕಾವ್ಯ ಭಾಗ	
1. ವಚನಗಳು : ಬಸವಣ್ಣ, ಅಕ್ಕಮಹಾದೇವಿ, ಅಲ್ಲಮಪ್ರಭು, ಆಯ್ದಕ್ಕಿ ಮಾರಯ್ಯ, ಜೇಡರದಾಸಿಮಯ್ಯ, ಆಯ್ದಕ್ಕಿ ಲಕ್ಕಮ್ಮ.	
2. ಕೀರ್ತನೆಗಳು : ಅದರಿಂದೇನು ಫಲ ಇದರಿಂದೇನು ಫಲ - ಪುರಂದರದಾಸರು	
ತಲ್ಲಣಿಸದಿರು ಕಂಡ್ಯ ತಾಳು ಮನವೇ - ಕನಕದಾಸರು	
3. ತತ್ತ್ವಪದಗಳು : ಸಾವಿರ ಕೊಡಗಳ ಸುಟ್ಟು - ಶಿಶುನಾಳ ಶರೀಫ	
ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

ಘಟಕ -3 ಆಧುನಿಕ ಕಾವ್ಯಭಾಗ	
1. ಡಿವಿಜಿ ರವರ ಮಂಕುತಿಮ್ಮನ ಕಗ್ಗದಿಂದ ಅಯ್ದು ಕೆಲವು ಭಾಗಗಳು	
2. ಕುರುಡು ಕಾಂಚಾಣ : ದಾ.ರಾ. ಬೇಂದ್ರೆ	
3. ಹೊಸಬಾಳಿನ ಗೀತೆ : ಕುವೆಂಪು	
ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

ಘಟಕ -4 ತಾಂತ್ರಿಕ ವ್ಯಕ್ತಿಗಳ ಪರಿಚಯ	
1. ಡಾ. ಸರ್. ಎಂ. ವಿಶ್ವೇಶ್ವರಯ್ಯ : ವ್ಯಕ್ತಿ ಮತ್ತು ಇತಿಹ್ಯ - ಎ ಎನ್ ಮೂರ್ತಿರಾವ್	
2. ಕರಕುಶಲ ಕಲೆಗಳು ಮತ್ತು ಪರಂಪರೆಯ ವಿಜ್ಞಾನ : ಕರಿಗೌಡ ಬೀಚನಹಳ್ಳಿ	
ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

ಘಟಕ -5 ಕಥೆ ಮತ್ತು ಪ್ರವಾಸ ಕಥನ	
1. ಯುಗಾದಿ : ವಸುಧೇಂದ್ರ	
2. ಮೆಗಾನೆ ಎಂಬ ಗಿರಿಜನ ಪರ್ವತ : ಹಿ.ಚಿ. ಬೋರಲಿಂಗಯ್ಯ	
ಬೋಧನೆ ಮತ್ತು ಕಲಿಕಾ ವಿಧಾನ	ಪುಸ್ತಕ ಆಧಾರಿತ ಬ್ಲಾಕ್ ಬೋರ್ಡ್ ವಿಧಾನ, ಪ್ರಮುಖ ಅಂಶಗಳ ಚಾರ್ಟ್ ಗಳನ್ನು ಬಳಸುವುದು, ಪಿಪಿಟಿ ಮತ್ತು ದೃಶ್ಯ ಮಾಧ್ಯಮದ ವಿಡಿಯೋಗಳನ್ನು ಬಳಸುವುದು, ವಿದ್ಯಾರ್ಥಿಗಳೊಂದಿಗೆ ಚಟುವಟಿಕೆಗಳ ಮುಖಾಂತರ ಚರ್ಚಿಸುವುದು.

ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ ಕಲಿಕೆಯಿಂದ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ಆಗುವ ಪರಿಣಾಮಗಳು (course Outcomes):

1. ಕನ್ನಡ ಭಾಷೆ, ಸಾಹಿತ್ಯ ಮತ್ತು ಕನ್ನಡದ ಸಂಸ್ಕೃತಿಯ ಪರಿಚಯವಾಗುತ್ತದೆ.

2. ಕನ್ನಡ ಸಾಹಿತ್ಯದ ಆಧುನಿಕ ಪೂರ್ವ ಮತ್ತು ಆಧುನಿಕ ಕಾವ್ಯಗಳು ಮತ್ತು ಸಂಸ್ಕೃತಿಯ ಬಗ್ಗೆ ಆಸಕ್ತಿಯು ಮೂಡುತ್ತದೆ.

3. ತಾಂತ್ರಿಕ ವ್ಯಕ್ತಿಗಳ ಪರಿಚಯವಾಗುತ್ತದೆ.

4. ಕನ್ನಡ ಭಾಷಾಭ್ಯಾಸ, ಸಾಮಾನ್ಯ ಕನ್ನಡ ಹಾಗೂ ಆಡಳಿತ ಕನ್ನಡದ ಪದಗಳ ಪರಿಚಯವಾಗುತ್ತದೆ.

ಮೌಲ್ಯಮಾಪನದ ವಿಧಾನ (Assessment Details- both CIE and SEE) :

(methods of CIE - MCQ, Quizzes, Open book test, Seminar or micro project)
The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The student has to obtain a minimum of 40% marks individually both in CIE and 35% marks in SEE to pass. Theory Semester End Exam (SEE) is conducted for 50 marks (01 hour duration). Based on this grading will be awarded.

Continuous Internal Evaluation:
Three Tests each of **20 Marks (duration 01 hour)**
        a. First test at the end of 5th week of the semester
        b. Second test at the end of the 10th week of the semester
        c. Third test at the end of the 15th week of the semester
Two assignments each of **10 Marks : 1.** First assignment at the end of 4th week of the semester
        2. Second assignment at the end of 9th week of the semester
Group discussion/Seminar/quiz any one of three suitably planned to attain the COs and POs for **20 Marks (duration 01 hours)**
        3. At the end of the 13th week of the semester
The sum of three tests, two assignments, and quiz/seminar/group discussion will be out of 100 marks and will be **scaled down to 50 marks**
CIE methods /question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

ಸೆಮಿಸ್ಟರ್ ಅಂತ್ಯದ ಪರೀಕ್ಷೆಯು ಈ ಕೆಳಗಿನಂತಿರುತ್ತದೆ - Semester End Exam (SEE):

SEE will be conducted by University as per the scheduled timetable, with common question papers for the subject.
1. The question paper will have 50 questions. Each question is set for 01 mark.

SEE Pattern will be in MCQ Model for 50 marks. Duration of the exam is 01 Hour.

ಪಠ್ಯಪುಸ್ತಕ :

ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ

ಡಾ. ಹಿ.ಚಿ.ಬೋರಲಿಂಗಯ್ಯ ಮತ್ತು ಡಾ. ಎಲ್. ತಿಮ್ಮೇಶ,
ಪ್ರಸಾರಾಂಗ, ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ.

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